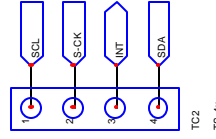


Main power in



serial connection in

| | | | |
|-------------|----------------------|-----------|-----------|
| ChipCircuit | | | |
| Title: | Port Expander layout | File: | Root.sch |
| Size: | A | Project: | Prototype |
| Date: | | Sheet: | 1 of 1 |
| | | Revision: | 0.0 |