

**ESP-TS1**

**TURBO SCSI CARD**





# FIELD CHANGE NOTICE

DATE 24/ 3 / 93

NUMBER 123

ORIGINATOR Chris Alfred

PRODUCT: CMI / MFX

ASSEMBLY No. ESP-TS1 DESCRIPTION Turbo SCSI card

This FCN applies to REV No: REV 1.2 A or B New REV No is: REV 1.3 A or B

### REASON FOR CHANGE:

To advance refresh latch time to allow for latching refresh at channel card

THIS FCN SHOULD BE DONE ONLY WITH THE AUTHORIZATION OF FAIRLIGHT

SEE FCN 124

### DETAILS OF CHANGE:

1. Cut component side track to U32 pin 3 (74LS74) before the track passes between pins 12 and 11 on U33 (74HC10)
2. Connect U32 pin 3 (74LS74) to U56 pin 13 (447-250-10)
3. Mark PCB as REV 1.3 A or B depending on the software you are running,

ORIGINATOR: <i>C. F. Alfred</i>	DATE: 24/3/93	TEST:	DATE:
SERVICE:	DATE:	PROD:	DATE:
		KIT LIST CHANGE: YES (NO)	



# FIELD CHANGE NOTICE

DATE 27/ 1 /93  
NUMBER 117

ORIGINATOR Shane Morris

PRODUCT: CMI / MFX

ASSEMBLY No. TS-1

DESCRIPTION TURBO SCSI

This FCN applies to REV No: REV 1.2

The New REV No is: REV 1.2 B

## REASON FOR CHANGE:

This FCN is to be used only with the installation of REV 11 B. ( full option version of REV 11). The replacement of U25 (TSCLAT2 8D6F) with GAL TSCFG-B will enable full options available in REV 11.

This FCN is not required if REV 11A is to be installed. U25 (TSCLAT2 8D6F) remains.

See FCN Numbers 99, 102, 108, 109A, 112, 113, 118

## DETAILS OF CHANGE:

1. Power down system.
2. Remove Turbo SCSI card (slot 16 in Digital rack) Check / carry out all relevant FCN's to Turbo SCSI card.
3. Locate U25 and remove GAL TSCLAT2
4. Install GAL TSCFG-B
5. Label card REV 1.2B
6. Replace card.

ORIGINATOR:	DATE:	PROD: <i>Shane Morris</i>	DATE: 4/2/92
SERVICE:	DATE:	KIT LIST CHANGE:	YES NO



# Starlight

Page 1 of 1

## FIELD CHANGE NOTICE

DATE 21 / 8 / 92  
NUMBER 99

ORIGINATOR Chris Alfred

PRODUCT: CMI / MFX

ASSEMBLY No. ESP-TS1 DESCRIPTION TURBO SCSI CARD

This FCN applies to REV No: 1.0

The New REV No is: 1.1

### REASON FOR CHANGE:

Improves SCLK generation to stop read errors when the waveform supervisor accesses waveform memory when system is hot.

### DETAILS OF CHANGE:

1. Remove 74HCT244 at U46
2. Replace with 74ACT244
3. Mark PCB with new revision number

ORIGINATOR: *C. Alfred*

DATE: 21-8-1992

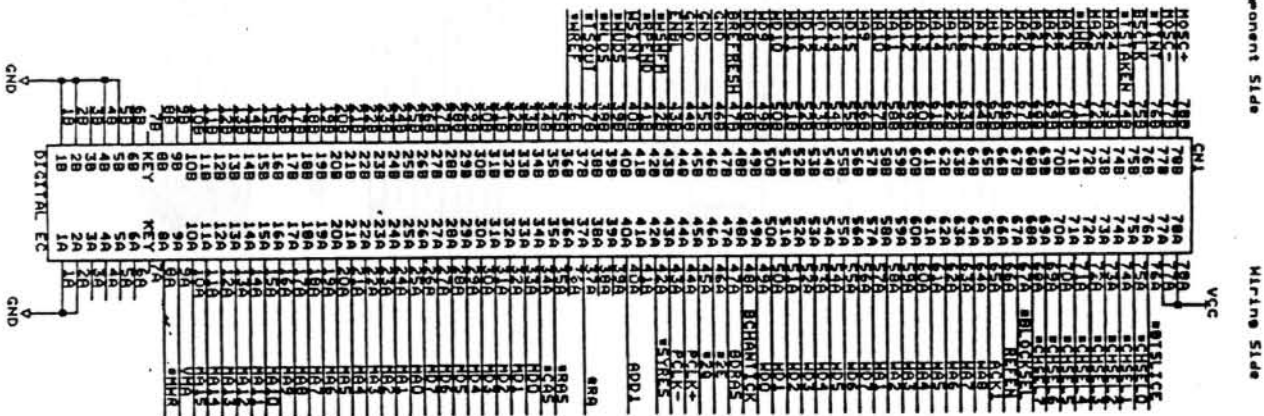
SERVICE MANAGER: *Alvin*

DATE: 21-8-1992

Notes on ESP-TS1 Card

- (1) Vcc to be decoupled by 47uF electrolytics at approx 12 locations at edges of board.
- (2) Vcc to be decoupled by 0.1uF - one per 4 ICs
- (3) Vcc to be decoupled by 0.1uF directly to:
  - U14 - 74E71
  - U15 - 74E12
  - U16 - 74E12
  - U42 - 44256
  - U43 - 44256
  - U44 - 44256
  - U45 - 44256
- (4) Ground plane to be placed under U52 (S3C94)
- (5) Edge connector (CN1) B side connections on COMPONENT SIDE

Rev 1.0  
 Rev 2.0 25/8/1992





PCB REV A Rev 1

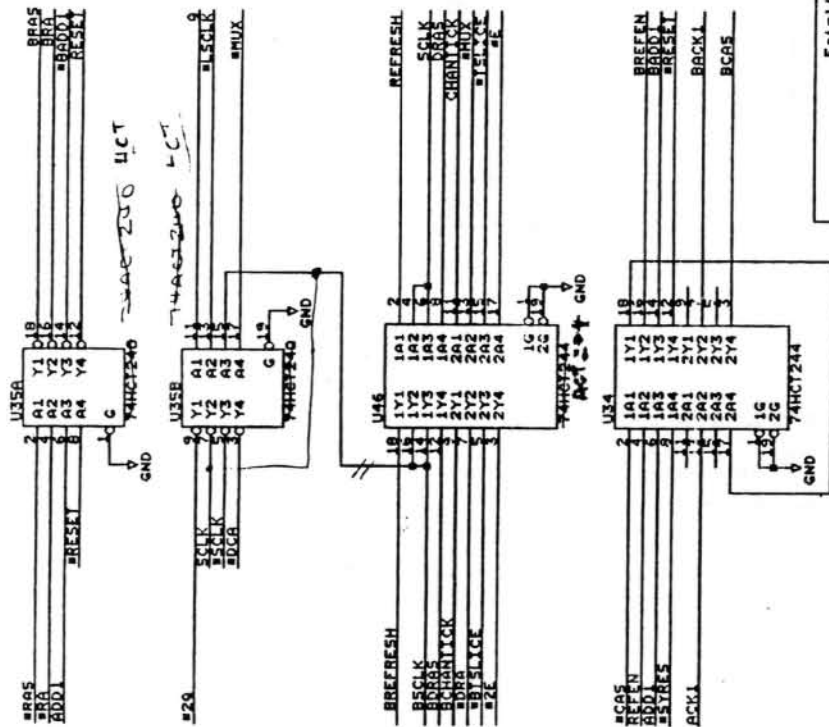
1. no trace connecting U33A to U33B  
 2. no trace connecting U33A to U33C

Rev 2

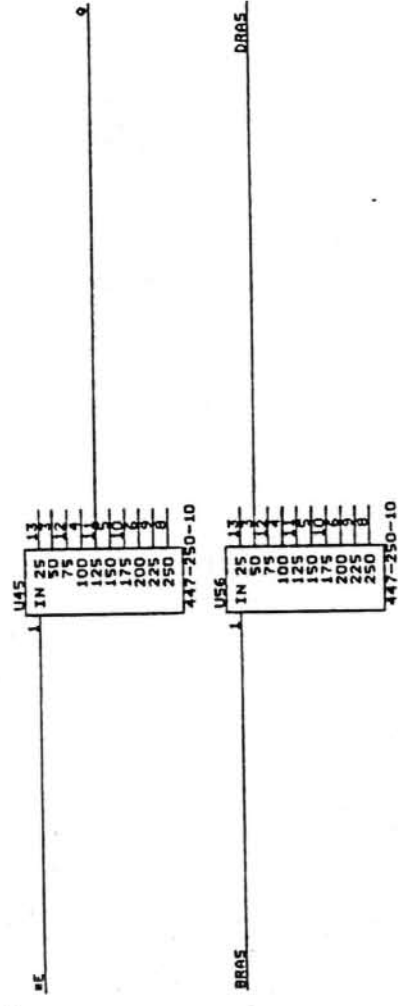
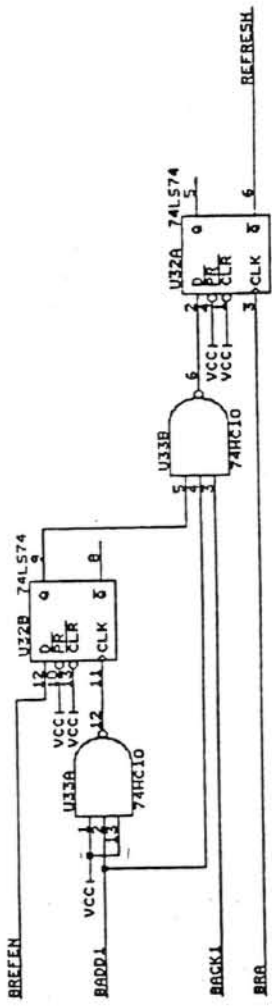
U46 to 74ACT244

pin 16 U33

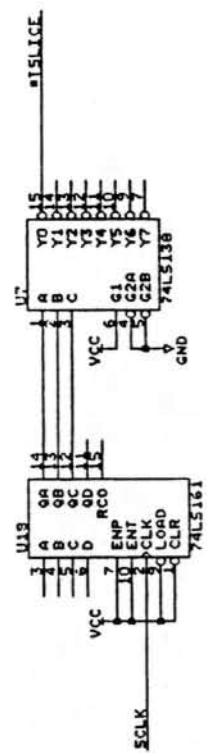
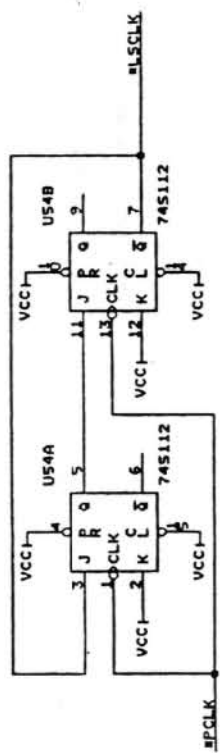
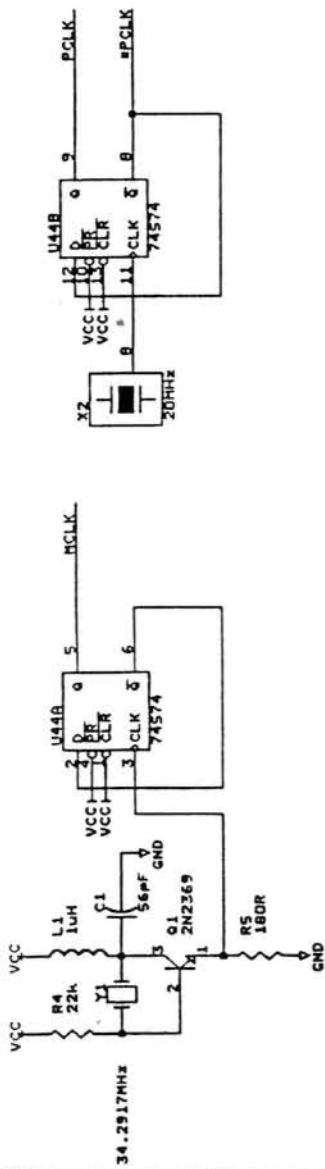
to pin 3 U32



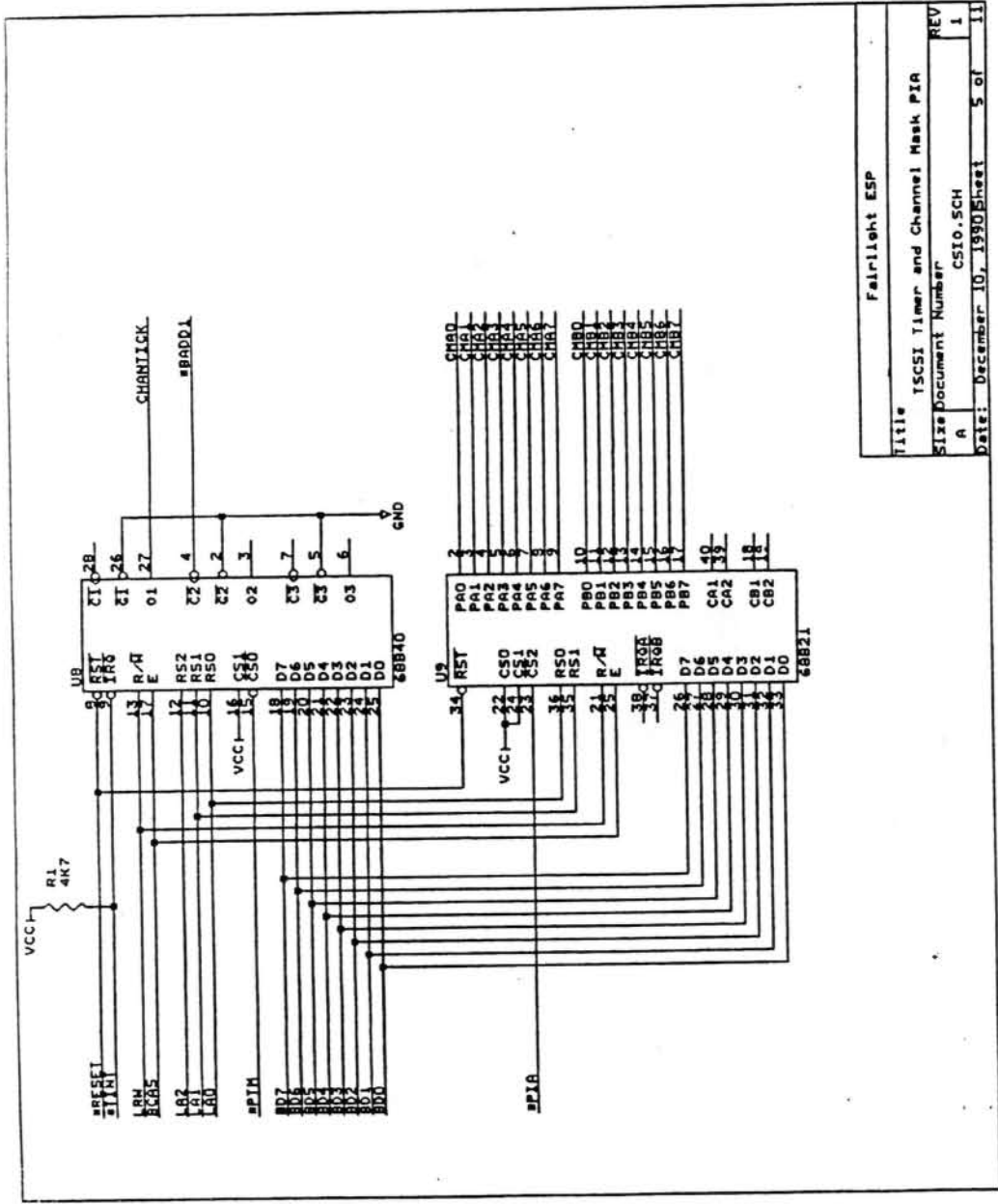
Title	Fairlight ESP
Size	YCSI Buffers
Document Number	TSBUFF.SCH
REV	1
Date:	May 21, 1991
Sheet	2 of 11



Fairlight ESP	
Title	_ TCSI Channel Card Timing
Size	Document Number
REV	CSCLK.SCH
1	1
Date:	May 2, 1991 Sheet 3 of 11



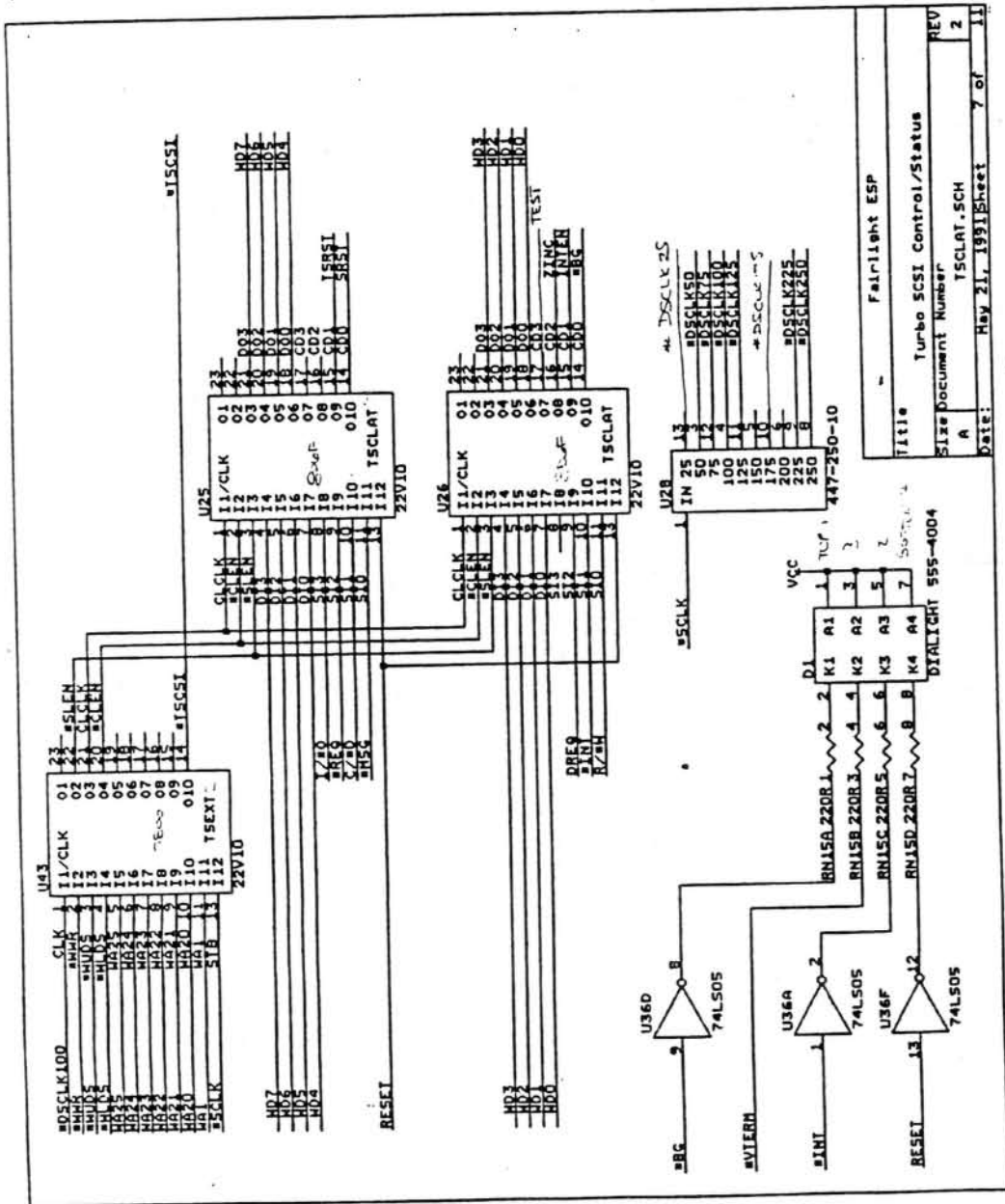
Fairlight ESP	
Title	TCSI MBUSS time slice signals
Size	Document Number
REV	CSSLICE.SCH
1	1
Date:	December 10, 1990 Sheet 4 of 11



Fairlight ESP	
Title	TSCSI Timer and Channel Mask PIA
Size	Document Number
REV	CS10.SCH
A	1
Date:	December 10, 1990
Sheet	5 of 11



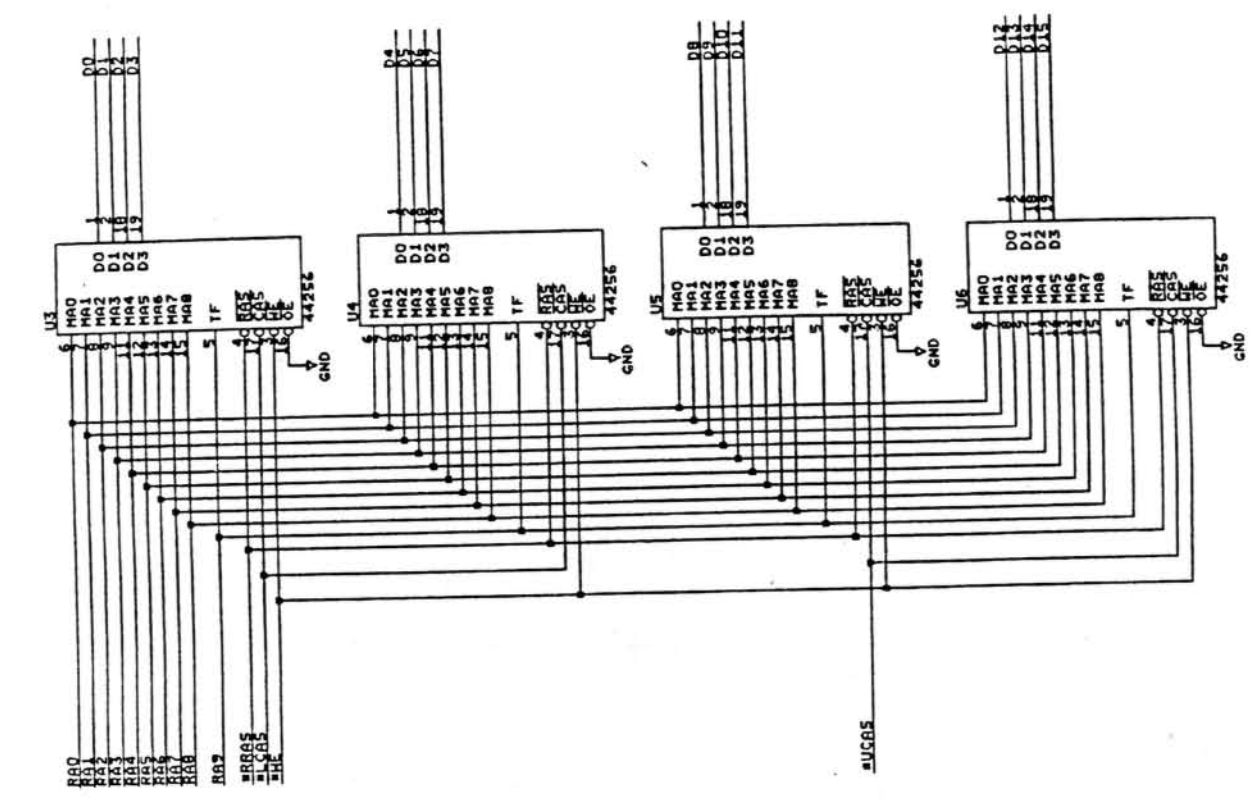
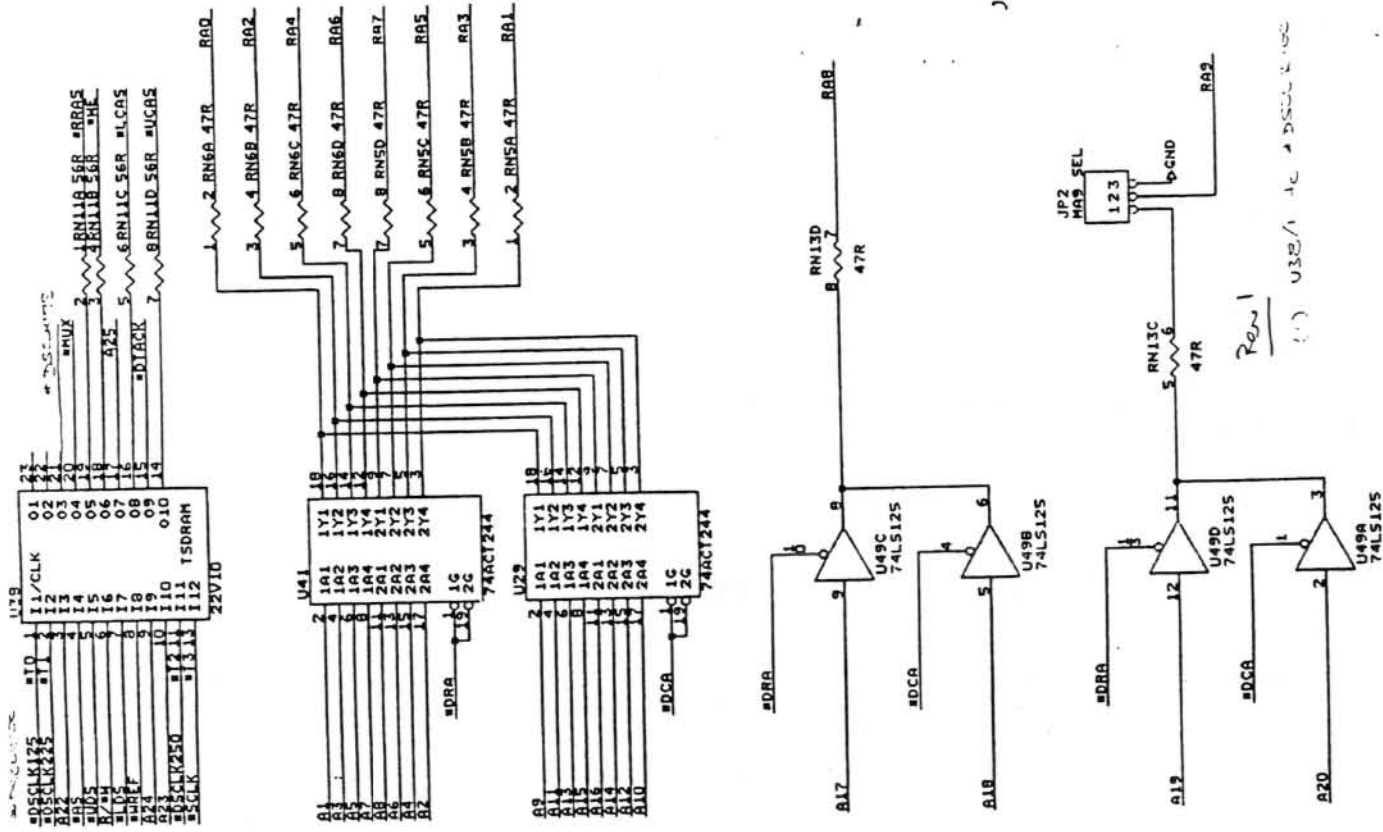
16/10/91 - TEST mode entered on bit CD3



REV	1
Size	7 of 11
Document Number	TSCLAT.SCH
Date:	May 21, 1991
Sheet	7 of 11

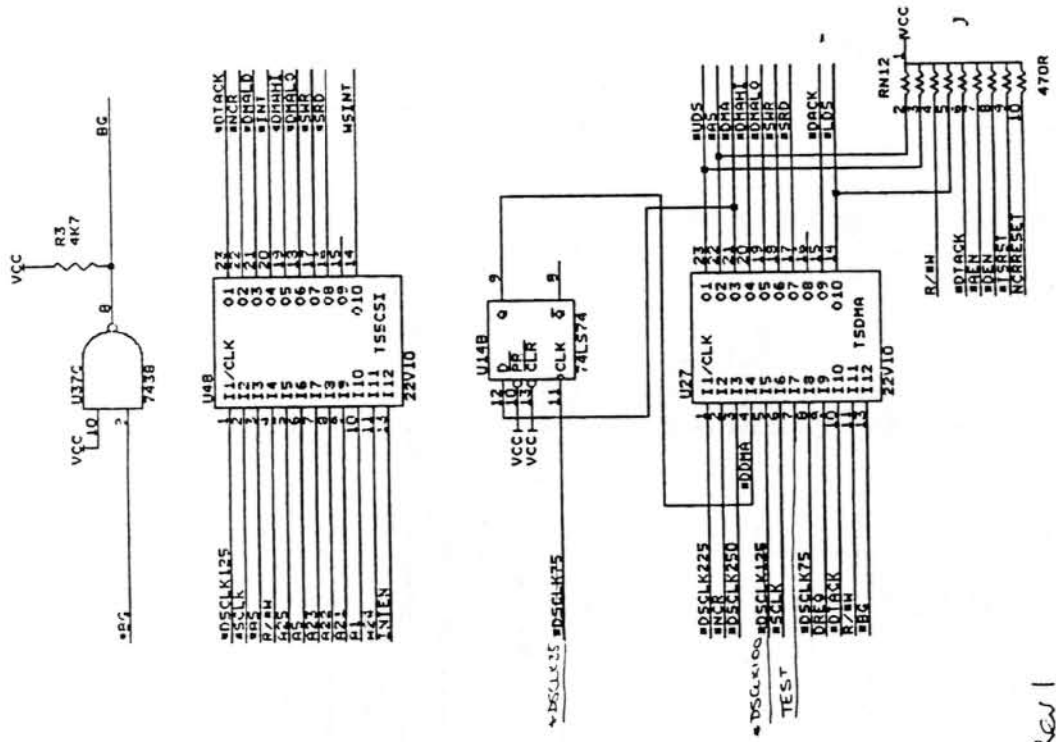
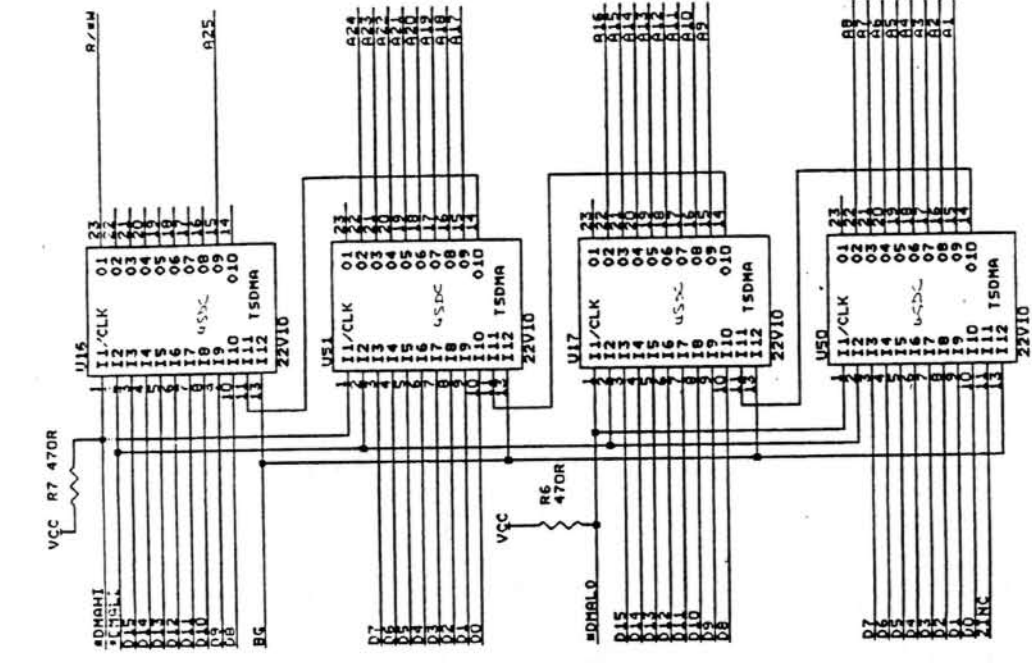
Title Turbo SCSI Control/Status  
 Fairlight ESP  
 Date: May 21, 1991 Sheet 7 of 11



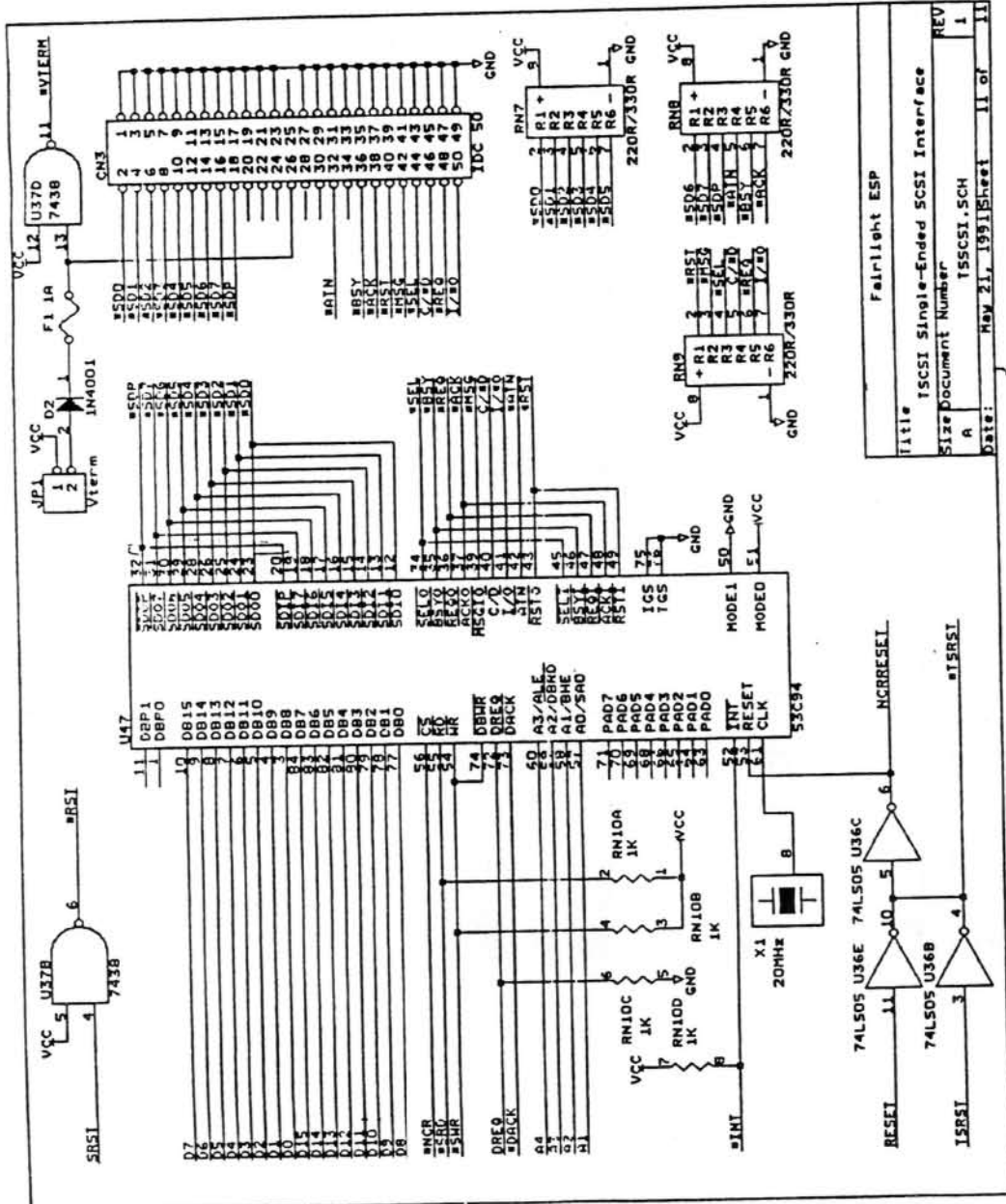


DRAMS 120ns or faster

Rev 1  
 (1) USB/A IC DISOLVING



Rev1  
 10/10/91 - test input circuit to TSDMA.  
 20/1/92 - change DSCLK225 to DMHA on U17  
 Rev2  
 11/11/92 - change DSCLK225 to DRACK100 on U27



Title	Fairlight ESP
Size	155CSI.SCH
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REV	1
Date:	May 21, 1991