

disklavier

SERVICE MANUAL

MX-100A

IMPORTANT NOTICE

This manual has been provided for the use of authorized Yamaha Retailers and their service personnel. It has been assumed that basic service procedures inherent to the industry, and more specifically Yamaha Products, are already known and understood by the users, and have therefore not been restated.

WARNING: Failure to follow appropriate service and safety procedures when servicing this product may result in personal injury, destruction of expensive components and failure of the product to perform as specified. For these reasons, we advise all Yamaha product owners that all service required should be performed by an authorized Yamaha Retailer or the appointed service representative.

IMPORTANT: The presentation or sale of this manual to any individual or firm does not constitute authorization, certification, recognition of any applicable technical capabilities, or establish a principle-agent relationship of any form.

The data provided is believed to be accurate and applicable to the unit(s) indicated on the cover. The research, engineering, and service departments of Yamaha are continually striving to improve Yamaha products. Modifications are, therefore, inevitable and changes in specification are subject to change without notice or obligation to retrofit. Should any discrepancy appear to exist, please contact the distributor's Service Division.

WARNING: Static discharges can destroy expensive components. Discharge any static electricity your body may have accumulated by grounding yourself to the ground buss in the unit (heavy gauge black wires connect to this buss).

IMPORTANT: Turn the unit OFF during disassembly and parts replacement. Recheck all work before you apply power to the unit.

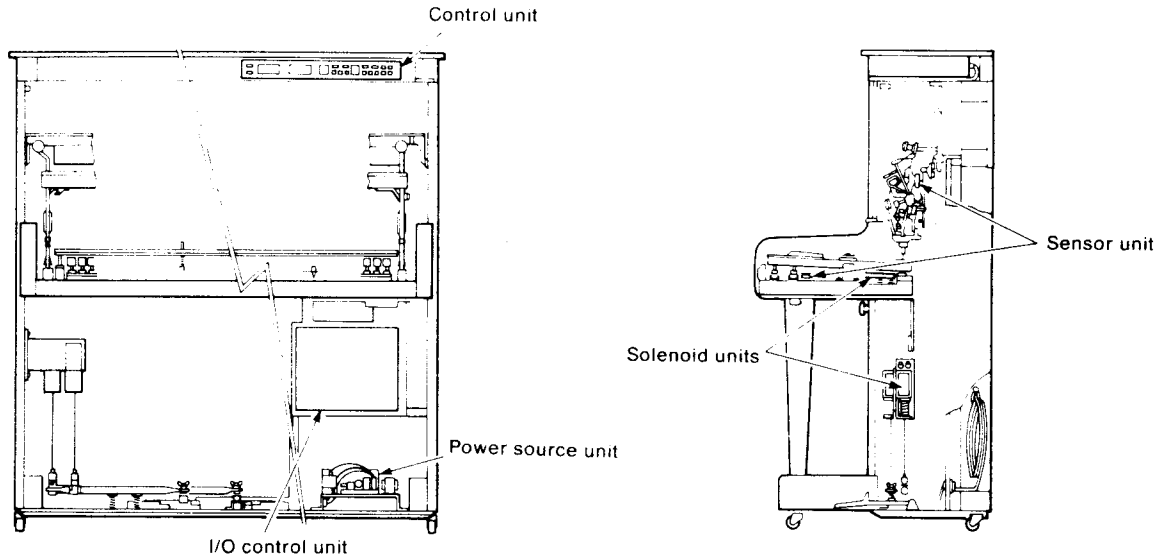
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■ The Disklavier Design

The Disklavier consists of a control unit and a drive unit comprising of a sensor unit, solenoid units, power source unit and I/O control unit.



■ Specifications

Design

| | | |
|---------------|--|---|
| Control unit | <ul style="list-style-type: none"> • Key switches • LCD • LED indicators • LED display • Disk drive • Jacks • Media | <p>18 Dot matrix (16 characters x 2 lines) Power, Record, Volume, Tempo, Transposition, Metronome, MIDI. 7 segments x 2 digits 3.5 inch micro-floppy disk drive MIDI IN/OUT, foot switch phone jack Double sided, double density, double track 3.5 inch microfloppy disks Memory capacity: 628 kbytes Number of pieces: 60 Music titles: 32 characters Disk titles: 64 characters</p> |
| Solenoid unit | <ul style="list-style-type: none"> • Polyphonic voices | 16 |
| Sensor unit | <ul style="list-style-type: none"> • Polyphonic voices | 16 |
| Power supply | <ul style="list-style-type: none"> • U.S. & Canadian Models: • General Model: • Power consumption • Environmental temperature: | <p>120V - 60 Hz 220V - 50Hz 250 W 4°C ~ 40°C</p> |

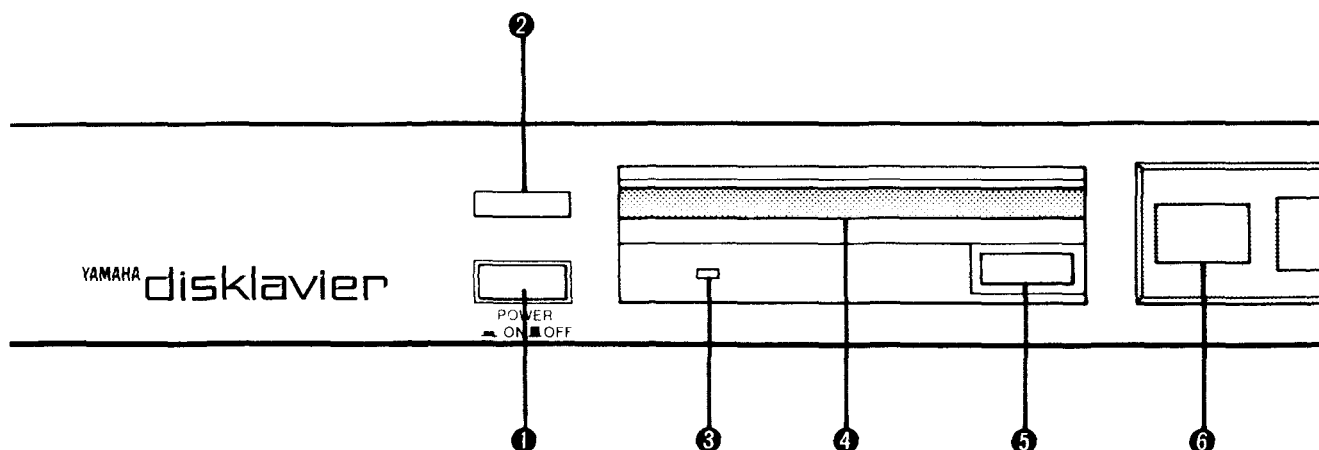
Standard equipment

Wireless remote control unit, prerecorded floppy disk

* Specifications and exterior design are subject to change without notice.

■ Names and Functions

● Front Panel



① Power Switch

Pressing this switch turns power on, shown by the power indicator ③ and the Volume button indicator ⑬ lighting up. At the same time, the LCD will display the message "DISK NOT READY!".

② Remote control signal window

The infrared signals from the remote control unit are received at this window. Be sure to leave it uncovered so the signals from the remote control unit can be received properly.

③ Power indicator

Lights up when power is turned on.

④ Disk slot

Floppy disks are inserted here.

⑤ Disk Eject button

Press this button when you wish to remove an inserted disk.

* Please do not remove the disk during playback or recording as this will damage the disk drive head and the disk itself.

⑥ Music number indication

Displays the number of the selected piece of music. Confirm this display before you play back or record.

⑦ Liquid Crystal Display (LCD)

The LCD displays a variety of information such as the music title, adjustment values, the time that has elapsed since the beginning of the piece of music, playback and recording levels, and so on.

⑧ Start Pause button

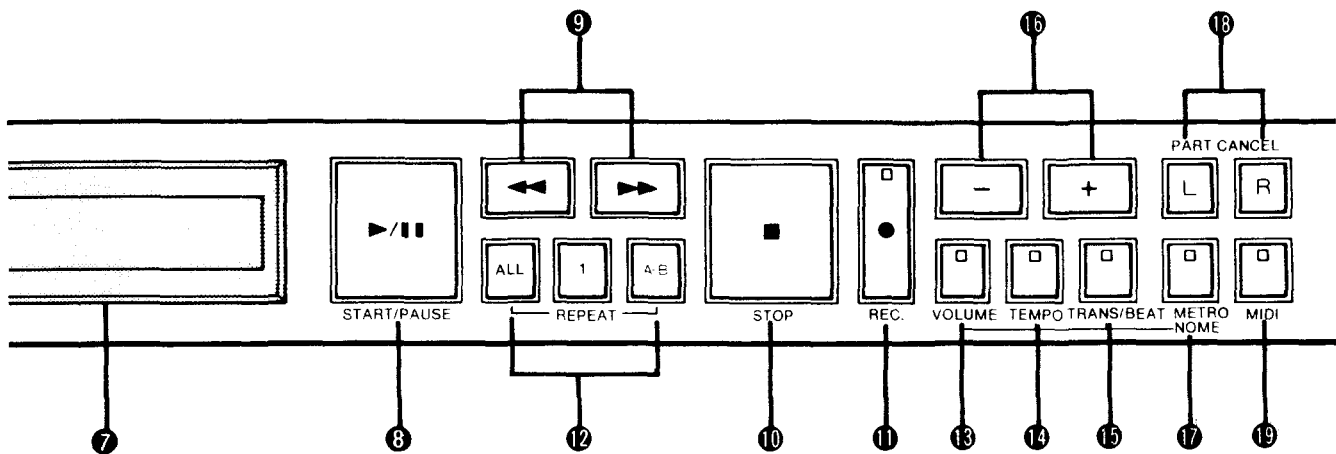
Press this button when you want to start playback or recording. Also use it when you wish to temporarily stop (pause) or restart a piece of music during playback or recording. (If this button is pressed during fast forward or rewind, playback will recommence from there.)

⑨ Tune Select Return-FF buttons

In stop or record stand-by mode, the function of these buttons is to select the music number, while they function as Fast Forward or Return buttons during playback. To stop fast forward or rewind operation, press the respective button again. This will restart playback from the point where the button was pressed. If the button is not pressed again, fast forward or Return will stop at the beginning of the next piece of music or the present one respectively.

⑩ Stop button

This button stops all operations such as playback and recording, fast forward or rewind.



⑪ Rec. button

Pressing this button causes its indicator to light up, entering record stand-by mode. Recording can now be started by pressing the Start/Pause button.

If you would like to delete part of a recorded piece of music and rerecord something else in its place, first enter record stand-by mode by pressing this button. Then display the number of the respective piece of music with the Music Selection/Return-FF buttons ⑨ and use the Start/Pause button ⑧ in order to commence rerecording.

★ The number of pieces of music that can be recorded on a single floppy disk is 60 for a total recording time of about 90 minutes. However, this is only an approximation and the actual number of pieces and length of recording time depends on the kind of music recorded.

★ If this button is pressed once again during record stand-by mode, the mode will change to "music/disk title entry mode", which is used to input the names of single pieces of music or entire disks.

★ If this button is pressed again twice during record stand-by mode, the mode will change to "split point setting for separate left-and right-hand parts".

⑫ Repeat buttons

Operate these buttons when you desire repeated playback of certain pieces of music or sections within them.

⑬ Volume button

In order to adjust the playback volume, press this button in stop mode, during playback or playback pause, making sure that the button's indicator lights up. You can then adjust the volume with the + / - buttons ⑮. The adjustment range is from -03 to +02. It is usually set to 00.

In addition, this button can be used to turn the metronome sound on or off when the Metronome button ⑰ has been pressed to select the metronome function.

⑭ Tempo button

When you want to adjust the tempo (speed) of a piece of music for playback, press this button in stop mode, during playback or playback pause, making sure that the button's indicator lights up. You can then adjust the tempo with the + / - buttons ⑮. The adjustment range is from -50% to +20%. It is usually set to 00.

Besides, this button can be used to enable metronome tempo adjustment when the metronome function is on.

⑮ **Trans/Beat**

When you want to transpose (change) the key of a piece of music for playback, press this button in stop mode, during playback or playback pause, making sure that the button's indicator lights up. You can then transpose the music with the + / - buttons ⑯. The transposition range is from two octaves down to two octaves up. It is usually set to "normal".

In addition, this button can be used to set the metronome time when the metronome function is on.

⑯ **-/+ buttons**

These are used for various adjustments and settings such as volume, tempo or transposition. Pressing the " - " button decreases the respective value, pressing "+" increases it. With both buttons, keeping them depressed changes the value continuously while pressing them shortly changes the value in single steps.

⑰ **Metronome button**

The Disklavier has a metronome function which allows you to record music while listening to a metronome beat. It is also possible to use the metronome function when not recording. (However, the metronome cannot be used during playback.)

When you wish to use the metronome function, press this button.

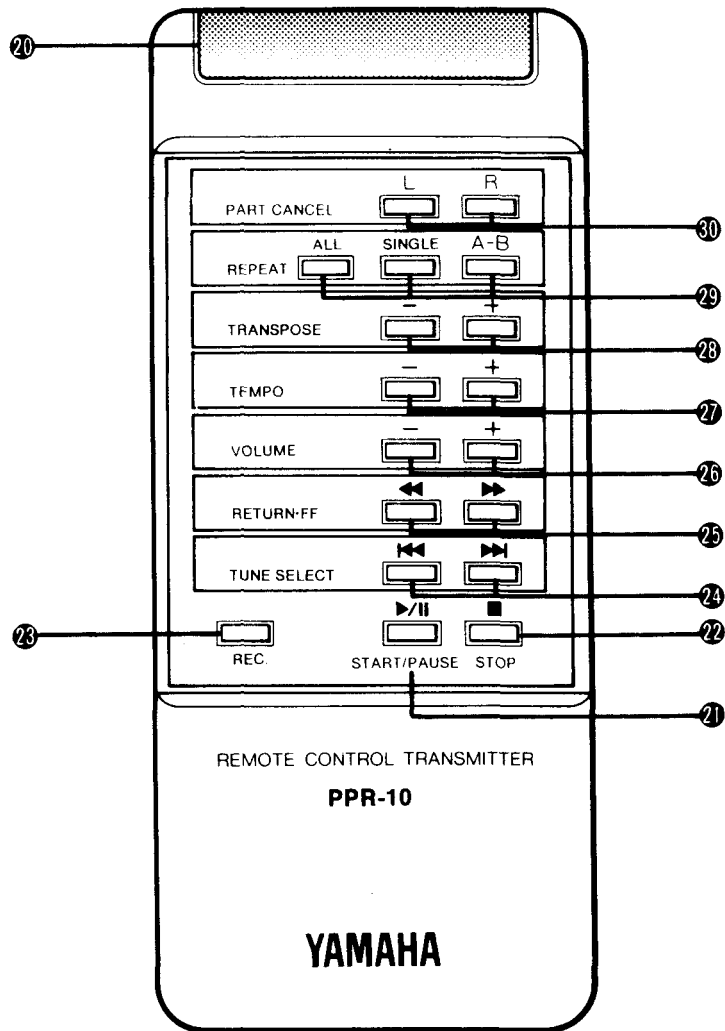
⑱ **Part Cancel buttons**

It is possible to cancel either the left-hand or right-hand part by pressing the corresponding Part Cancel button when playing back music recorded separately for the left and right hand. In other words, pressing the button "L" causes only the sound of the keys played by the right hand to be heard, while pressing "R" means that you will hear only the part for the left hand.

⑲ **MIDI button**

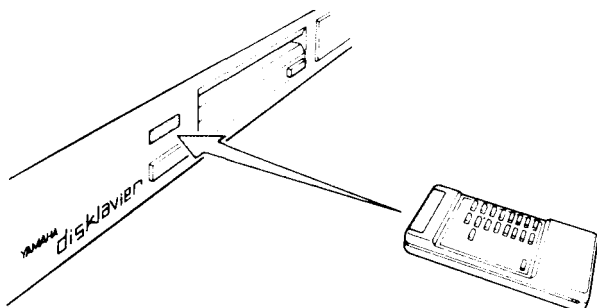
This button is only needed when MIDI signals are being exchanged with other connected MIDI equipment. Pressing this button enters MIDI set-up mode in which the conditions for MIDI communication are established.

● Remote Control Unit



⑳ Remote control signal window

Please operate the remote control while making sure that this infrared signal transmission window is aimed at the corresponding reception window ② on the front panel. The control signals from the remote control unit are transmitted from here.



㉑ Start/Pause button

Has the same functions as the Start/Pause button ⑧ on the front panel.

㉒ Stop button

Has the same functions as the Stop button ⑩ on the front panel.

㉓ Rec. button

Has the same functions as the Rec. button ⑪ on the front panel.

㉔ Tune Select buttons

The function of these buttons is to select the music number in stop mode, during playback or in record stand-by mode.

②⑤ Return-FF buttons

These buttons are for fast forward and rewind. They can be operated in stop mode or during playback. Fast forward or rewind continues until the respective button is pressed again.

②⑥ Volume buttons

These buttons adjust playback volume level. They can be operated in stop mode, during playback or playback pause.

②⑦ Tempo buttons

When you want to adjust the tempo of a piece of music for playback, use these buttons in stop mode, during playback or playback pause.

②⑧ Transpose buttons

When you want to transpose (change the key of) a piece of music for playback, press these buttons in stop mode, during playback or playback pause.

②⑨ Repeat buttons

Operate these buttons when you desire repeated playback.

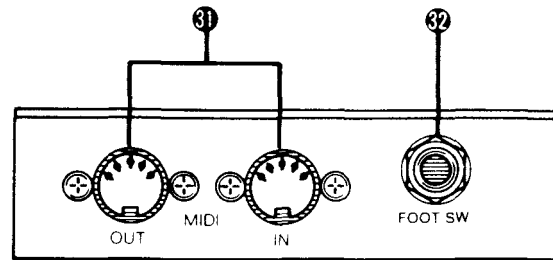
③⑩ Part Cancel buttons

These allow you to cancel either the left-hand or right-hand part for playback of music with separate parts for the left and right hand.

★Operations that cannot be carried out with the remote control unit:

- Operations with the metronome function on
- Operations in MIDI set-up mode
- Disk formatting, registration

● Connectors



③① MIDI jacks

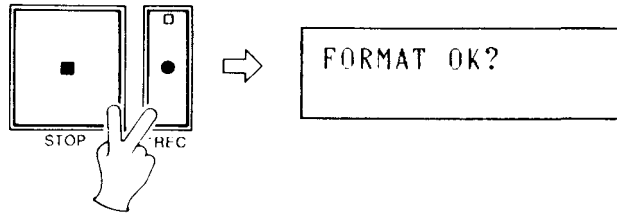
These jacks are used for MIDI connections. When you want to control other MIDI equipment from the Disklavier or control this unit via other MIDI equipment, these jacks enable MIDI signal communication.

③② Foot switch jack

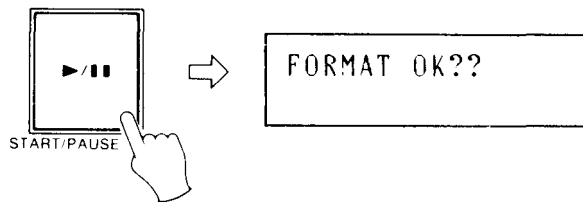
If an optional FC-5 foot switch has been connected to this jack, you can start, stop and restart playback or recording with your foot. (The foot switch then has the same functions as the Start/Pause button ⑧ on the front panel.)

■ How to Format Disks

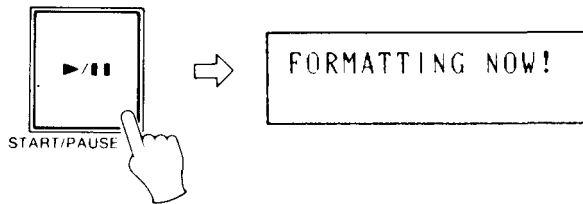
- ① After inserting a disk, press the Rec. button while keeping the Stop button depressed. You can release these buttons when the display reads "FORMAT OK?".



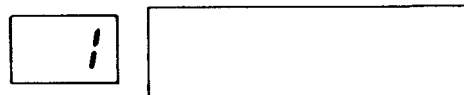
- ② In answer to the above message, press the Start/Pause button. (If you should decide not to proceed with formatting, remove the disk instead.)



- ③ Reconfirm the above question by again pressing the Start/Pause button. (If you decide not to proceed with formatting, remove the disk instead.)



- ④ After a few minutes, formatting is completed and the display will change as follows.

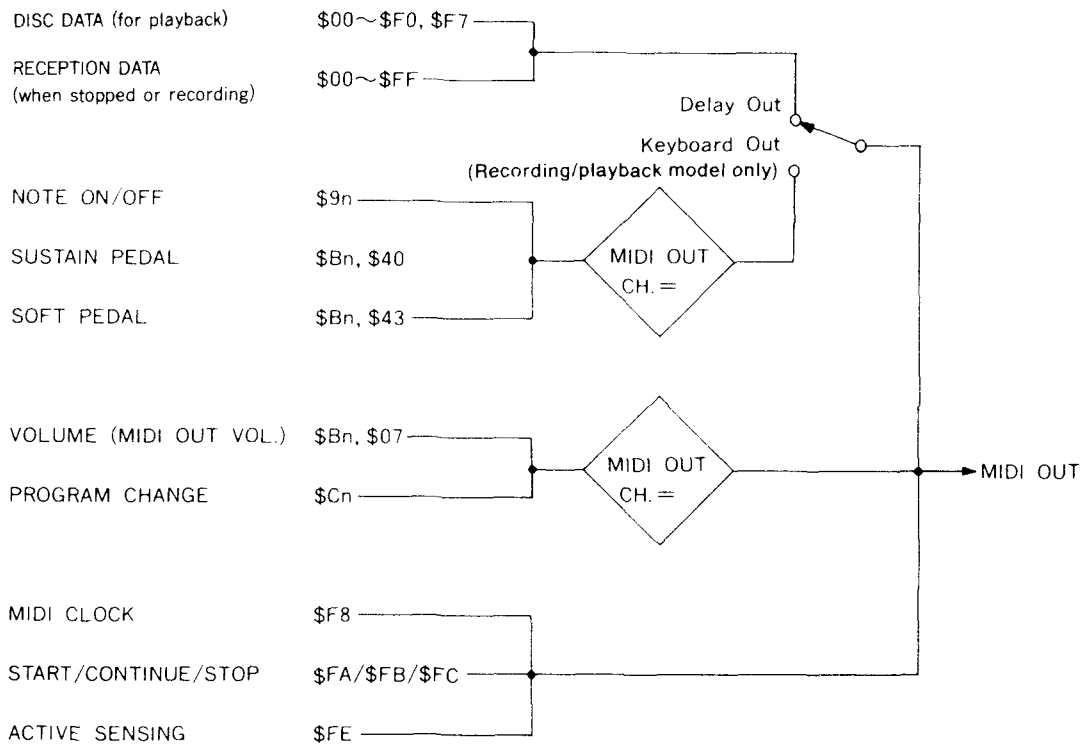


● Erasing recordings

Should you wish to delete all recordings on a disk at once, you can format that disk as described.

MIDI Data Format

Transmission Conditions



Transmission Data

• CHANNEL INFORMATION

1) CHANNEL VOICE MESSAGES

① Key On/Off

| | | |
|----------|----------------------|--|
| Status | 1 0 0 1 n n n n (9n) | n = channel number (0~15) |
| Note No. | 0 k k k k k k k | k = 21 (A ₋₁) ~108 (C ₇): Note 1 |
| Velocity | 0 v v v v v v v | v = 0: key off v = 1 ~127: key on |

Note 1: Transposition is also limited to this range.

② Control Change

| | | |
|---------------|----------------------|---------------------------|
| Status | 1 0 1 1 n n n n (Bn) | n = channel number (0~15) |
| Control No. | 0 c c c c c c c | |
| Control value | 0 v v v v v v v | |

| | |
|------------------------------|--------------------|
| Control No. | Control value |
| c = 7 volume (MIDI OUT VOL.) | v = 0~127 |
| c = 64 Sustain pedal | v = 0: off, 31: on |
| c = 67 Soft pedal | v = 0: off, 31: on |

③ Program Change

| | | |
|-------------|----------------------|---------------------------|
| Status | 1 1 0 0 n n n n (Cn) | n = channel number (0~15) |
| Program No. | 0 p p p p p p p | p = 0~127 |

- **SYSTEM INFORMATION**

- 1) **SYSTEM REALTIME MESSAGES**

- ① **Timing clock**

- Status 1 1 1 1 1 0 0 0 (F8): Note 2

- Note 2: The clock tempo can be changed within a variable range of ♩ = 40~230.

- ② **Start**

- Status 1 1 1 1 1 0 1 0 (FA)



- ③ **Continue**

- Status 1 1 1 1 1 0 1 1 (FB)

- ④ **Stop**

- Status 1 1 1 1 1 1 0 0 (FC)

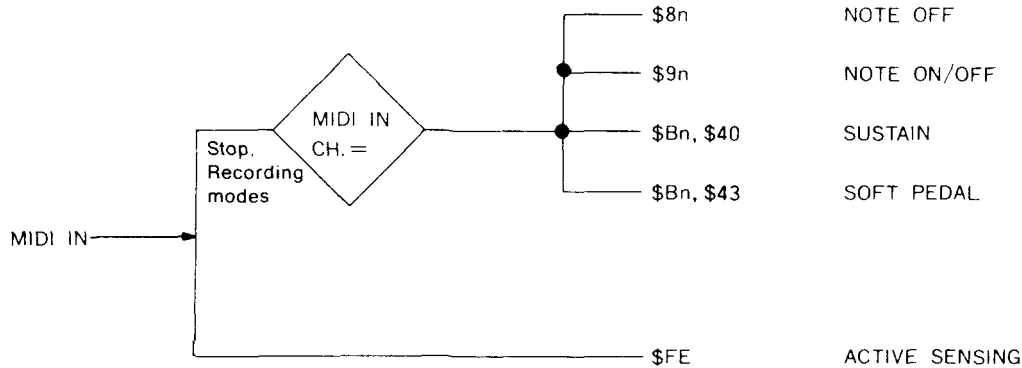
- ⑤ **Active sensing**

- Status 1 1 1 1 1 1 1 0 (FE): Note 3

- Note 3: When the preferred data, including active sensing, has been transmitted, if data is not transmitted within an interval of approximately 80 milli seconds, the main clock is transmitted.

- * The system realtime messages Timing Clock, Start, Continue, and Stop can be transmitted only when the metronome function is ON.
 - * When playing back a disk recorded on another system (E-seq), all system information as well as system exclusive messages can be transmitted.
 - * In MIDI OUT = DELAY OUT conditions, and also in Stop or Recording mode, delay the data received through MIDI IN by 500msec, and merge it with MIDI OUT without any changes.

Reception Conditions



Reception Data

• CHANNEL INFORMATION

1) CHANNEL VOICE MESSAGES

① Key Off

| | | |
|----------|----------------------|--|
| Status | 1 0 0 0 n n n n (8n) | n = channel number (0~15) |
| Note No. | 0 k k k k k k k | k = 21 (A ₁)~108 (C ₇) |
| Velocity | 0 v v v v v v v | v = ignored |

② Key On/Off

| | | |
|----------|----------------------|--|
| Status | 1 0 0 1 n n n n (9n) | n = channel number |
| Note No. | 0 k k k k k k k | k = 21 (A ₁)~108 (C ₇) |
| Velocity | 0 v v v v v v v | v = 0: key off v = 1~127: key on |

③ Control Change

| | | |
|---------------|----------------------|---------------------------|
| Status | 1 0 1 1 n n n n (Bn) | n = channel number (0~15) |
| Control No. | 0 c c c c c c c | |
| Control Value | 0 v v v v v v v | |
| Control No. | c = 64 Sustain pedal | Control Value |
| | c = 67 Soft pedal | v = 0: off, 31: on |
| | | v = 0: off, 31: on |

• SYSTEM INFORMATION

1) SYSTEM REALTIME MESSAGES

① Active Sensing

| | |
|--------|------------------------------|
| Status | 1 1 1 1 1 1 1 0 (FE): Note 4 |
|--------|------------------------------|

Note 4: When this code is received, sensing begins. If there is neither status nor data for an interval of 300msec, the piano keyboard and pedals become OFF, as do the Note ON of MIDI OUT and the Sustain Pedal ON.

■ MIDI Implementation Chart

(DISKLAVIER) Date : 2/4, 1986
 Model MX100A MIDI Implementation Chart Version : 1.0

| Function ... | Transmitted | Recognized | Remarks |
|---|-------------------------|----------------------------|------------------|
| :Basic Default | : 1 | : 1 | : |
| :Channel Changed | : 1 - 16 | : 1 - 16 | : |
| : Mode Default | : 3 | : 3 | : |
| : Mode Messages | : x | : x | : |
| : Mode Altered | : ***** | : x | : |
| :Note Number : True voice | : 21 - 108 : ***** | *1: 21 - 108 : 21 - 108 | : |
| :Velocity Note ON | : o 9nH,v=1-127 | : o v=1-127 | : |
| : Velocity Note OFF | : x 9nH,v=0 | *1: x | : |
| :After Key's | : x | : x | : |
| :Touch Ch's | : x | *1: x | : |
| :Pitch Bender | : x | *1: x | : |
| : Control 7 | : o | : x | : |
| : Control 64 | : o | : o | : |
| : Control 67 | : o | : o | : |
| :Change | : | *1: | : |
| :Prog Change : True # | : o 0 - 127 : ***** | *1: x | : |
| :System Exclusive | : x | *1: x | : |
| :System : Song Pos | : x | : x | : |
| : : Song Sel | : x | : x | : |
| :Common : Tune | : x | : x | : |
| :System :Clock | : o | : x | :if Metronome is |
| :Real Time :Commands | : o | : x | : used |
| :Aux :Local ON/OFF | : x | : x | : |
| : :All Notes OFF | : x | : x | : |
| :Mes- :Active Sense | : o | : o | : |
| :sages:Reset | : x | : x | : |
| :Notes: All recognized data are transmitted 500msec later | | | |
| : if controller mode is STOP or RECORD and DELAYOUT mode is on. | | | |
| : *1 = All data are enabled to transmitted as record data | | | |
| : if other E_seq disk is used and DELAYOUT mode is on. | | | |
| Mode 1 : OMNI ON, POLY | Mode 2 : OMNI ON, MONO | o : Yes | |
| Mode 3 : OMNI OFF, POLY | Mode 4 : OMNI OFF, MONO | x : No | |

MIDI Troubleshooting and Know-how

When MIDI is connected, there are a few mistakes and problems that occur frequently.

- ① **A sound source instrument has been connected to the MIDI OUT terminal, but no sound is produced by the external instrument.**
 - Are you sure the MIDI cable is firmly connected between the MIDI OUT terminal of the Disklavier and the MIDI IN of the external instrument?
 - Does the reception channel of the external instrument match the transmission channel of the Disklavier?
 - If you are performing, is the MIDI OUT switched to KEYBOARD OUT?
 - During playback, is the MIDI OUT switched to DELAY OUT? (In this case, the transmission channel of the Disklavier cannot be changed.) Or, if it is KEYBOARD OUT, is the volume setting of the MIDI OUT VOL. or the piano volume too low?
- ② **A keyboard or sequencer is connected to the MIDI IN terminal, and performance took place on the external instrument, but the piano keys were not struck.**
 - Is the MIDI cable firmly connected between the MIDI OUT terminal of the external instrument and the MIDI IN of the Disklavier?
 - Is the power on, and has a floppy disk been inserted?
 - Does the reception channel of the Disklavier match the transmission channel of the external instrument?
 - Is the volume setting of the piano (MIDI IN VOL.) too low?
- ③ **A keyboard or sequencer is connected to the MIDI IN terminal of the Disklavier, and performance took place on the external instrument, but the piano keys were struck with delay.**
 - It takes about 0.5 seconds from the time the information is received until the time the piano keys are struck.
- ④ **There is a difference in pitch between the piano and the external instrument.**
 - Is the transposition function on?
 - Is the TRANS/BEAT set to something other than "normal"?
- ⑤ **The voicing of the external instrument can not be changed.**
 - Does the transmission channel of the Disklavier match the reception signal of the external instrument? (If they do not match, the voicing cannot be changed.)
- ⑥ **The MIDI OUT VOL. function does not operate for the external instrument.**
 - Does the transmission channel of the Disklavier match the reception channel of the external instrument? (If they do not match, switching is not possible.)
- ⑦ **During performance or playback on the piano, there is no problem with the piano, but the upper or lower tones are not produced on another keyboard.**
 - Are you playing music separated for left and right hand?
 - Is the split function on?

- ⑧ **Split, transposition functions do not work on the external instrument.**
- Is the MIDI OUT set to DELAY OUT? (If it is not set to KEYBOARD OUT, these functions will not operate.)
 - Does the transmission channel of the Disklavier match the reception channel of the external instrument?
- ⑨ **When playing ensemble music, the external instrument receives all parts.**
- Is the reception channel of the external instrument set to OMNI ON?
- ⑩ **The MIDI OUT CH. was specified, but information is not output through the specified channel.**
- Is the MIDI OUT set to DELAY OUT? (If the performance information transmission channel was set to KEYBOARD OUT only, you can change to the original channel and transmit.)
- ⑪ **Voicing data was dumped from the keyboard or other instrument connected to the MIDI IN terminal of the Disklavier to the keyboard or instrument connected to the MIDI OUT terminal of the Disklavier, but on the Disklavier, "MIDI DATA ERROR" is displayed and the command is not implemented.**
- If a large volume of data is transmitted, dump without sending the data via the Disklavier.
- ⑫ **You recorded ensemble music yourself, but when you play it back, the rhythm goes haywire.**
- Sometimes if the amount of data is too large, the rhythm goes haywire.
To protect the piano, avoid sending overly large amounts of data. Also, avoid entering quick repeated notes or successive or long notes as much as possible.
- ⑬ **The piano repeats the same note over and over.**
- Is there a loop in the wiring of the MIDI cable?
- ⑭ **Playback is started on the Disklavier, and you wanted to start the rhythm machine and sequencer simultaneously by using system realtime messages, but cannot start them.**
- Timing Clock, Start, Continue, and Stop can be transmitted only in Stop or Recording mode, when the metronome function is ON.

■ Warning Displays and Error Messages

This Disklavier will display warnings and error messages on its LCD when operation has been incorrect or other problems have arisen. Should this happen, please take the appropriate measures after having found out what the trouble is by referring to the table below. Please note that some of these problems are easy to mistake for malfunctions though they are actually very easy to solve by yourself.

| Display | Cause | What to do... |
|--------------------------------------|--|---|
| DISK NOT READY! | Always displayed when power turned on. | |
| DISK WRITE PROTECTED! | Floppy disk contents are protected. | When recording or entering a music or disk title, cancel the disk's memory protect function by sliding back the erasure protection tab. |
| UNFORMATTED DISK | Unformatted disk has been inserted. | When you want to record on a new disk, you must first format it. |
| CONVERT DISK! | Disk that has been used with other system has been inserted. | In order to use a disk that was previously used with another system, it must be converted. |
| ERROR OCCURRED!! (PUSH STOP KEY) | Disk defective. | Replace the disk. |
| | Hardware problem. | Press the Stop key to cancel the error message. |
| MIDI DATA ERROR! (PUSH STOP KEY!) | MIDI data faulty. | Press the Stop key. |
| | Too large a volume of MIDI data received at once. | Press the Stop key. |
| | MIDI connections faulty. | After turning off the power for both pieces of equipment, check the connections. |
| FILE NOT FOUND! RE-CONVERT DISK! | Error occurred during playback or recording. | Reconvert, then rerecord. |
| | During rerecording, the disk's memory capacity has been exceeded. | Reconvert and record on a different disk. |
| CANNOT FORMAT! | Disk defective. | Replace the disk. |
| DISK FULL! | Disk full to capacity. | Record on a different disk. |
| CANNOT CONVERT! | Disk defective. | Replace the disk. |
| THIS FILE IS PROTECTED! | A purchased disk contains a piece of music which cannot be rerecorded. | |
| EXECUTING! | This message appears whenever internal processes take some time during which external operation is not possible. Please do not remove the disk as long as this message is displayed. | |
| CANNOT RECORD MORE SONGS | Attempt was made to record a 61st piece of music on a disk. | The maximum number of pieces that can be recorded on a single disk is 60. |

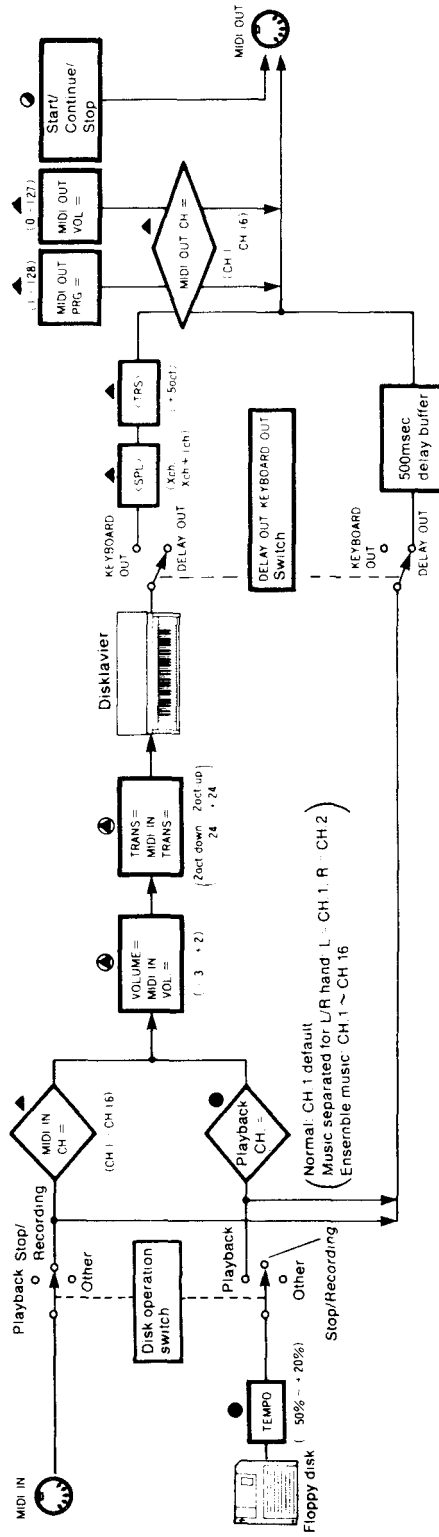
■ Error Number and Description in Details

When error occurred, the error number will be indicated on the 7 segment LED with pushing the L and R buttons at a time.

| Error No. | Error message on LCD | Description |
|-----------|--------------------------------------|--|
| 99 | DISK NOT READY! | Disk not ready |
| 98 | ERROR OCCURRED!! (PUSH STOP KEY!) | Disk read and write retry over |
| 97 | ERROR OCCURRED!! (PUSH STOP KEY!) | Buffer write overrun |
| 96 | ERROR OCCURRED!! (PUSH STOP KEY!) | Buffer read overrun |
| 94 | DISK FULL! | Disk track full |
| 93 | ERROR OCCURRED!! (PUSH STOP KEY!) | Disk seek retry over |
| 92 | ERROR OCCURRED!! (PUSH STOP KEY!) | Write error |
| 91 | DISK WRITE PROTECTED! | Write into protected disk |
| 90 | CONVERT DISK! | Unsuitable disk format("PIANODIR.FTL" not found) |
| 88 | ERROR OCCURRED!! (PUSH STOP KEY!) | Byte counter miss matching |
| 87 | ERROR OCCURRED!! (PUSH STOP KEY!) | Event frame buffer overflow(Greater than 38) |
| 81 | REPEAT 255 TIMES ALREADY | Repeat 255 times already |
| 80 | CANNOT PAUSE IN THIS MODE! | Use REC.PAUSE in MDR mode. |
| 79 | UNFORMATTED DISK | Un-formatted disk(Directory read DMA error) |
| 78 | ERROR OCCURRED!! (PUSH STOP KEY!) | Directory write DMA error(Write or seek error) |
| 76 | ERROR OCCURRED!! (PUSH STOP KEY!) | FDD restore error |
| 74 | THIS FILE IS PROTECTED! | This music file is protected |
| 73 | PUSH STOP KEY! | Drive power off |
| 70 | FILE NOT FOUND! RE-CONVERT DISK! | File not exist though exist in DIR. FIL |
| 69 | ERROR OCCURRED!! (PUSH STOP KEY!) | Invalid get data |
| 68 | ERROR OCCURRED!! (PUSH STOP KEY!) | Get circuite error(Parity/Framing error) |
| 67 | ERROR OCCURRED!! (PUSH STOP KEY!) | Get key over |
| 66 | ERROR OCCURRED!! (PUSH STOP KEY!) | Get circuite error(Receive overrun) |
| 65 | MIDI DATA ERROR! (PUSH STOP KEY!) | MIDI data error |
| 64 | ERROR OCCURRED!! (PUSH STOP KEY!) | DOS error |
| 63 | CANNOT FORMAT! | Format error |
| 62 | CANNOT CONVERT! | Convert error |

Error number 95, 89, 86, 85, 84, 83, 82, 77, 75, 72 and 71: NUL

Block Diagram



* This block diagram shows the MIDI functions in block form.

- Can be set in normal mode or MIDI Setup Mode.
- Can be set in normal mode.
- ▲ Can be set in MIDI Setup Mode.
- Produced during metronome operation.

■ Disassembly Procedure

1. Removing the Case Parts and Action

★When tuning:

- 1) Open and lock the Top Board.
- 2) Loosen the screws and securing fasteners on the inside of the left Side Board. (See Figure 1.)
- 3) Remove the Upper Front Board.
- 4) Remove the Fall Board.
- 5) Remove the Muffler Assembly.

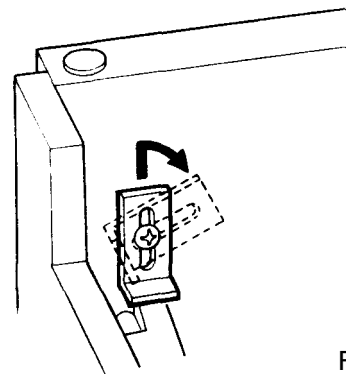


Figure 1.

★When regulating:

- 6) Remove the Key Stop Rail.
- 7) Remove the three screws shown at (A) in Figure 2, and the bolt shown at (B). Remove the Key Stopper Assembly.

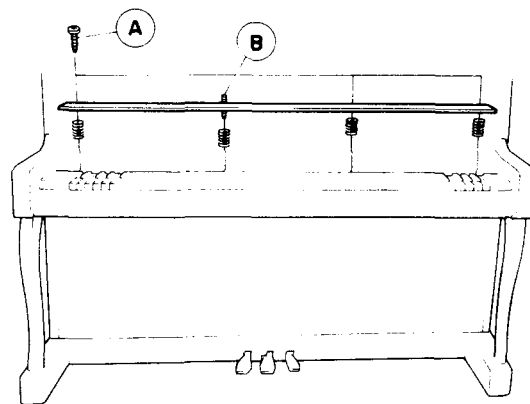


Figure 2.

- 8) Remove the stopper screw from the Lower Front Board Spring.
- 9) While pressing on the spring, pull the knob towards you, and remove the Lower Front Board. (See Figure 3.)

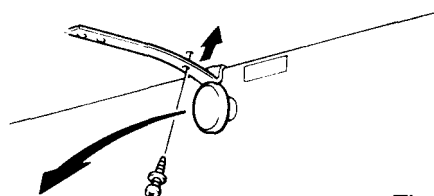


Figure 3.

★Removing the piano action:

- 10) Disconnect the four connectors from both sides of the action bracket.
- 11) Take off the knob bolt, and remove the action unit. (See Figure 4.)

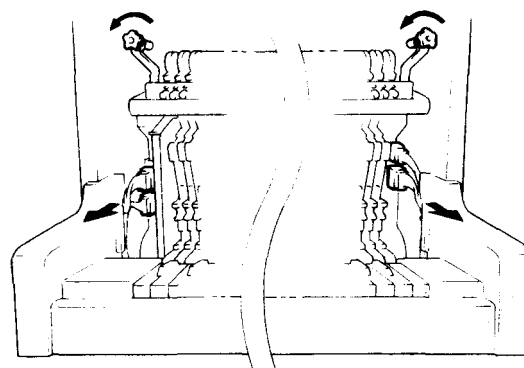


Figure 4.

2. Removing the Electrical Unit:

*If necessary, cut the binding tie.

* For connector numbers, refer to the circuit diagram.

* Before removing each unit, be sure to disconnect the AC plug.

★ Removing the Control Unit:

- 12) Remove the following screws shown in Figure 5: the four at (A), the six at (B), the one at (C) and the two at (D).
- 13) Shift the unit to the top and remove it.

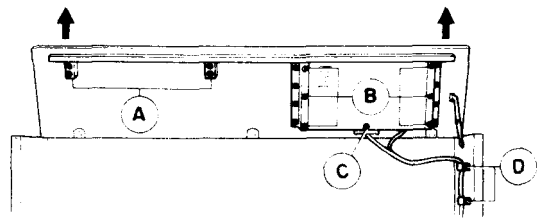


Figure 5.

★ Removing the CPU Circuit Board and FDD Unit:

- 14) Disconnect all the Connectors from the CPU circuit board, remove the six screws, and take out the unit.
- 15) Disconnect the two connectors of the FDD unit, remove the four screws shown in Figure 6, and remove the unit.

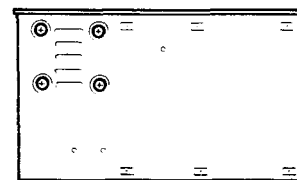


Figure 6.

★ Removing the Hammer Sensor Unit:

- 16) Remove the Action Unit (See steps 10 and 11).
- 17) loosen the two attachment screws on the right and left of the action bracket (see (A) on Figure 7), and remove the two center screws attaching the Hammer Sensor Unit (see (B) on Figure 7).
- 8) Pull up on the unit in the direction of the arrow, and remove it.

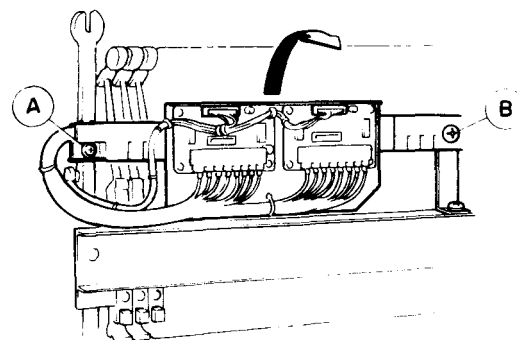


Figure 7.

★ Removing the Key Sensor Unit:

- 19) Remove the Keyboard.
- 20) Disconnect the three connectors to the unit (CN8, 9, 4).
- 21) Remove the six screws shown at (A) on Figure 8, and remove the unit.

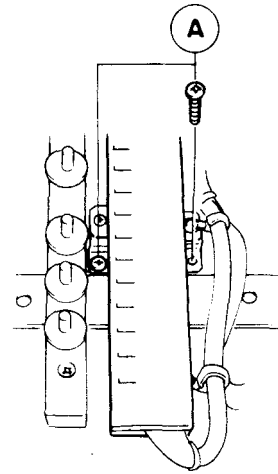


Figure 8.

★ Removing the Key Drive Unit:

- 22) Remove the fourteen connectors of the I/O Circuit Board (CN8, CN10~22) and the connector of the power source unit (CN8).
- 23) Remove the six attachment screws from the drive unit (see (A) of Figure 9.) and take out the unit.

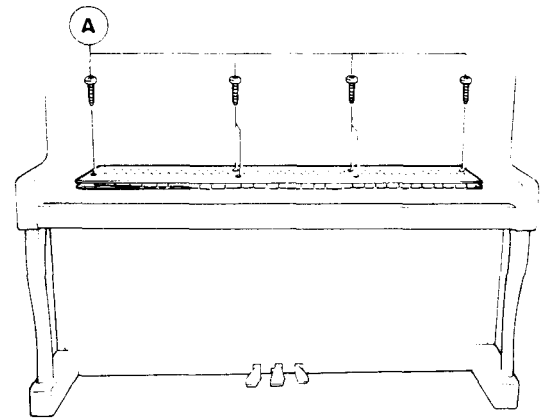


Figure 9.

★ Removing the Pedal Drive Unit:

- 24) Remove the two connectors at (A).
- 25) Loosen the hex nut at (B) on Figure 10 and then remove the hex nut at (C).
- 26) Loosen the four screws of the Side Board (D).
- 27) Shift the unit to the top and remove it.

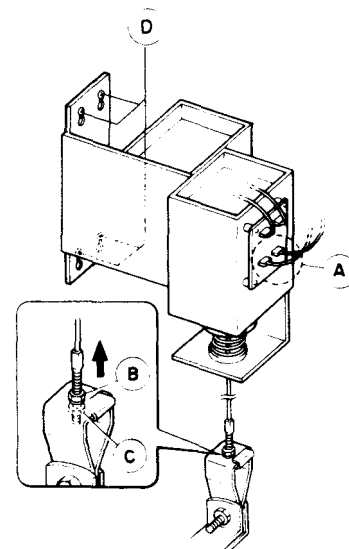


Figure 10.

★ **Removing the Pedal Sensor Unit:**

- 28) Remove the connector of the Sensor Circuit Board (CN3).
- 29) Remove the screws shown in Figure 11, and take out the unit.

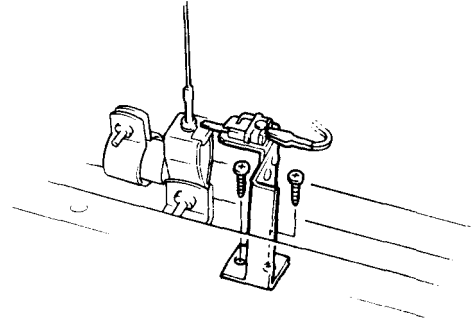


Figure 11 .

★ **Removing the I/O and Sensor Circuit Boards:**

- 30) Remove all of the connectors.
- 31) Remove the six screws of the Sensor circuit board and the seven screws of the I/O circuit board., and take out the two units. (See Figure 12.)

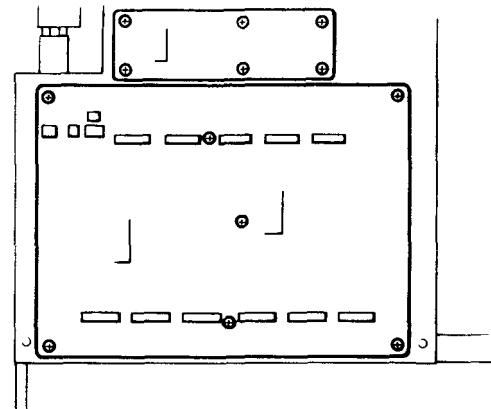


Figure 12.

★ **Removing the Power Source Unit:**

- * Disconnect the AC plug.
- 32) Disconnect the three connectors at (A). (See Figure 13.)

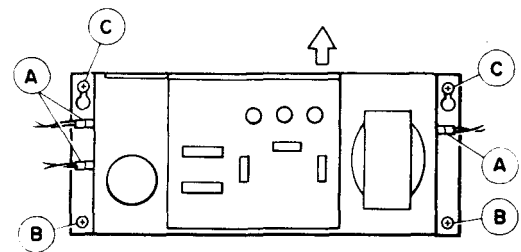


Figure 13.

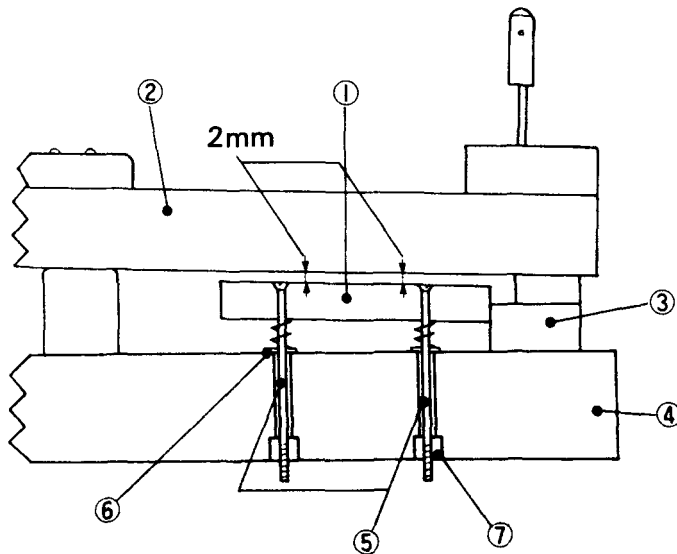
* The figure shows the power source unit as viewed from directly above.

- 33) Remove the two screws at (B) and loosen the two screws at (C).
- 34) Shift the unit in the direction of the arrow and take it out.

■ Adjustment Procedure

1-1. Adjustment of the Key Drive Unit

The key drive unit is mounted as shown in the figure below.



- ① Key drive unit
- ② Keyboard
- ③ Key back rail
- ④ Key bed
- ⑤ Adjustment screw 4×75
- ⑥ Spacer
- ⑦ Nut

• Adjustment-Front and Back

Fit the unit against the front surface of the key back rail.

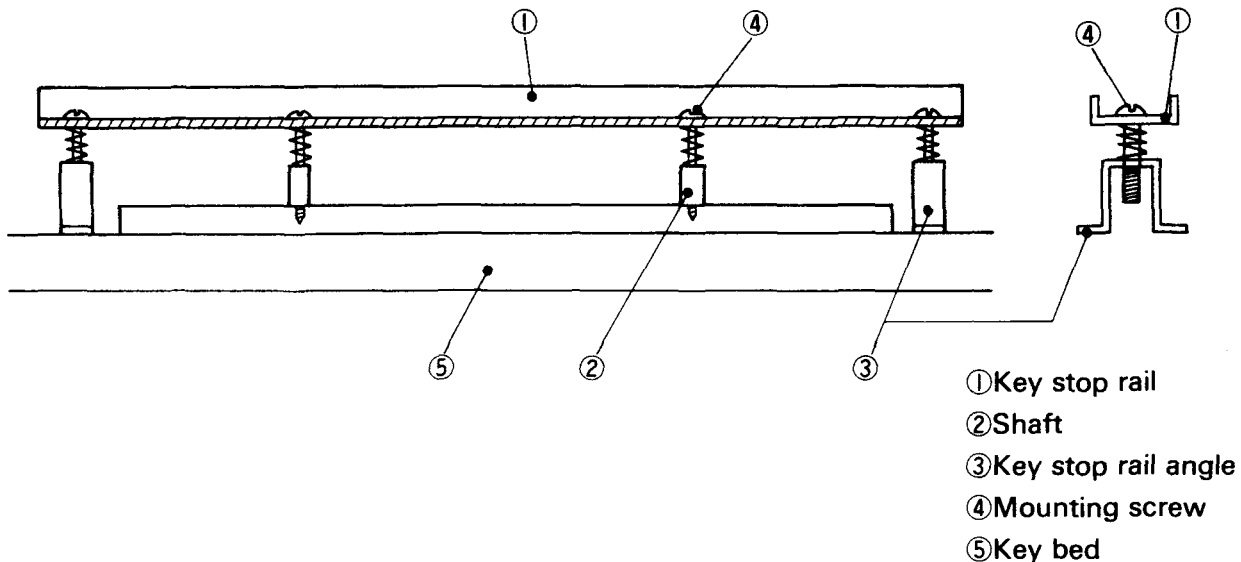
• Adjustment-Right and Left

Install Keys 2, 50, and 87 (black keys), then align the key drive unit so that each solenoid is at the center of its corresponding key.

• Adjustment-Up and Down

Install the remaining Keys except those at the position of the ⑤ adjustment screws. Use the adjustment screws to adjust the gap between the key drive unit's top surface and the keyboard's bottom surface to 2mm.

1-2. Adjustment of the Key Stop Rail Unit



- ① Key stop rail
- ② Shaft
- ③ Key stop rail angle
- ④ Mounting screw
- ⑤ Key bed

• **Height Adjustment**

Use the ④ mounting screws to adjust the gap between the top of the black keys and the bottom surface of the key stop rail to 0 (visually).

* If the screws are excessively tightened:

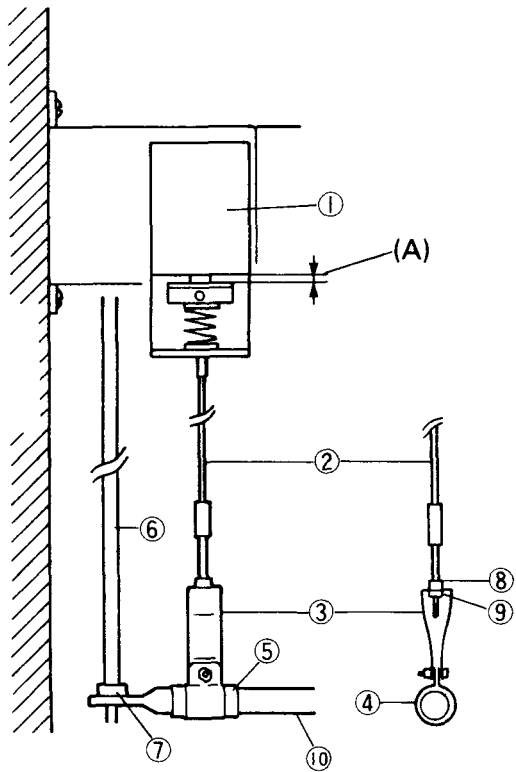
Key touch becomes heavy.

* If the screws are insufficiently tightened:

The keys may fly up during playback

Faulty sound generation

1-3. Adjustment of the Pedal Drive Unit



- ① Pedal solenoid
- ② Wire
- ③ Pedal lever hook
- ④ Pedal lever clamp
- ⑤ Rubber plate
- ⑥ Pedal rod
- ⑦ Pedal rod dowel
- ⑧ Hex nut M4
- ⑨ Hex nut M4
- ⑩ Pedal lever

Adjust the wire length so that the gap at section (A) is:

5mm: For the soft pedal

3mm: For the loud pedal

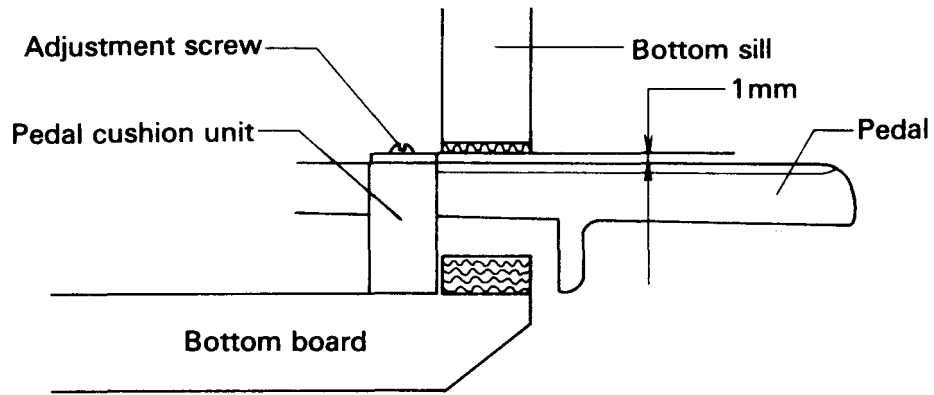
After performing adjustment using the © nut, tighten the ® nut.

*Set the wire so that it is positioned vertically, both front/back and side directions.

If the wire is slanted, it deviates from the center of the solenoid axis which will cause mechanical noise.

1-4. Adjustment of the Pedal Cushion Unit

Adjustment of the pedal cushion unit is as follows.

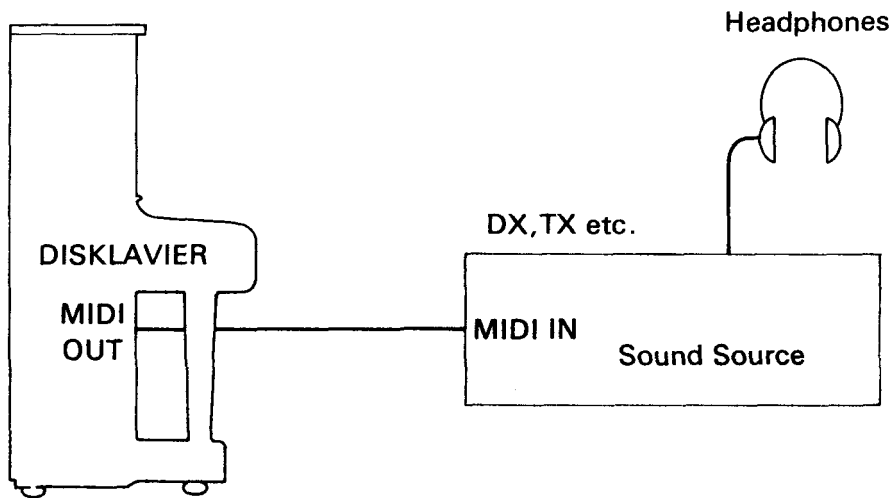


Cushion unit is too high---Pedal rebound noise can occur.

Cushion unit is too low---Pedal may not disengage.

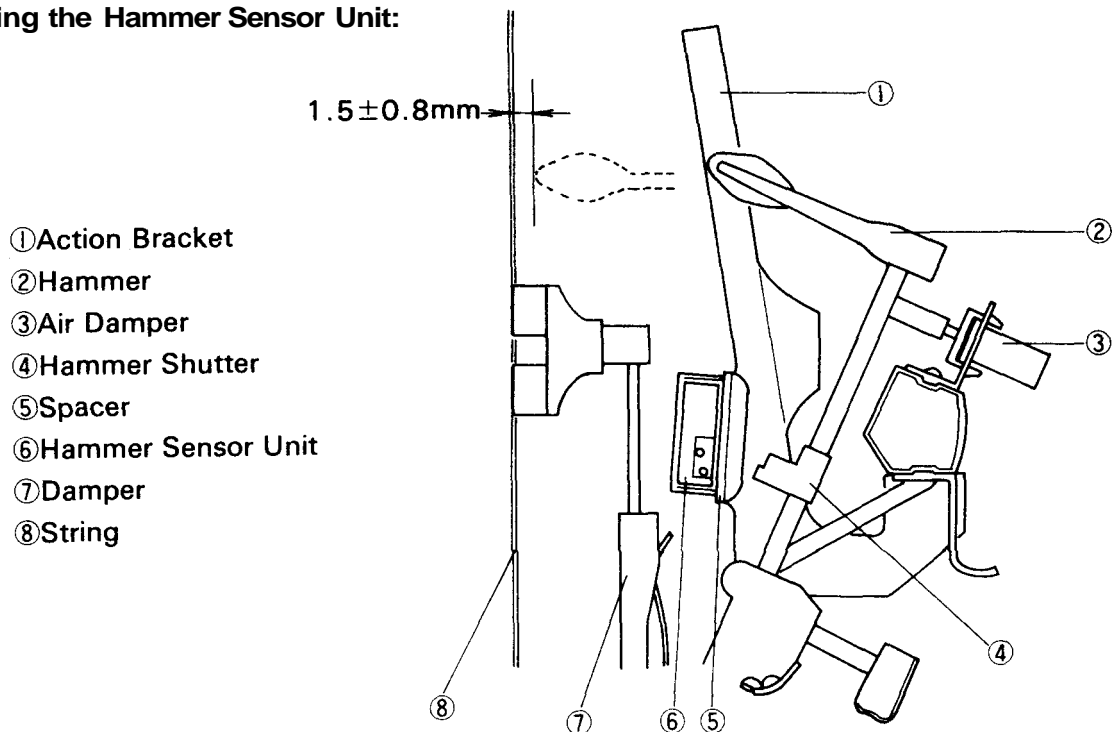
1-5. Adjustment Procedure for the Sensor Units

1) Settings When Adjusting the Sensors:



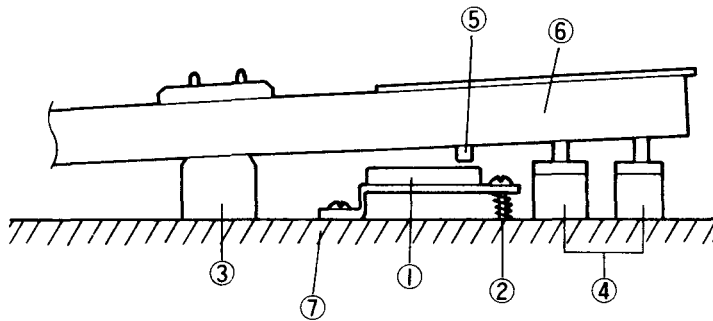
- Connect MIDI OUT on the DISKLAVIER to MIDI IN on the external sound source and set individual each unit so that transmission and reception of signals is possible.
- To adjust, select a sustaining sound such as an organ voice from the sound source.
- Adjust the sensors while monitoring the sound source with headphones.

2) Adjusting the Hammer Sensor Unit:



Increase or decrease the spacer (3 locations, number ⑤ in Fig.) to adjust.
Slowly push the hammer forward without playing the keys. Sound from the sound source is produced when the distance between the hammer tip and surface of the string is $1.5 \pm 0.8\text{mm}$.
Measure the bass, middle and treble sections and adjust to the mean of 2 ~ 3 keys.

3) Adjusting the Key Sensor Unit:

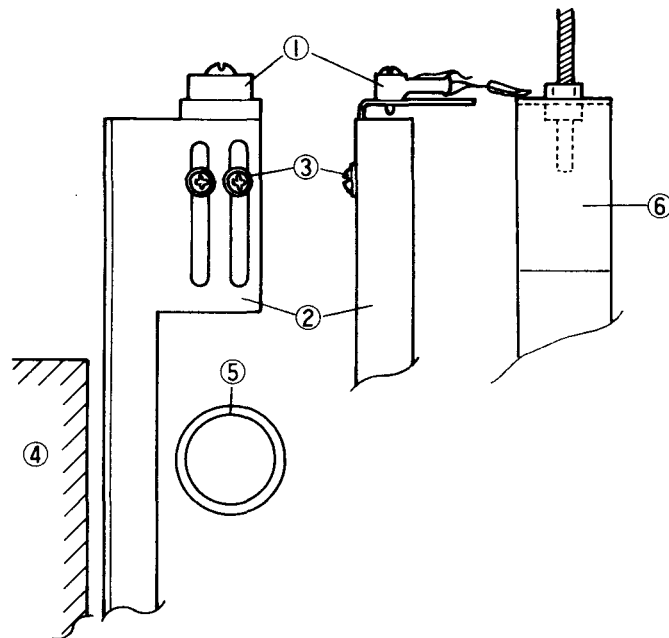


- ① Key Sensor Unit
- ② Height Adjustment Screw
- ③ Balance Rail
- ④ Front Rail
- ⑤ Key Shutter
- ⑥ Keyboard
- ⑦ Key Bed

Depress a white key making sure sound is being produced on the tone generation system.
Slowly release the white key and adjust the height adjustment screws (4 locations).
The sound source should stop at a height of 4.5 ± 0.5 mm (half of key depth) from normal key level.

Sound stop timing is too fast...Loosen the screw (counterclockwise)
Sound stop timing is too slow...Tighten the screw (clockwise)

4) Adjusting the Pedal Sensor Unit:



- ① Pedal Sensor
- ② Sensor Bracket
- ③ Adjustment Screw
- ④ Bottom Sill
- ⑤ Pedal Lever
- ⑥ Pedal Lever Hook

4)-1. Adjusting the Damper Pedal Sensor Unit:

Play the 49th key while holding down the damper pedal and confirm that the sound from the tone generator continues with the pedal depressed. Slowly return the pedal and adjust the screws (③) so that sound stops when the damper felt (in the vicinity of the 49th key) is approximately 1 mm removed from the string.

4)-2. Adjusting the Soft Pedal Sensor Unit:

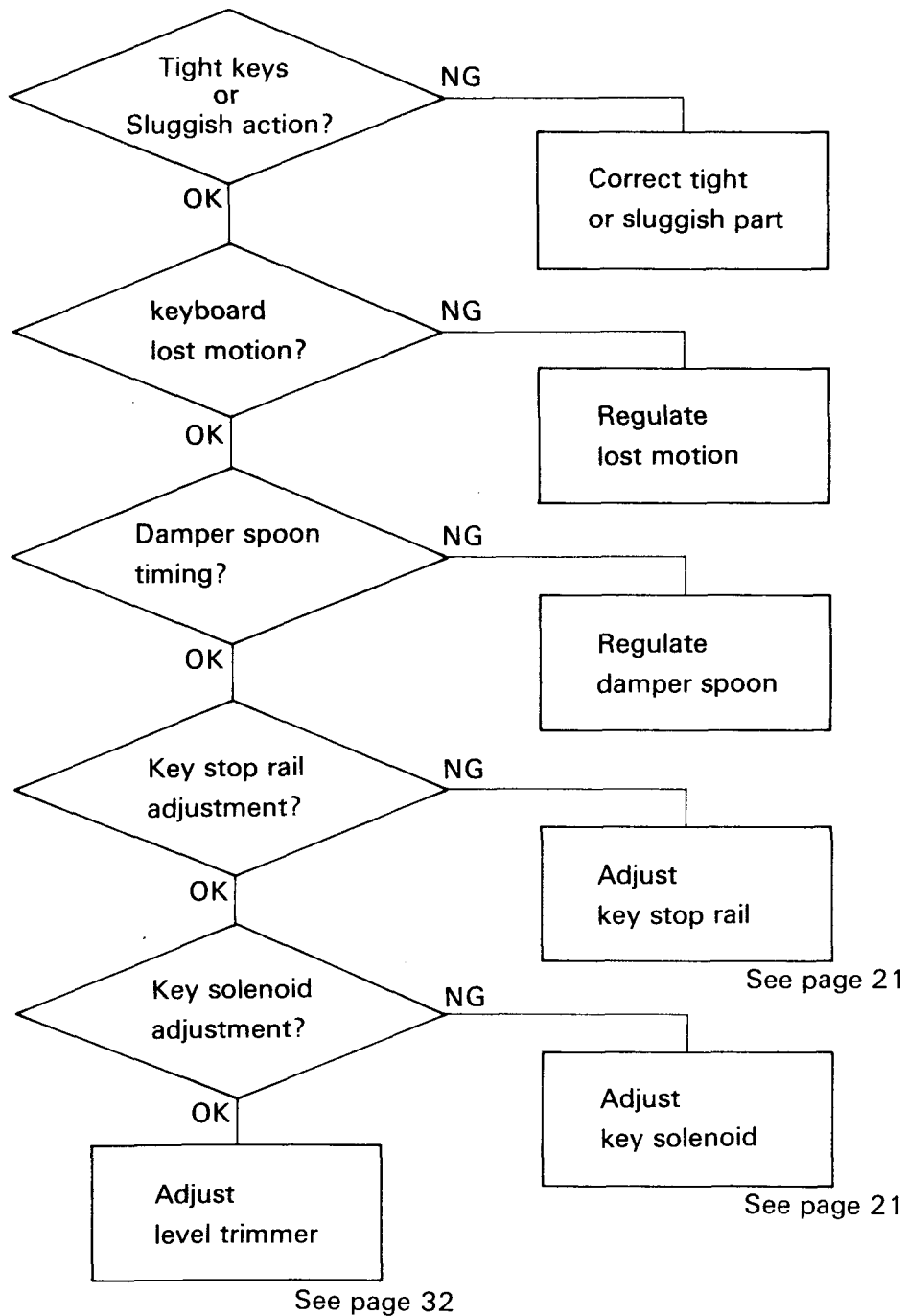
Adjust the screw (③) so that the soft pedal sensor is ON when the hammer rail is off blocking felt approximately 2 ~ 3mm. (eye measurement)

Operation Test of Each Unit Using The Test Disk

[TEST 1]

Minimum-pressure single-note scale of 1 to 88 keys. Tests whether the keys are played back when the minimum touch pressure is applied.

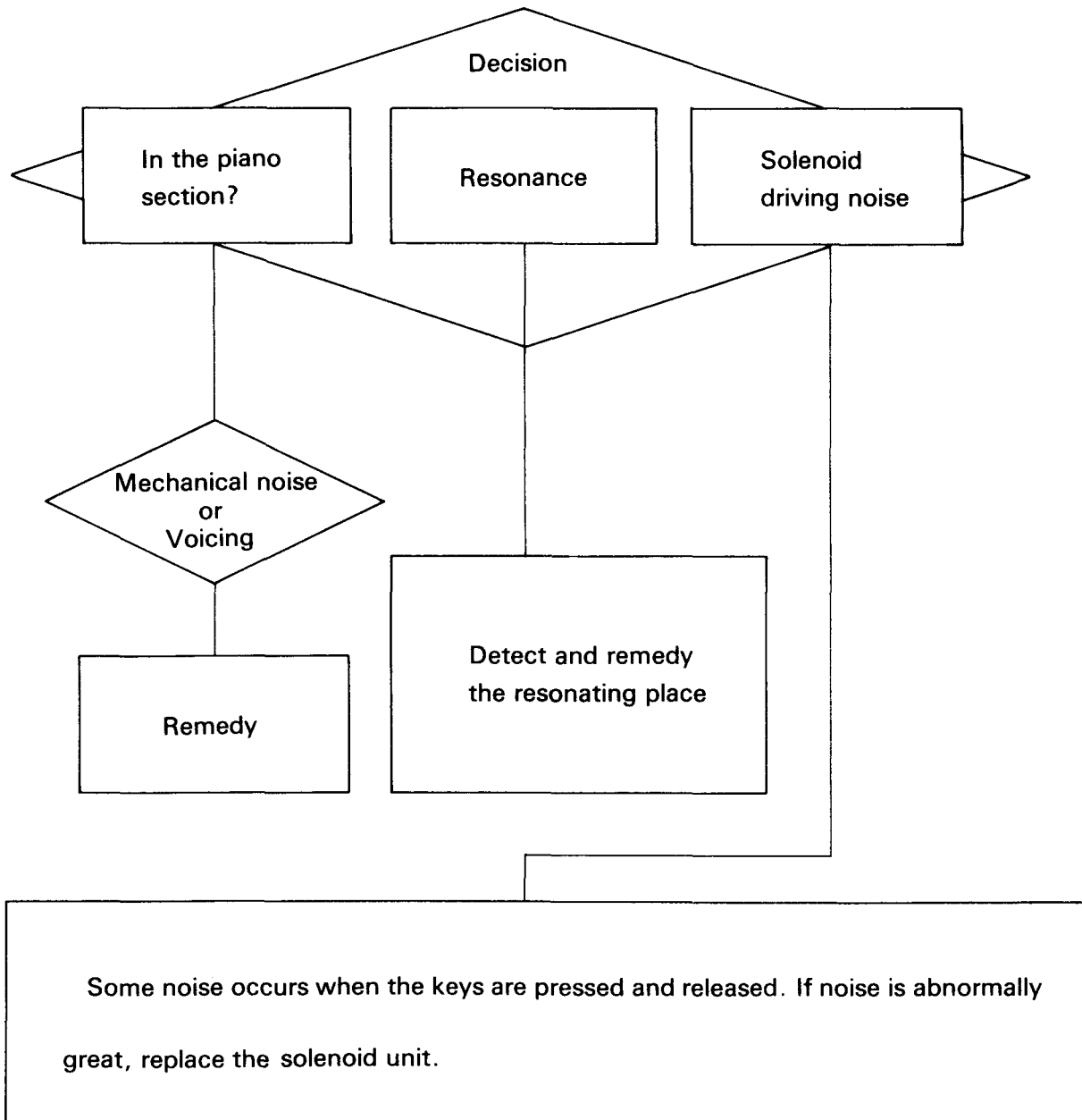
If a note is not sounded (although the keyboard operates normally):



[TEST 2]

Standard-pressure single-note scale of 1 to 88 keys. Tests the operation of the key solenoid unit (for noise, etc.).

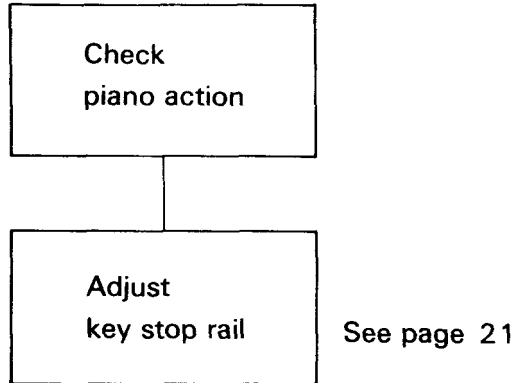
If noise is generated:



[TEST 3]

Applies pressure onto 5 consecutive keys. Tests the sound generation while playing trills (check for noise, etc.)

If sound generation is faulty:

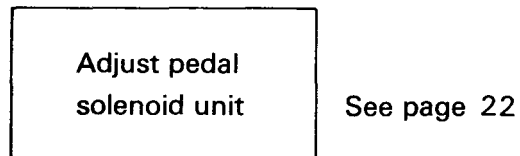


If noise is generated: See [TEST 2]

[TEST 4]

Turns ON the Damper Pedal/Soft Pedal five times for one second each time. Tests the efficacy of the Damper Pedal and Soft Pedal.

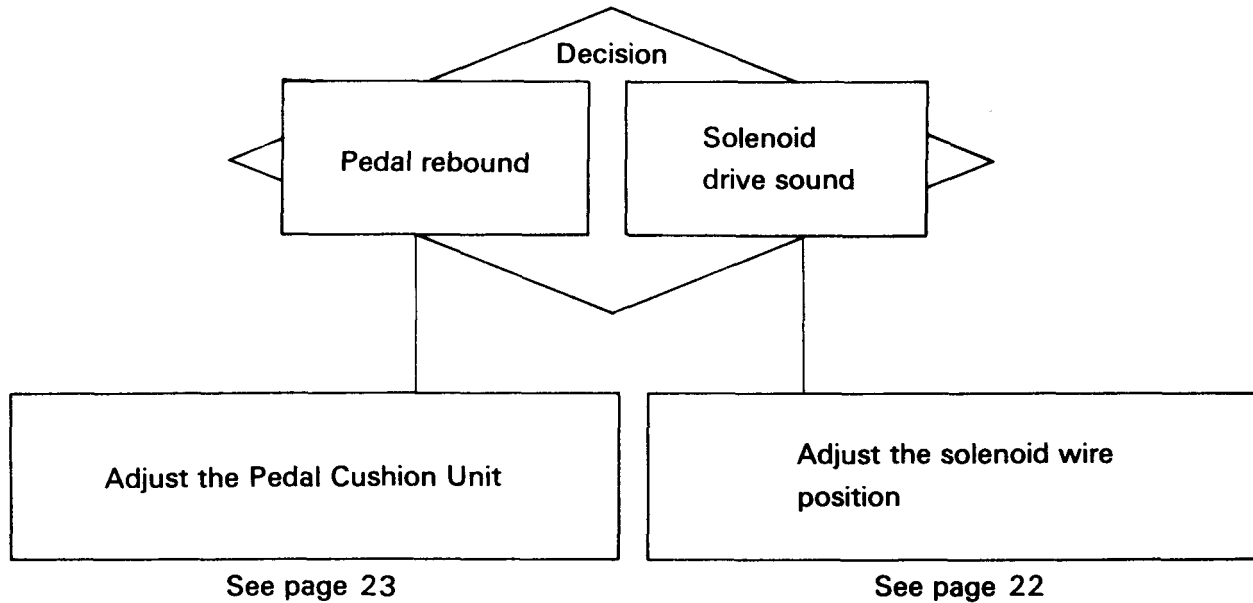
If a pedal malfunctions:



[TEST 5]

Turns ON the Damper Pedal ten times for one second each time. Tests the operation of the Damper Pedal (for noise, etc.)

If noise is generated:



[TEST 6]

Demonstration performance. Comprehensively tests all units in the playback system. Re-confirms [TEST 1] through [TEST 5] .

[TEST 7]

Recording and playback test. Tests the operation of the units in the recording system (using a disk for recording).

Records then plays back the scale of Keys 1 to 88, pedal ON/OFF, and so on.

The location of the defect can be determined by the error symptom that occurs, as listed below.

- Hammer/Key Sensor Units

Recording conditions: Key 1-88 scale, normal volume

| Symptom | | | | | | | Faulty Section |
|--------------|------------------------|-----------------|--------|--------|------------|----------------|---|
| Abnormal Key | | Playback Volume | | | Key Return | | |
| 1 Octave | Same note in each oct. | Highest | Lowest | Normal | Return | Doesn't Return | |
| | ○ | ○ | | | ○ | | LED drive circuit of Hammer Sensor Unit |
| ○ | | | ○ | | | ○ | M1 detect circuit of Hammer Sensor Unit |
| ○ | | ○ | | | ○ | | M2 detect circuit of Hammer Sensor Unit |
| | ○ | | | ○ | | ○ | LED drive circuit of Key Sensor Unit |
| ○ | | | | ○ | | ○ | Key detect circuit of Key Sensor Unit |

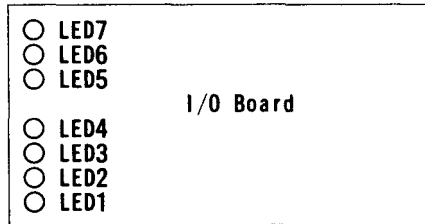
- Pedal Sensor Unit

| Symptom | Faulty Section |
|---|---|
| Pedal doesn't work | Pedal Sensor Unit is positioned too high. |
| Pedal remains depressed and doesn't return. | Pedal Sensor Unit is positioned too low. |

* Attention must also be given to the connector of the action brackets (right and left).

■ Error Display LEDs on the I/O Board

As shown in the figure below, the I/O board is mounted with LEDs that indicate abnormal operation in the driving system. If an error occurs, the pertinent LED lights up next, after the solenoid power supply is turned OFF, the other LEDs also gradually light up.



| | Error Circuit |
|------|--|
| LED1 | Key Solenoid Unit-Keys 25-48: Short-circuited diode or overcurrent to the drive transistor. |
| LED2 | Key Solenoid Unit-Keys 1-24: Short-circuited diode or overcurrent to the drive transistor. |
| LED3 | Key Solenoid Unit-Keys 49-72: Short-circuited diode or overcurrent to the drive transistor. |
| LED4 | Key Solenoid Unit-Keys 73-88: Short-circuited diode or overcurrent to the drive transistor. |
| LED5 | Soft Pedal: Short-circuited diode or overcurrent to the drive transistor. |
| LED6 | Damper Pedal: Short-circuited diode or overcurrent to the drive transistor. |
| LED7 | Activation of the thermostat within the Key Solenoid Unit |

- * Please note that the same display occurs if the 1 -M resistance for 100VDC voltage division is OPEN, or if the soldering between the error display comparator and the 10 μ F/16V tantalum capacitor is faulty.
- * When the error display circuit is activated, the 100V power supply for driving the solenoids is automatically switched OFF.

■ Adjusting the Trimmer

The operating efficiency of the piano action and keyboard will vary slightly according to the piano model. Even if two pianos have the same model number, minute differences may occur due to humidity, adjustment procedures, and other factors. When a person plays the piano, she/he listens to the sound and feels the key touch, then plays while making the necessary compensations. In case of a DISKLAVIER, however, the setting of the trimmer value must be regulated to the optimum status.

- Adjustment Procedure

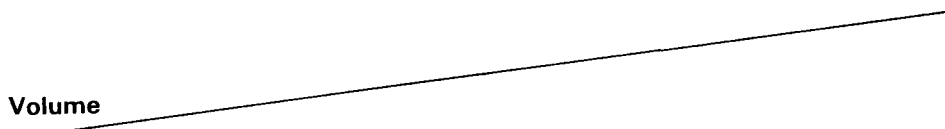
Conduct "TEST 1. Sound Generation Test under Minimum Pressure" on the floppy disk.

Contents: Minimum-pressure single-note scale of 1 to 88 keys.

Set the trimmer value so that all keys are sounded at the minimum touch pressure.

The better the regulation and action conditions, the more the sounding of notes under low touch pressure is improved. Therefore, make sure to check the adjustment then perform re-adjustment if necessary.

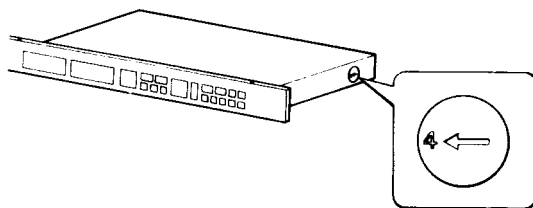
- The Trimmer Value and the Volume



| | | | | | | | | | | |
|----------------------------------|----|----|----|---------------|-----|-----|----|----|----|----|
| Trimmer Value | F | 0 | 1 | 2 | * 3 | * 4 | 5 | 6 | 7 | 8 |
| Normal Mode | -5 | -4 | -3 | -2 | -1 | 0 | +1 | +2 | +3 | +4 |
| * Low-Pressure Compensation Mode | | | | Trimmer Value | A | B | | D | E | |
| | | | | | +1 | +2 | | +1 | +2 | |
| | | | | Trimmer Value | 9 | | c | | | |
| | | | | | -1 | | | -1 | | |

* The recommended trimmer values are the circled values of "3" and "4".

* Low-Pressure Compensation Mode: When the volume data of a piano note indicates low volume, only that volume of that note is raised in this mode (9 and C are lowered).

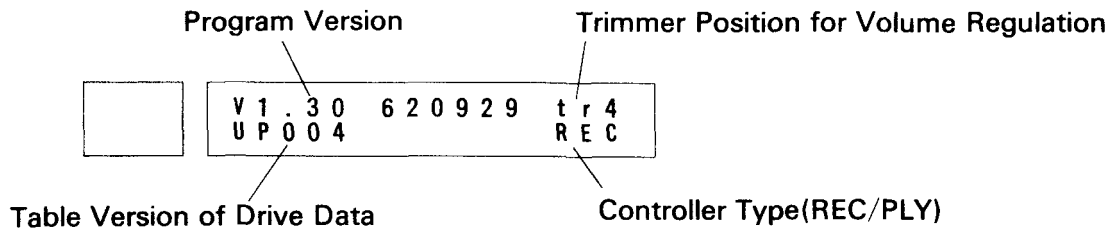


■ Test Program

1. STARTING/ENDING THE TEST PROGRAM

Among the two ROMs used for piano player control, ROM B contains the test program. When you simultaneously hold down the L and R buttons while you turn ON the power, TEST mode is entered.

Upon entering TEST mode, the ROM Version, Table Version, Trimmer Value, and Controller Type are displayed as shown below.

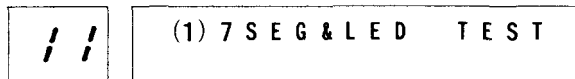


After the above display appears, use the "+" button to increase the value of the Test No. so that each test can be executed. (Press the "-" button to return to the previous Test No.) To terminate TEST mode, turn OFF the power to the controller.

2. DESCRIPTION OF EACH TEST

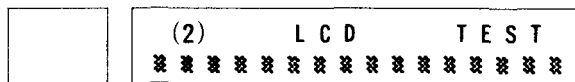
1) 7-Segment LED Test

The two 7-segment digits change from "0" to "9", the six LEDs light up successively from left to right next, all LEDs light up then go out. This entire process is repeated.



2) LCD Test

The characters which have been preset at the lower row of the LCD are displayed as flashing characters.



3) Buzzer Test

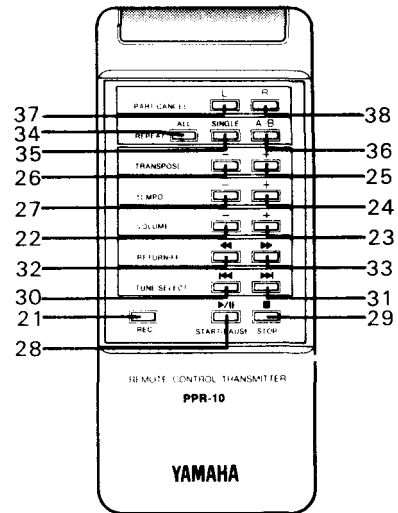
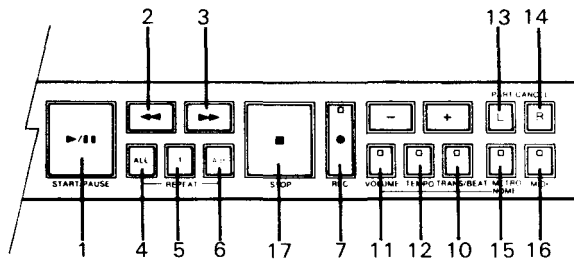
Along with the flashing display of "EXECUTING!" at the lower row of the LCD, the buzzer is sounded in alternately long and short intervals.



4) Switch Test

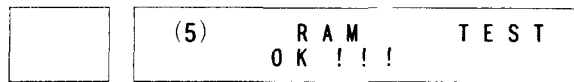
The 7-segment LEDs display the number corresponding to each switch.

Foot Switch=40



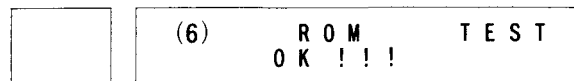
5) RAM Test

Write, Read, and Verify operations are conducted, then the result is displayed. (OK!!/NG!!)



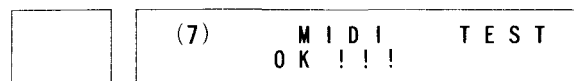
6) ROM Test

The checksum of ROM B is calculated, and the result is displayed. (OK!!/NG!!)



7) MIDI Test

The MIDI IN and MIDI OUT terminals of the controller are directly connected, and the result is displayed. (OK!!/NG!!)



8) PIANO Test

For the playback system, single keys are successively pressed during approximately 0.5 seconds from Key 1. After Key 88 is pressed, the Loud Pedal and Soft Pedal are operated, then the sequence returns to Key 1 and is repeated.



During playback, the START/PAUSE, Fast Forward (1-Key Skip), and Fast Reverse can be used.

For the recording system, the Key No. of the pressed key is displayed at the seven-segment LEDs (Loud Pedal=90 Soft Pedal=91), and GET-DATA is displayed at the lower row of the LCD.

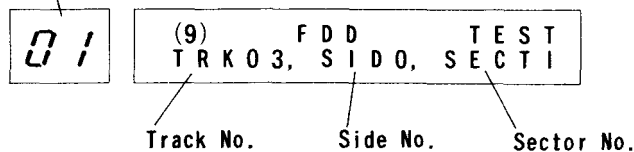
If an error occurs, "NG!!" is displayed at the lower row of the LCD and the Error No. is displayed at the 7-segment LEDs.

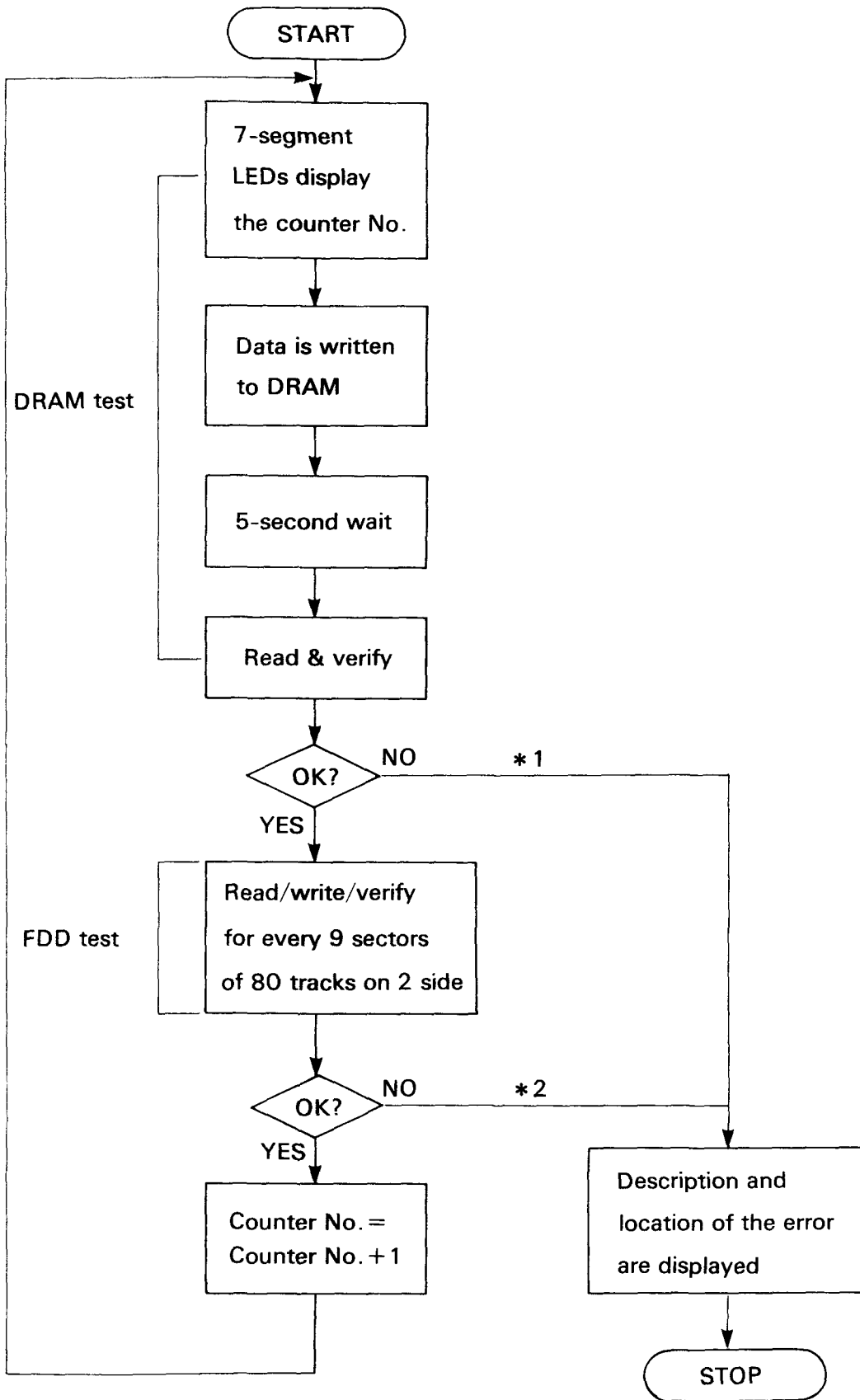
| Error No. | MEANING |
|-----------|--|
| 69 | INVALID GET DATA |
| 68 | GET CIRCUITE ERROR(PARITY/FRAMING ERROR) |
| 67 | GET KEYOVER |
| 66 | GET CIRCUITE ERROR(RECEIVE OVER-RUN) |

9) DRAM FDD Test

This test checks the refresh operation of the PMAC's DRAM, and performs Read, Write, and Verify operations on the floppy disk drive.

Number of test times





* Example 1: "AA" was written to Address \$1234, but "00" was read back.

| |
|-------------------------|
| 1 2 3 4 A A - 0 0 |
|-------------------------|

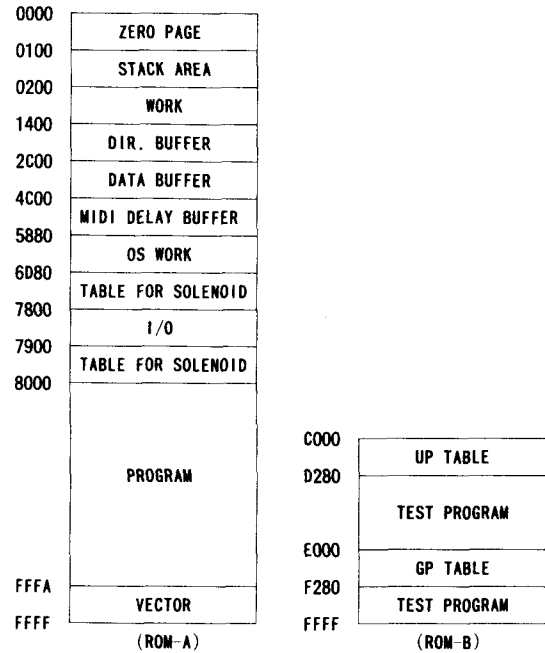
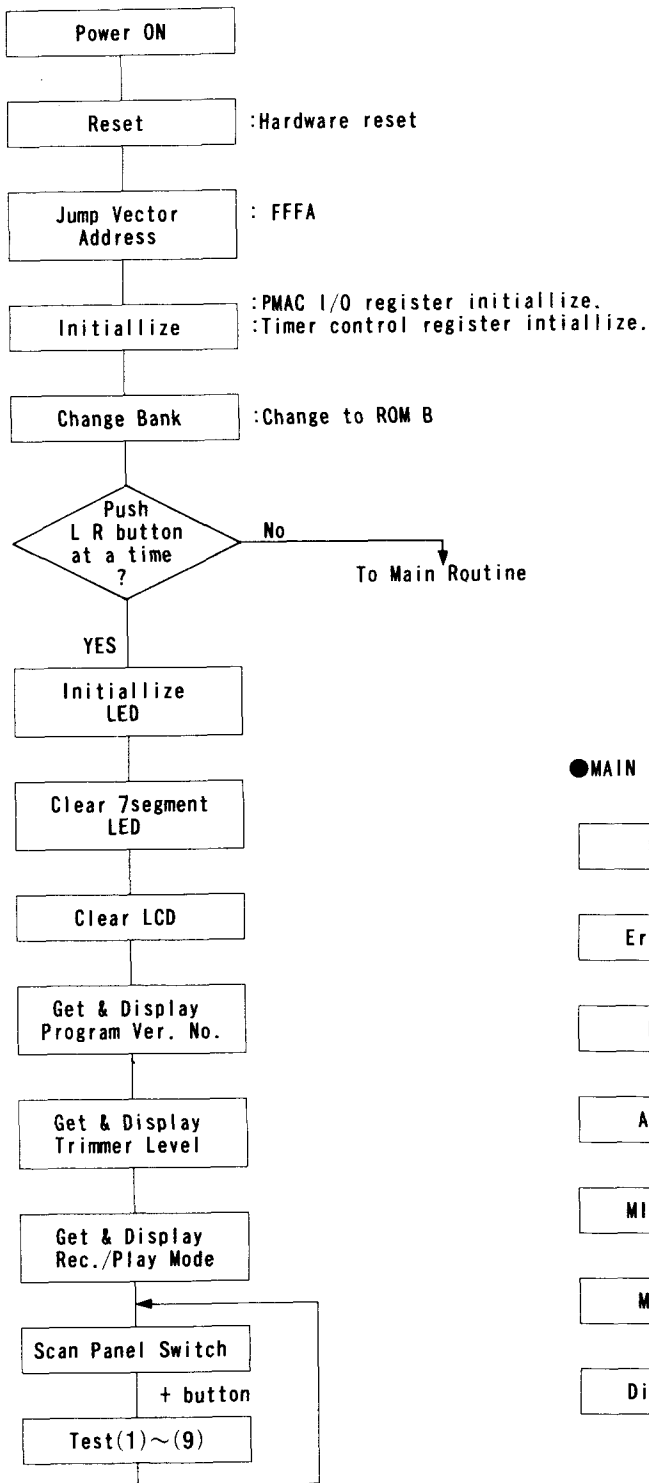
* Example 2: During the test, the Track No., Side, and Sector No. being tested are displayed at the lower row of the LCD.

- If an error occurs, its description and location are alternately displayed.

Error Description

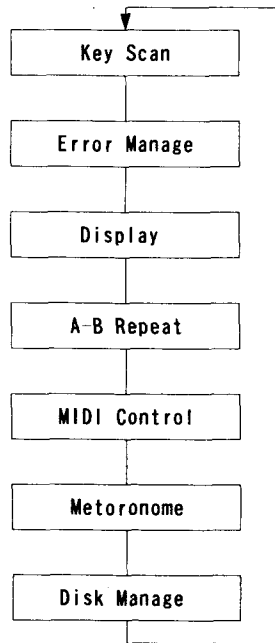
| Display | |
|----------------|--|
| DISK NOT READY | Disk is not installed. |
| SEEK ERROR | |
| READ ERROR | |
| WRITE ERROR | This is also displayed if the disk is write protected. |
| VERIFY ERROR | |

Program Flow-chart & Memory Map of Disklavier

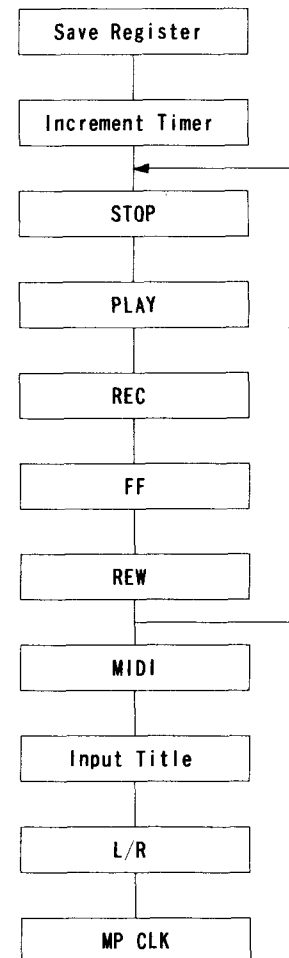


* UP or GP table in ROM-B is selected with dip switch condition, and transferred to the address of table for solenoid.

● MAIN LOOP



● IRQ(4msec.)

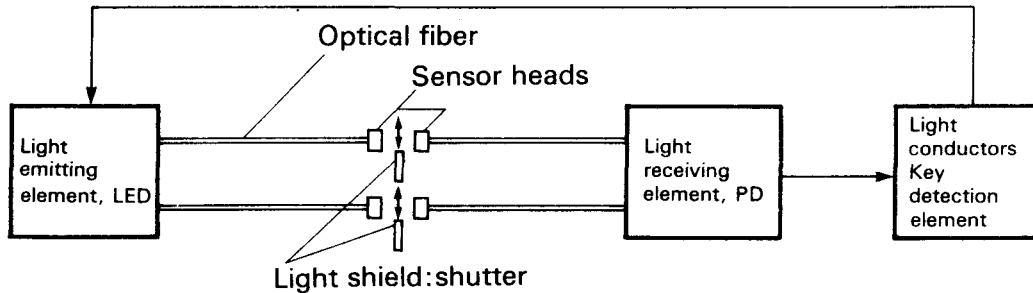


■ Explanation of Sensor System

The sensors on the DISKLAVIER are as follows:

- Key Sensor Unit...Optical Sensor...Detects key information.
- Hammer Sensor Unit...Optical sensor...Detects key information.
- Pedal Sensor Unit...Mechanical sensor...Detects pedal information.

1. Key Sensor Unit, Hammer Sensor Unit:

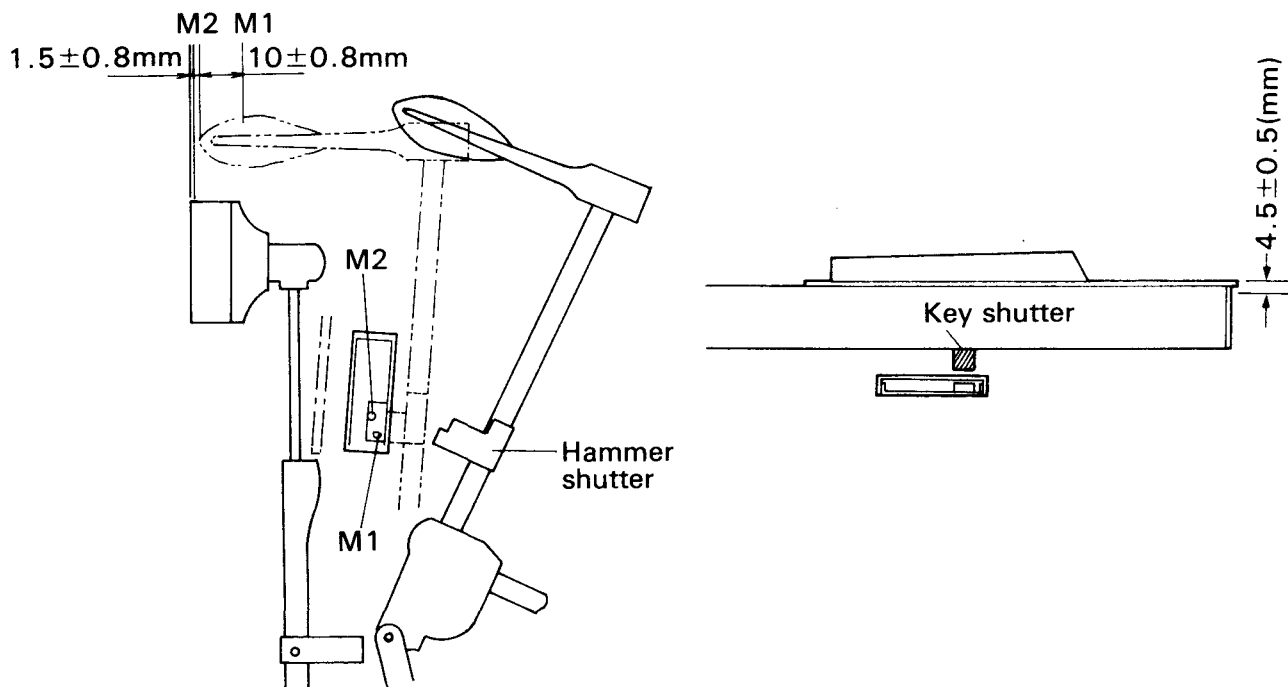


The Key Sensor, Hammer Sensor Units include the following equipment:

- Light emitting element...LED
- Light receiving element...Photo-diode (PD)
- Key detection element...LSI KAPP (YM2019)
- Conductor...Optical fiber, sensor head
- Shield...Shutter

Information is obtained by using a shutter to "Cut Off" the path of conduction for the light.

For each key there are three sensor heads, one on the keyboard (K) and two on the hammer (M1, M2). The unit is constructed in such a way that the light will be blocked when (K) is located 4.5mm from the start of keyboard depression, when the distance from the hammer tip to the string is 10mm for (M1), and 1.5mm for (M2).



This sensor system is used to obtain the key information listed below:

| Detection contents | Detection method |
|--------------------------------|---------------------------------|
| Which key is (Key Assign)? | OR of K and M1 (see Note 1) |
| When is (Note On Timing)? | M2 |
| How strong is (Touch Data)? | Time from M1 to M2 (see Note 2) |
| How long is (Note Off Timing)? | AND for K and M1 (see Note 3) |

Note 1: OR...Time during which either key sensor K or hammer sensor M1 has its light blocked by the shutter.

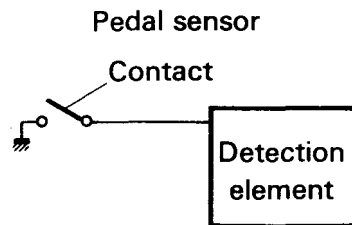
Note 2: When the time from M1 to M2 is:

Long...Hammer has passed slowly=Low sound volume

Short...Hammer has passed quickly=High sound volume

Note 3: AND...Time when the light for both K and M1 has been transmitted (shutter has returned).

2. Pedal Sensor Unit:



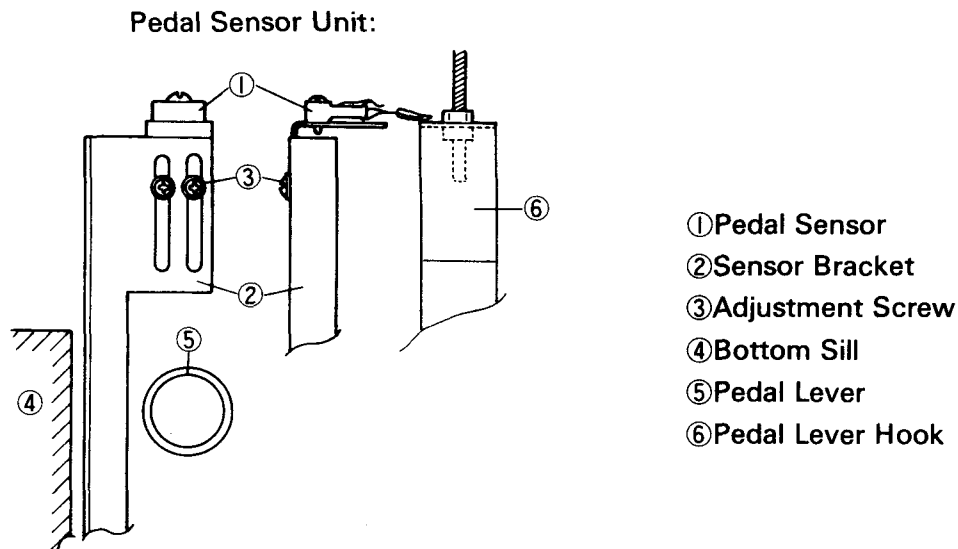
The damper and soft pedal sensor unit is constructed from the following equipment:

Contact...Leaf switch

Detection element...LSI KAPP (YM2019)

These components are used to detect the ON-OFF timing for each pedal.

The pedal sensor detects the ups and downs of the pedal lever hook and is set so that the switch goes on when the damper is 1 mm from the string.



■ LSI Data Table

● YM5205 (PMAC: Memory Access Controller)

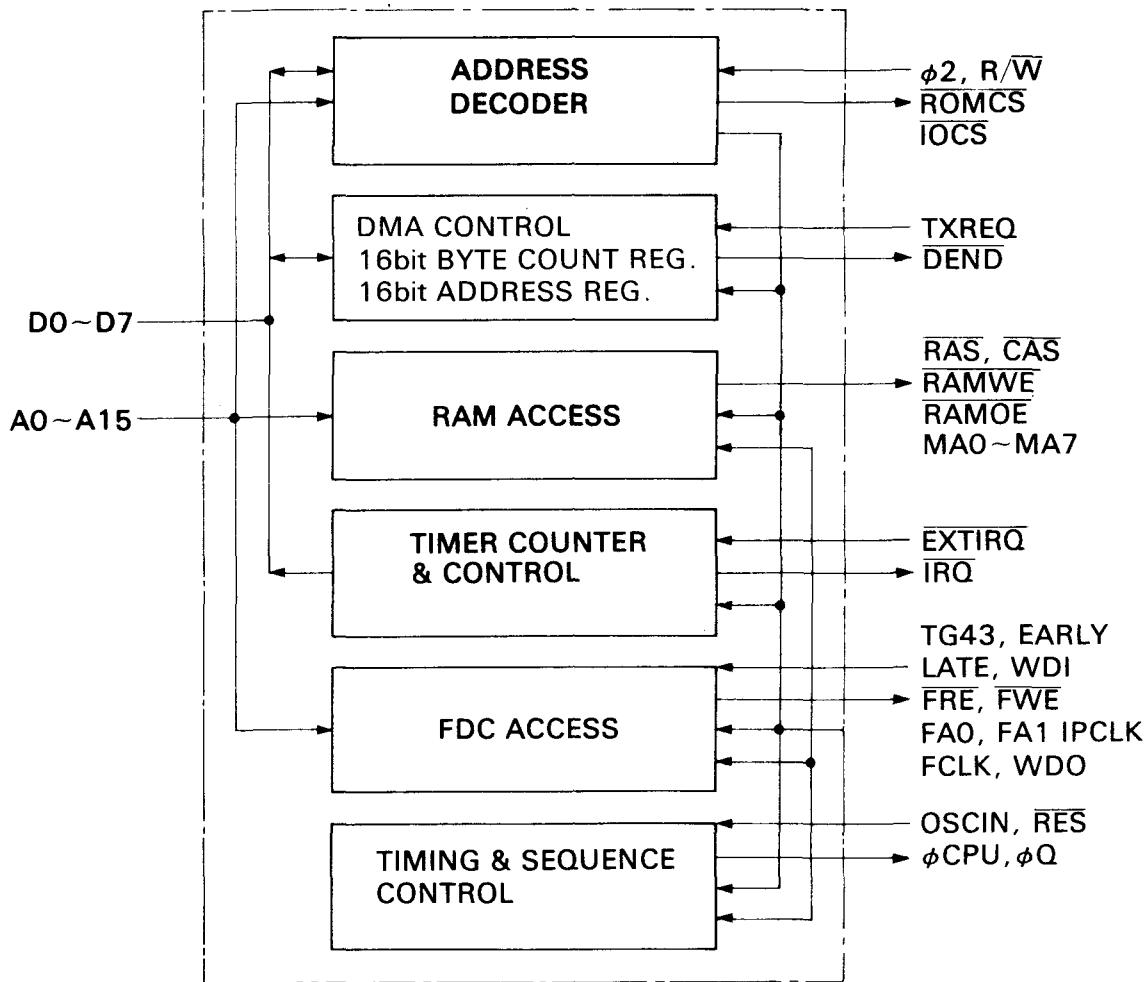
1. OVERVIEW

The PMAC contains the CPU peripheral circuit in a 64-pin OIL LSI. It selects addresses in and refreshes the dynamic RAM and also makes communication between the dynamic RAM and the FDC possible in the DMA mode. The other functions of PMAC include interrupt generation using an internal timer, decoding addresses for ROM I/O operations and FDC, CPU clock extension for low speed I/O access, generation of FDC, and write precompensation for the floppy disks.

To use the PMAC, the system must include a 2MHz YM2002CPU, the MB8877 FDC, MB8116Hx 8 (16KBytes) RAMs, and 2732-35x2 (8KBytes) ROMs. For future extensions, however, one can use 16KByte ROMs and 64KByte RAMs.

Use of PMAC assures the following:

- ① Almost total elimination of the random logic involving DMAC (LSI) and CPU.
- ② Reduction in the overheads caused due to DMA and memory refreshings because PMAC supervises the CPU operation clock.



YM5205 Block Diagram

2. CONSTITUENT PARTS

The internal circuit in PMAC consists roughly of six parts:

- ① DRAM controller...Controls address switching, $\overline{\text{RAS}}$ and $\overline{\text{CAS}}$ timing, and memory refreshing
- ② DMA controller...Controls DMA transfers between FDC and DRAM
- ③ Timer control...Controls interrupts generated by the internal timer and external triggers
- ④ Address decoder...Decodes the addresses from the CPU and generates the address decode signals for $\overline{\text{ROMCS}}$ and $\overline{\text{IOCS}}$
- ⑤ FDC support circuit...Generates clock signals for FDC and carries out write data precompensation for the disks
- ⑥ Timing controller...Uses microprograms to sequence the PMAC

PMAC operations are sequentially guided by the 8MHz clock.

| | | | |
|---------------------------|----|----|---------------------------|
| Vss1 | 1 | 64 | D0 |
| $\overline{\text{ROMCS}}$ | 2 | 63 | D1 |
| A0 | 3 | 62 | D2 |
| A1 | 4 | 61 | D3 |
| A2 | 5 | 60 | D4 |
| A3 | 6 | 59 | D5 |
| A4 | 7 | 58 | D6 |
| A5 | 8 | 57 | D7 |
| A6 | 9 | 56 | $\overline{\text{RES}}$ |
| A7 | 10 | 55 | ϕCPU |
| A8 | 11 | 54 | ϕQ |
| A9 | 12 | 53 | $\overline{\text{EX1RQ}}$ |
| A10 | 13 | 52 | $\overline{\text{TXREQ}}$ |
| A11 | 14 | 51 | $\overline{\text{TRQ}}$ |
| A12 | 15 | 50 | $\overline{\text{OSCIN}}$ |
| Vss2 | 16 | 49 | $\overline{\text{DEND}}$ |
| V00 | 17 | 48 | VGG |
| A13 | 18 | 47 | FCLK |
| A14 | 19 | 46 | WDI |
| A15 | 20 | 45 | WDO |
| R/W | 21 | 44 | LATE |
| $\phi 2$ | 22 | 43 | EARLY |
| $\overline{\text{RAS}}$ | 23 | 42 | IPCLK |
| $\overline{\text{CAS}}$ | 24 | 41 | TG43 |
| MA0 | 25 | 40 | $\overline{\text{RAMWE}}$ |
| MA1 | 26 | 39 | $\overline{\text{RAMOE}}$ |
| MA2 | 27 | 38 | FA0 |
| MA3 | 28 | 37 | FA1 |
| MA4 | 29 | 36 | $\overline{\text{FCS}}$ |
| MA5 | 30 | 35 | $\overline{\text{FRE}}$ |
| MA6 | 31 | 34 | $\overline{\text{FWE}}$ |
| MA7 | 32 | 33 | $\overline{\text{IOCS}}$ |

YM5205 Pin Assignment

3. LIST OF PMAC TERMINALS

| Area | Pin Name | Pin No | I/O | Function details |
|---------------|--------------------------|---------------|--------|---|
| Power source | Vss1 | 1 | | 0V(Ground) |
| | Vss2 | 16 | | 0V(Ground) |
| | Vcc | 17 | | +5V(±5%) |
| | Vdd | 48 | | Power is supplied via the +2V (across the 15Ω/5W resistance from Vcc) |
| Reset | $\overline{\text{RES}}$ | 56 | IN | Master reset. Is received if the low level continues for 8μ sec or more. Resetting registers and flip-flops(TMRH, TMRL, TMRC, DMCR, IRQF/F, and DRQF/F) to OOH and the I/O page register(IOPR) to COH. Accordingly, $\overline{\text{TRQ}}$ and $\overline{\text{DEN0}}$ go high. During the resetting period, output of a 2MHz clock occurs in φ CPU, but access is disable to RAM, FDC, PMAC and the I/O units. |
| Clock | OCSIN | 50 | IN | Takes the 8MHz master clock as an input. All PMAC operations depend on this upper limit of the master clock frequency is determined by the CPU, ROM, and RAM operating speeds and the lower limit by RAM refreshing sequence. This calls for an accuracy of 8MHz ±5%. |
| | φ CPU | 55 | OUT | This outputs a clock for CPU operations and is connected to the pin φ 0 of the YM2002. φ CPU goes high or low according to the system operations(I/O access, refresh period, DMA operations, etc.). During ROM/RAM access or CPU internal cycle...Low period: 250nsec High period: 250nsec During accesses to FDC, PMAC, or other I/O...Low period: 500nsec High period: 500nsec During refreshing period...Low period is extended by 500nsec DMA period...Low period is extended by 1μ sec |
| | FCLK | 47 | OUT | Outputs a 1MHz 1/2 duty cycle clock as the FDC operation clock. |
| | IPCLK | 42 | OUT | Outputs a 15.625KHz 1/2 duty cycle pulse as the dummy index pulse for floppy disk control. |
| CPU interface | φ 2 | 22 | IN | The φ 2 clock is issued only as φ CPU is delayed. However, it is used for correct access to PMAC by the CPU. |
| | A0~A15 | 3~15 18~20 | | Connected to CPU address bus. |
| | R/ $\overline{\text{W}}$ | 21 | IN | Connected to CPU R/ $\overline{\text{W}}$. |
| | D0~D7 | 57~64 | IN/OUT | Bidirectional(three state) terminal for connection to the data bus. |
| | $\overline{\text{TRQ}}$ | 51 | OUT | Output terminal for generation of interrupt requests (open drain negative logic output). The source generating IRQ is either the internal timer in PMAC or an external trigger(EXTIRQ). |

| Area | Pin Name | Pin No | I/O | Function details |
|---------------------------|---------------------------|--------|-----|--|
| Address decoder output | $\overline{\text{ROMCS}}$ | 2 | OUT | The address decoder output. A low logic if the addresses is given output by the CPU for those area in ROM. The range of the ROM area can be selected by bit 1 of the I/O page register. If the bit is '0', the area extends from (E000H) to (FFFFH) and if the bit is '1', from (C000H) to (FFFFH). If the area assigned to the I/O pages overlaps this area, the 256 overlapping bytes are given priority for I/O accesses. $\overline{\text{ROMCS}}$ does not go low now. |
| | $\overline{\text{IOCS}}$ | 33 | OUT | If the address output by CPU refers to an area other than the PMAC or FDC, the $\overline{\text{IOCS}}$ goes low. By specifying the high order 6bits of the address using bits 2-7 of the I/O page register(IOPR), the I/O area may be assigned to any optional address location(1Kbyte steps). The I/O page contains 256bytes. However as high order 16bytes(A07~04=F) are assigned to PMAC and the following 16bytes(A07~00=00~DF) on the low order side are available for external I/O. Therefore the $\overline{\text{IOCS}}$ goes low only if an address in this address range is accessed for the CPU. |
| RAM interface | $\overline{\text{RAS}}$ | 23 | OUT | Connection of $\overline{\text{RAS}}$ to DRAM. Connection of $\overline{\text{CAS}}$ to DRAM. Connection of $\overline{\text{WE}}$ to DRAM. Connected to the enable terminal of the DRAM output buffer (e.g. LS244) |
| | $\overline{\text{CAS}}$ | 24 | OUT | |
| $\overline{\text{RAMWE}}$ | 40 | OUT | | |
| $\overline{\text{RAMOE}}$ | 39 | OUT | | |
| | MA0~MA7 | 25~32 | OUT | Connected to DRAM address terminal. MA7 is provided if the 64K DRAM is used. Bit 0 of the I/O page register must be set to '1' now. In this way, all the area within the address space except the ROM area and the I/O pages is treated as the RAM area. If used with the 16K DRAM, bit0 of the I/O page register must be set to '0', and no connection must be made to MA7. |
| FDC interface | FA1 | 37 | OUT | Connected to A1 on the FDC(MB8877). Selects FDC internal register. Connected to A0 on the FDC. Selects FDC internal register. Connected to $\overline{\text{RD}}$ on the FDC. This is FDC read strobe. Connected to $\overline{\text{WT}}$ on the FDC. This is FDC write strobe. Connected to $\overline{\text{CS}}$ on the FDC. This is FDC chip select. Connected to $\overline{\text{DRQ}}$ on the FDC. The DMA transfer request input signal. DMA end. Indicates end of DMA. Active low. Connected to TG43 of FDC EARLY LATE WD Write data output to the floppy disk. This is write precompensation signal. |
| | FA0 | 38 | OUT | |
| | $\overline{\text{FRE}}$ | 35 | OUT | |
| | $\overline{\text{FWE}}$ | 34 | OUT | |
| | $\overline{\text{FCS}}$ | 36 | OUT | |
| | TXREQ | 52 | IN | |
| | $\overline{\text{DEND}}$ | 49 | OUT | |
| | TG43 | 41 | IN | |
| | EARLY | 43 | IN | |
| | LATE | 44 | IN | |
| | WDI | 46 | IN | |
| WDO | 45 | OUT | | |
| IRQ outside | EXIRQ | 53 | IN | Interrupt request from outside. Interrupt requests to the CPU are controlled by the timer interrupt control register(TMRC) written PMAC. Either the internal timer or the EXIRQ signal may be selected as the source of request. |

● MB8877 (FDC: Floppy Disk Controller)

To enter data on a floppy disk, 8 bits of serial data that is sent from the computer, must be converted into the corresponding parallel data. To this, again, the clock pulses must be applied. When reading data from a disk, the same procedure must be repeated, but the reverse order. Floppy disk controller FDC is, more than anything else, an LSI controlling these operations and also the floppy disk drive.

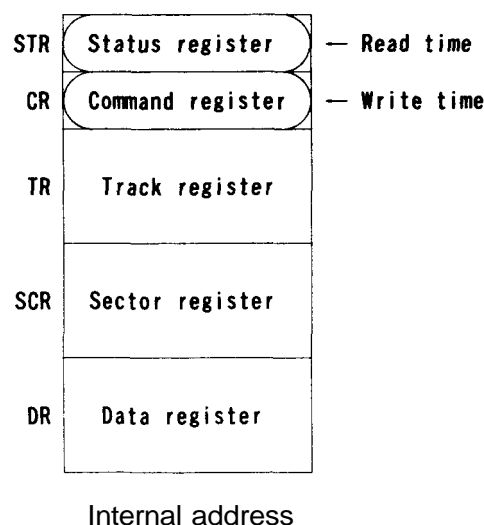
MB8877 has five internal registers that may be selected by using address lines A0 and A1. Internal commands may be written on the data I/O terminal to read in data from a diskette or write data on it. Each time a 1-byte data is read out from a diskette or written on it, the data request (DRQ) line issues a signal. A signal is issued at the IRQ terminal as soon as the execution of the command ends.

The data to be written on the diskette appears as the output at terminal WD (pin 30). $\overline{\text{RAW READ}}$ (pin 27 PRD) is the input terminal for data read from the diskette. This data includes clock pulses, though, only the data read out because of the window pulse entered through the RCLK terminal (pin 26). EARLY and LATE are the write compensation pulse output terminals indicating whether the write data must be accelerated or delayed. Terminal TG43 (pin 41) issues signals to indicate whether the head occurs ahead of or before track 43. The floppy disk drive uses this signal to change the strength of the write current. Recording density on the outer side of the disks is not the same as that on the inner side, so the write current must be adjusted to attain a uniform recording characteristic.

Terminal $\overline{\text{DDEN}}$ (pin37) is intended to change the format in MB8877 to double density. Pulse output terminal STEP (pin 15) moves the head through a track at a time in the direction determined by the output at DIRC (pin 46). $\overline{\text{TR00}}$ (pin 34) is an input terminal informing the FDC head is at track 0. $\overline{\text{IP}}$ (pin 35) is an input terminal for notifying detection of an index hole.

Signal HLD (pin 28) is meant to press the head on the diskette while terminal HLT (pin 23) indicates that the head is pressed on the diskette as it should be. Terminal READY (pin 32) indicates that the floppy disk controller is ready for normal operation.

| | | | |
|------------------------|----|----|------------------------------|
| VSS | 1 | 40 | +12V |
| $\overline{\text{WE}}$ | 2 | 39 | IRQ |
| $\overline{\text{CS}}$ | 3 | 38 | DRQ |
| $\overline{\text{RE}}$ | 4 | 37 | $\overline{\text{DDEN}}$ |
| A0 | 5 | 36 | WPRT |
| A1 | 6 | 35 | $\overline{\text{IP}}$ |
| DAL0 | 7 | 34 | $\overline{\text{TR00}}$ |
| DAL1 | 8 | 33 | VFOE/WF |
| DAL2 | 9 | 32 | READY |
| DAL3 | 10 | 31 | WD |
| DAL4 | 11 | 30 | WG |
| DAL5 | 12 | 29 | TG43 |
| DAL6 | 13 | 28 | HLD |
| DAL7 | 14 | 27 | $\overline{\text{RAW READ}}$ |
| STEP | 15 | 26 | PCLK |
| DIPC | 16 | 25 | VSS |
| EARLY | 17 | 24 | CLK |
| LATE | 18 | 23 | HLT |
| $\overline{\text{MR}}$ | 19 | 22 | $\overline{\text{TEST}}$ |
| VSS | 20 | 21 | +5V |



MB8877 Pin Assignment

LIST OF MB8877 TERMINALS

| Pin No. | Pin Name | I/O | Functions |
|---------|-----------------|-----|--|
| 1 | VSS | | VSS |
| 2 | <u>WE</u> | IN | Internal register write |
| 3 | <u>CS</u> | IN | Chip selection terminal |
| 4 | <u>RE</u> | IN | Internal register read |
| 5 | A0 | IN | Internal register selection terminal |
| 6 | A1 | IN | Internal register selection terminal |
| 7 | DAL0 | I/O | |
| 8 | DAL1 | I/O | |
| 9 | DAL2 | I/O | |
| 10 | DAL3 | I/O | Data IN/OUT terminal |
| 11 | DAL4 | I/O | |
| 12 | DAL5 | I/O | |
| 13 | DAL6 | I/O | |
| 14 | DAL7 | I/O | |
| 15 | STEP | OUT | Head movement pulse output terminal |
| 16 | DIPC | OUT | Head movement direction indication |
| 17 | EARLY | OUT | Shift write data to the faster side |
| 18 | LATE | OUT | Shift write data to the delay side |
| 19 | MR | IN | Chip reset terminal |
| 20 | VSS | | VSS |
| 21 | VCC | | +5V |
| 22 | <u>TEST</u> | IN | Chip test terminal (delay in internal timer is ignored) |
| 23 | HLT | IN | Head has been pressed |
| 24 | CLK | IN | Clock pulse input terminal |
| 25 | VSS | | VSS |
| 26 | <u>PCLK</u> | IN | Read data window pulse input terminal |
| 27 | <u>RAW READ</u> | IN | Input terminal for the raw data directly obtained from the disk |
| 28 | HLD | OUT | Press the head on the diskette |
| 29 | TG43 | OUT | Head occur at a larger track than 44 |
| 30 | WG | OUT | Data is being written on the disk |
| 31 | WD | OUT | Write data output terminal for the disks |
| 32 | READY | IN | Disk is set |
| 33 | <u>VFOE/WF</u> | IN | Write data error generated in disk |
| 34 | <u>TROO</u> | IN | Head occurs on track 0 |
| 35 | <u>IP</u> | IN | Index hole detection |
| 36 | <u>WPRT</u> | IN | Diskette write disabled |
| 37 | <u>DDEN</u> | IN | Access a double density disk |
| 38 | DRQ | OUT | Indicates that a byte of data has been read/written (Reset on DR access) |
| 39 | IRQ | OUT | Indicates that execution of a command has ended (Reset on writing of the STR read command) |
| 40 | VDD | | +12V |

●YM3802 MCS (MIDI Communication & Service Controller)

LIST OF YM3802 TERMINAL

| Pin No. | Pin Name | I/O | Functions | |
|---------|----------|-----|---|--|
| 1 | VDD | | VDD | |
| 2 | RXD | IN | Serial data input terminal | |
| 3 | RXF | IN | Audio signal input terminal from the magnetic tape such as a cassette tape. FSK modulated serial data are input to this terminal at the TTL level. | |
| 4 | CLKM | IN | Input terminal for a clock that generates an MIDI communication rate of 31.25K bps(baud). Usually 1MHz or 0.5MHz is input, and 1/16 or 1/32 of this can be used as the communication rate. This clock signal is divided to obtain a count clock for the general purpose timer and the MIDI clock timer and also for the output pulse width at the SYNC and CLICK terminals. A divide by two circuit is included so that the same setting time can be obtained, irrespective of the input signal to the CLKM terminal(1MHz or 0.5MHz). | |
| 5 | CLKF | IN | Input terminal for a clock that generates a communication rate of 75×2n series. Usually 614.4KHz is input and communication rates from 1/8192nd of this frequency(75 bps) to 1/32nd of the same frequency(19200 bps) can be used. Can be used also for counting in the MIDI active sense function. | |
| 6 | TXD | OUT | Serial data output terminal | |
| 7 | TXF | OUT | Audio signal output terminal to the magnetic tape such as a cassette tape. FSK modulated serial data are output from this terminal at the TTL level. | |
| 8 | SYNC | OUT | A 2msec width pulse is output from this terminal in synchronization with the MIDI clock. Used as a SYNC signal for other hardware. | |
| 9 | CLICK | OUT | A 2msec width pulse is output from this terminal in synchronization with the MIDI clock divided. Used when a metronome, etc., which is synchronized with the MIDI, is configured. | |
| 10 | TEST 0 | IN | Terminals used for testing the LSI. | |
| 11 | TEST 1 | IN | Thus, no connections are generally made. | |
| 12 | P7 | I/O | General purpose I/O port with which the input/output direction of each bit can be separately set. | |
| 13 | P6 | I/O | | |
| 14 | P5 | I/O | | |
| 15 | P4 | I/O | | |
| 16 | P3 | I/O | | |
| 17 | P2 | I/O | | |
| 18 | P1 | I/O | | |
| 19 | P0 | I/O | | |
| 20 | VSS | | | VSS |
| 21 | D0 | I/O | These terminals are used to provide interface to the host CPU. | |
| 22 | D1 | I/O | | |
| 23 | D2 | I/O | | |
| 24 | D3 | I/O | | |
| 25 | D4 | I/O | | |
| 26 | D5 | I/O | | |
| 27 | D6 | I/O | | |
| 28 | D7 | I/O | | |
| 29 | A0 | IN | | |
| 30 | A1 | IN | | |
| 31 | A2 | IN | The YM3802 is reset by the L level input signal to this terminal. The reset pulse width must be more than 32 clocks of the system clock to the CLK terminal (32 TCLK). The same reset operation can be made by the host CPU operation on the internal registers of YM3802. | |
| 32 | CS | IN | | |
| 33 | WR | IN | | |
| 34 | RD | IN | | |
| 35 | VR | IN | | |
| 36 | TRQ | IN | | |
| 37 | TC | IN | | |
| 38 | TEST 2 | IN | | Terminal used for testing the LSI. Thus, no connections are generally made. |
| 39 | CLKI | OUT | | Internal timing clock output terminal. |
| 40 | CLK | IN | | System clock input terminal. The internal operation of the YM3802, input signal sampling and output signal changing are all performed in synchronization with the internal timing clock made by this system clock. The clock rate must be more than 32 times the communication rate used. In the case of an MIDI communication rate of 31.25K bps at least a 1MHz clock rate is necessary. The maximum rate is 4MHz. |

| | | | |
|--------|----|----|--------|
| VDD | 1 | 40 | CLK |
| RXD | 2 | 39 | CLK1 |
| RXF | 3 | 38 | TEST 2 |
| CLKM | 4 | 37 | TC |
| CLKF | 5 | 36 | TRQ |
| TXD | 6 | 35 | VR |
| TXF | 7 | 34 | RD |
| SYNC | 8 | 33 | WR |
| CLICK | 9 | 32 | CS |
| TEST-0 | 10 | 31 | A2 |
| TEST-1 | 11 | 30 | A1 |
| P7 | 12 | 29 | A0 |
| P6 | 13 | 28 | D7 |
| P5 | 14 | 27 | D6 |
| P4 | 15 | 26 | D5 |
| P3 | 16 | 25 | D4 |
| P2 | 17 | 24 | D3 |
| P1 | 18 | 23 | D2 |
| P0 | 19 | 22 | D1 |
| VSS | 20 | 21 | D0 |

YM3802 Pin assignment

●YM2019 KAPP (Key Assigner for Piano Player)

1. OVERVIEW

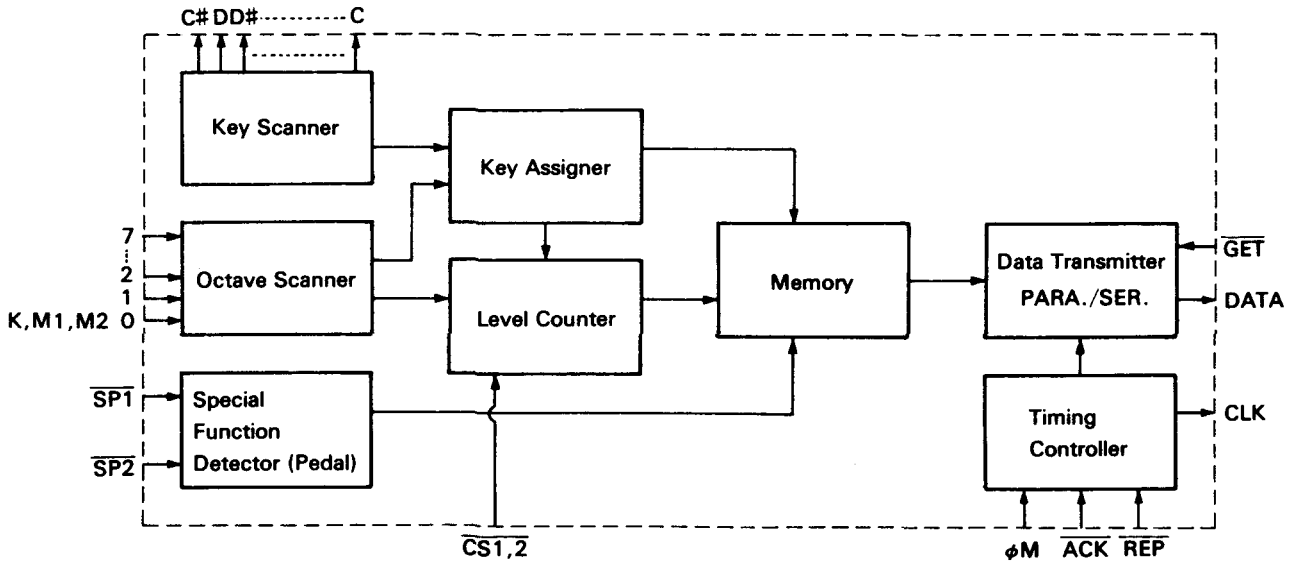
This LSI detects the operation being executed (keys, hammers and pedals) while a piano is being played. Its offers the features listed below:

- 1) Detects the ON/OFF timing of keys in eight octaves (96keys) and the hammer speed. (For up to 16 concurrently sounded notes.)
- 2) Detects the ON/OFF status of the damper and shift pedals.
- 3) The above performance events are output as serial data each time a GET pulse is input.

2. LIST OF YM2019 TERMINALS

| Pin Name | Pin No. | I/O | Function |
|--|-------------------------------|-----|--|
| VDD | 63 | | Power supply +5V. |
| VSS | 1,17 | | GND |
| ϕ M | 64 | IN | Inputs the master clock(4 MHz) |
| C#~C | 31~20 | OUT | Output the note scan pulses. |
| $\overline{\text{SPT}}$ | 16 | IN | Detects the ON/OFF status of the damper pedal. |
| $\overline{\text{SP2}}$ | 15 | IN | Detects the ON/OFF status of the shift pedal. |
| K0~K7 | 32,35,38 41,44,47 50,53 | IN | Detect the key Assign and Key Off timing. |
| M10~M17 | 33,36,39 42,45,48 51,54 | IN | Detect the Start timing of the level counter. |
| $\overline{\text{M20}}\sim\overline{\text{M27}}$ | 34,37,40 43,46,49 52,55 | IN | Detect the level counter's Stop timing and the Note ON timing. |
| $\overline{\text{CS1}},\overline{\text{CS2}}$ | 58,57 | IN | Selects the conversion curve for touch(speed) data. |
| $\overline{\text{CP}}$ | 56 | IN | Selects the method for detecting touch(speed). |
| $\overline{\text{TC}}$ | 2 | IN | Initial Clear terminal. |
| $\overline{\text{GET}}$ | 6 | IN | Inputs pulses for reading data. |
| DATA | 4 | OUT | Outputs serial data(events). |
| CLK | 9 | OUT | Outputs sync. clocks for data transmission. |
| $\overline{\text{ACK}}$ | 7 | IN | Inputs Wait Request signals. |
| $\overline{\text{REP}}$ | 8 | IN | Inputs Resend Data Request signals. |

3. Block Diagram

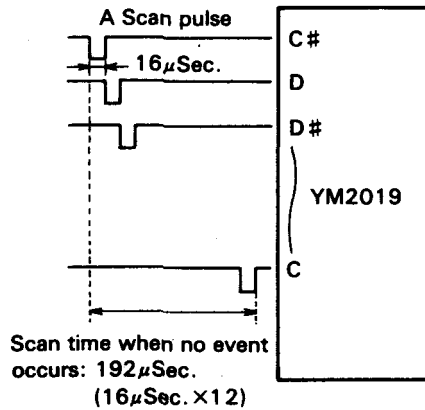


3-1. Key Scanner

Scanning is beginning from the C # note of Octave 0 to detect the status of the hammers within the octave for which Pin K is "1" (the octave in which a key is being pressed).

Key scanning is then stopped for 16μSec. while that hammer status is sent to the key Assignor. During the next 16μSec. interval, the next higher octave is scanned if the Pin K has not become "1" during that interval, scanning shifts to the next note (D). Consequently, if "N" is the number of keys being pressed, the scanning time for all keys (T) conforms to the equation below:

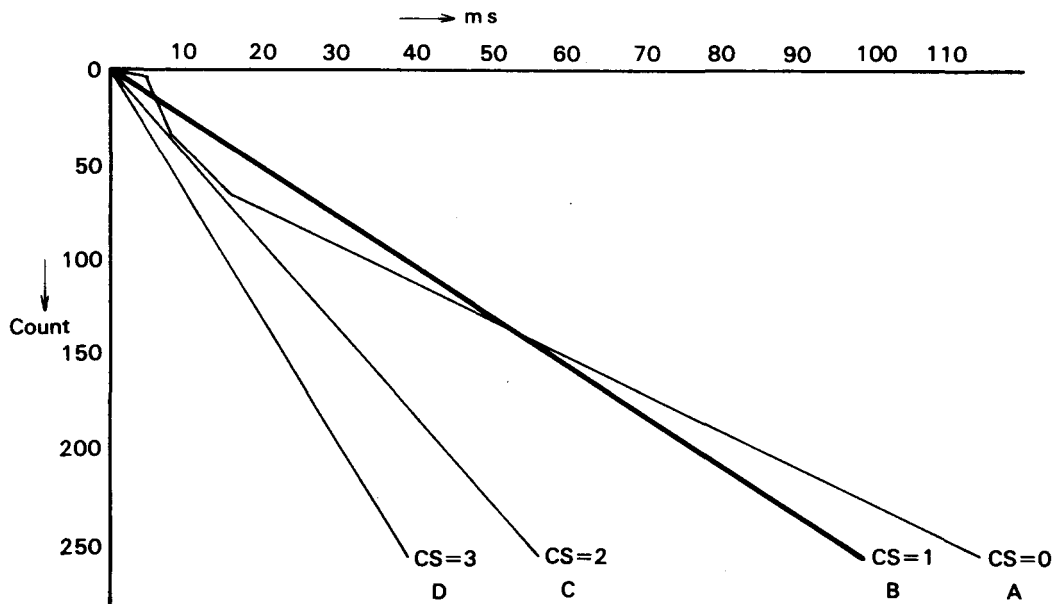
$$T = 16 \times (12 + N) \mu\text{Sec.}$$



3-2. Key Assignor and Level Counter

The data from the Key Scanner is assigned to an empty channel among the 16 channels. The operation conforms to the table as shown below, depending to the status of Pins K, M1, and M2. Based on the conversion curve selected by Pins $\overline{CS1}$ and $\overline{CS2}$, the Level Counter accumulates in 48 μ Sec. interval until the Count STOP timing. This counter value is the attenuation level.

| | |
|---------------------|-----------------------------|
| Assign | Pin K "0"-"1" |
| Count START | Pin M1 "0"-"1" |
| Count STOP/ON-Event | Pin $\overline{M2}$ "1"-"0" |
| OFF-Event | Pin K "1"-"0" |



• A Level Rate Selected by $\overline{CS1}$ and $\overline{CS=2}$: B is used in DISKLAVIER.

3-3. Special Function Detector

The special Function Detector detects the status of the damper pedal and shift pedal (ON/OFF switch status) at each GET pulse.

3-4. Memory

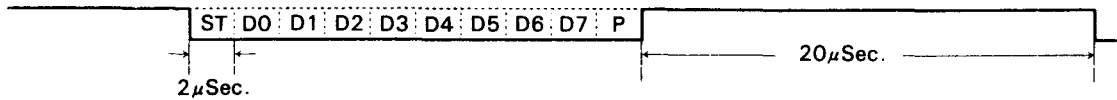
The Memory records the data listed below:

- ① The key address (note and octave) and Level Counter value of a Key-On event.
- ② The Key address of a Key-Off event.
- ③ The number (SP1 or SP2) of a special function (pedal).

3-5. Data Transmitter

When a $\overline{\text{GET}}$ pulse is received, control by the Timing Controller causes the data to be transmitted in the format shown below.

- ① Data Format for One Byte (40 μ Sec.).

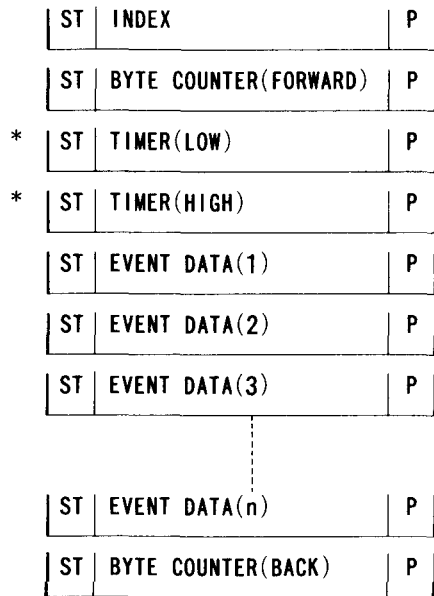


ST: Start bit, always "0".

P: Parity bit

The bit prior to ST and after P is always "1".

- ② Sequence of Data Transmission



· When no event has occurred, only the INDEX is transmitted.

* : Not used with the DISKLAVIER.

③ Description of the Data

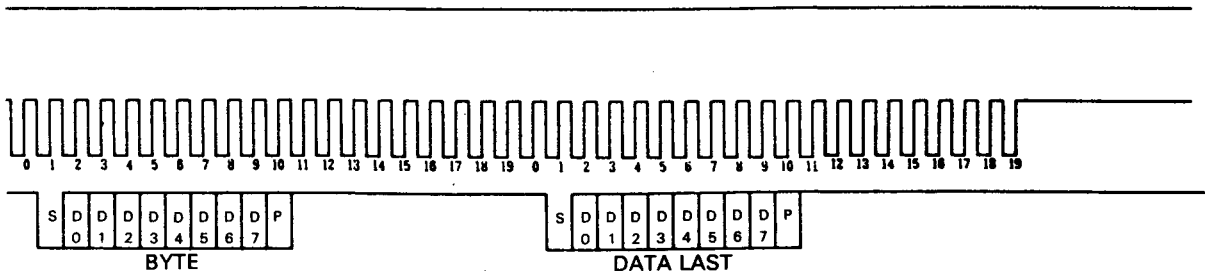
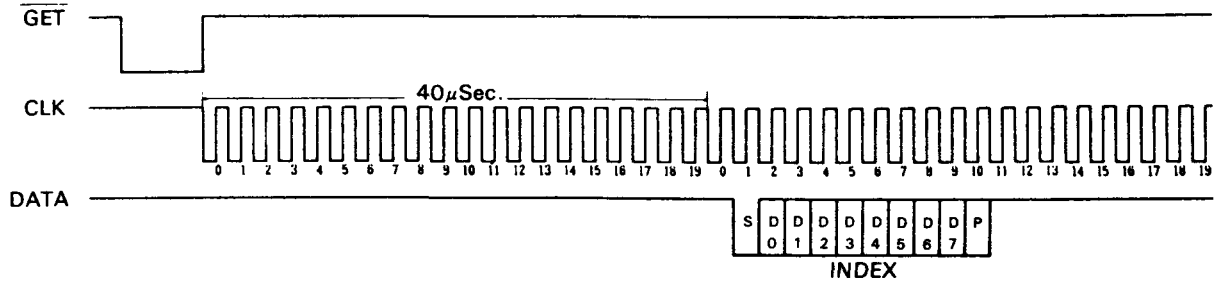
| Data Name | DO D1 D2 D3 D4 D5 D6 D7 | |
|----------------------|-------------------------|--|
| INDEX | —INDEX—0 1 EV | EV="1" when an event has occurred. EV="0" when no event has occurred. |
| Key-On Event 1/2 | —NOTE—BLOCK—1 | When a Key ON event occurs, key data and level data are sent as a pair. |
| Key-On Event 2/2 | —LEVEL— | NOTE(0,1,2,4,5,6,8,9,10,12,13,14) BLOCK(0~7) |
| Key-OFF Event | —NOTE—BLOCK—0 | |
| Special-ON Event | -Special- 0 0 0 0 1 | Special No.(1~7) |
| Special-OFF Event | -Special- 0 0 0 0 0 | |

3-6. Timing Controller

When an \overline{IC} pulse (minimum pulse width of 8mSec.) is received, the internal registers and timer are all cleared and the internal clock is woke up.

When a \overline{GET} pulse (minimum pulse width of 8mSec.) is received, changes in the Special Function switches are detected, and event data transmission is controlled.

3-7. Timing Chart of $\overline{\text{GET}}$, CLK, and DATA



●YM2020 PKD2

1. OVERVIEW

This LSI is an addressable PWM which uses 8-bit data to output pulses having the duty ratio set by data to the specified output pin. This PWM output permits the current flowing to the solenoids to be controlled, thereby allowing control of the piano's key touch.

2. FUNCTIONAL DESCRIPTION

- **IC (Initial Clear)**

This signal clears the internal circuitry when the power is turned ON. When IC is "0", the output (C0 to C4) all becomes "1".

- **CE (Chip Enable)**

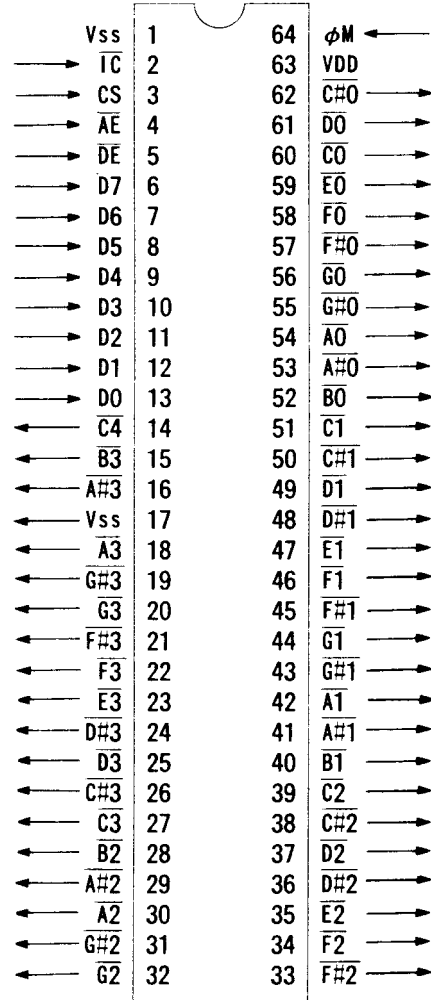
This signal is used to switch between C0~C4 and C4~C8. When CE is "0", C0~C4 (Address data 00~3E) are selected when CE is "1", C4~C8 (Address data 40~7E) are selected. During normal use, this terminal is connected to GND (for C0~C4) or Vdd (for C4~C8).

- **AE (Address Enable)**

This signal latches the key information (C # 0~C4) within the data. It enables the output pins (C # 0~C4) in accordance with the data (00~7E) during AE = "0" status.

| ADDRESS DATA | | OUTPUT |
|--------------|--------------------------------|-------------------|
| | D D D D D D D 6 5 4 3 2 1 0 | |
| 0 0 | 0 0 0 0 0 0 | $\overline{C\#0}$ |
| 0 1 | 0 0 0 0 0 1 | $\overline{D0}$ |
| 0 2 | 0 0 0 0 1 0 | $\overline{D\#0}$ |
| 0 4 | 0 0 0 1 0 0 | $\overline{E0}$ |
| 0 5 | 0 0 0 1 0 1 | $\overline{F0}$ |
| 0 6 | 0 0 0 1 1 0 | $\overline{F\#0}$ |
| 0 8 | 0 0 1 0 0 0 | $\overline{G0}$ |
| 0 9 | 0 0 1 0 0 1 | $\overline{G\#0}$ |
| 0 A | 0 0 1 0 1 0 | $\overline{A0}$ |
| 0 C | 0 0 1 1 0 0 | $\overline{A\#0}$ |
| 0 D | 0 0 1 1 0 1 | $\overline{B0}$ |
| 0 E | 0 0 1 1 1 0 | $\overline{C1}$ |
| 1 0 | 0 1 0 0 0 0 | $\overline{C\#1}$ |
| 1 1 | 0 1 0 0 0 1 | $\overline{D1}$ |
| 1 2 | 0 1 0 0 1 0 | $\overline{D\#1}$ |
| 1 4 | 0 1 0 1 0 0 | $\overline{E1}$ |
| 1 5 | 0 1 0 1 0 1 | $\overline{F1}$ |
| 1 6 | 0 1 0 1 1 0 | $\overline{F\#1}$ |
| 1 8 | 0 1 1 0 0 0 | $\overline{G1}$ |
| 1 9 | 0 1 1 0 0 1 | $\overline{G\#1}$ |
| 1 A | 0 1 1 0 1 0 | $\overline{A1}$ |
| 1 C | 0 1 1 1 0 0 | $\overline{A\#1}$ |
| 1 D | 0 1 1 1 0 1 | $\overline{B1}$ |
| 1 E | 0 1 1 1 1 0 | $\overline{C2}$ |
| 2 0 | 1 0 0 0 0 0 | $\overline{C\#2}$ |
| 2 1 | 1 0 0 0 0 1 | $\overline{D2}$ |
| 2 2 | 1 0 0 0 1 0 | $\overline{D\#2}$ |
| 2 4 | 1 0 0 1 0 0 | $\overline{E2}$ |
| 2 5 | 1 0 0 1 0 1 | $\overline{F2}$ |
| 2 6 | 1 0 0 1 1 0 | $\overline{F\#2}$ |
| 2 8 | 1 0 1 0 0 0 | $\overline{G2}$ |
| 2 9 | 1 0 1 0 0 1 | $\overline{G\#2}$ |
| 2 A | 1 0 1 0 1 0 | $\overline{A2}$ |
| 2 C | 1 0 1 1 0 0 | $\overline{A\#2}$ |
| 2 D | 1 0 1 1 0 1 | $\overline{B2}$ |
| 2 E | 1 0 1 1 1 0 | $\overline{C3}$ |
| 3 0 | 1 1 0 0 0 0 | $\overline{C\#3}$ |
| 3 1 | 1 1 0 0 0 1 | $\overline{D3}$ |
| 3 2 | 1 1 0 0 1 0 | $\overline{D\#3}$ |
| 3 4 | 1 1 0 1 0 0 | $\overline{E3}$ |
| 3 5 | 1 1 0 1 0 1 | $\overline{F3}$ |
| 3 6 | 1 1 0 1 1 0 | $\overline{F\#3}$ |
| 3 8 | 1 1 1 0 0 0 | $\overline{G3}$ |
| 3 9 | 1 1 1 0 0 1 | $\overline{G\#3}$ |
| 3 A | 1 1 1 0 1 0 | $\overline{A3}$ |
| 3 C | 1 1 1 1 0 0 | $\overline{A\#3}$ |
| 3 D | 1 1 1 1 0 1 | $\overline{B3}$ |
| 3 E | 1 1 1 1 1 0 | $\overline{C4}$ |

ADDRESS DATA TABLE



YM2020 Pin assignment

When CE is "0", output is enabled when D6 is "0".
 When CE is "1", output is enabled when D6 is "1".

• **DE (Touch Data Enable)**

This signal latches the latch information within the data.

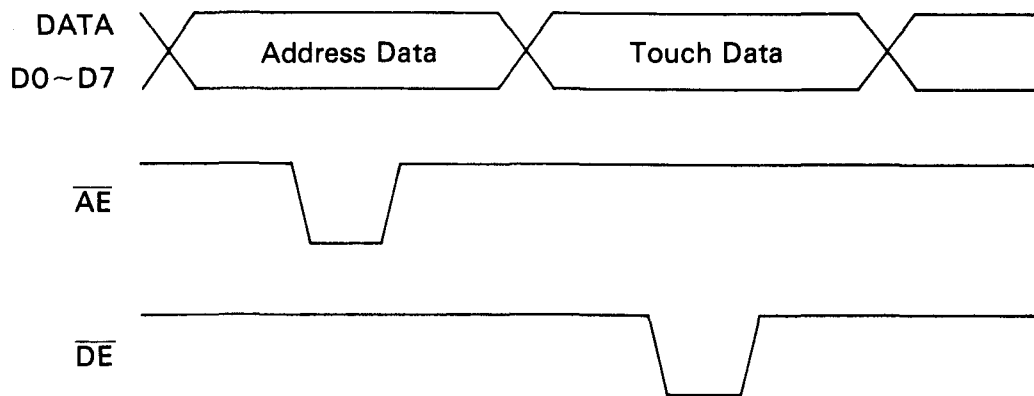
It modulates the pulse width of the output according to the data (00~7E) during DE = "0" status. D7 represents the Key ON/OFF data.

ADDRESS DATA TABLE

| | TOUCH DATA | | | | | | | | PULSE WIDTH | | KEY TOUCH |
|-----|------------|---|---|---|---|---|---|-------|-------------|---------------|--------------------------|
| | D | D | D | D | D | D | D | D | "1" | "0" | |
| | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 | | | |
| 8 0 | 1 | * | * | * | * | * | * | * | 1 2 8 | 0 | Key OFF |
| F F | | | | | | | | | | | |
| 7 F | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 2 7 | 1 | Min. pressure |
| 7 E | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 1 2 6 | 2 | 126 levels in between |
| 7 D | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 2 5 | 3 | | |
| 7 B | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 1 2 3 | 5 | | |
| 7 7 | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 1 1 9 | 9 | | |
| 6 F | 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 0 1 | 1 7 | | |
| 5 F | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 9 5 | 3 3 | | |
| 3 F | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 6 3 | 6 5 | | |
| 0 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 2 8 | Max. pressure | |

• **D0~D7**

These signals are latched by the previously-mentioned AE and DE signals, and used as Key data and Touch data. The relationship of D0~D7 with AE and DE is shown in Fig. 2 below.



CPU Circuit Board

CN13

| Pin No. | Pin Name |
|---------|------------|
| 7 | - |
| 6 | Foot SW |
| 5 | MIDI Pin 5 |
| 4 | MIDI Pin 4 |
| 3 | GND |
| 2 | MIDI Pin 5 |
| 1 | MIDI Pin 4 |

CN16

| Pin No. | Pin Name |
|---------|----------|
| 7 | LED6 |
| 6 | LED5 |
| 4 | LED3 |
| 3 | LED2 |
| 2 | LED1 |
| 1 | +5V |

CN4

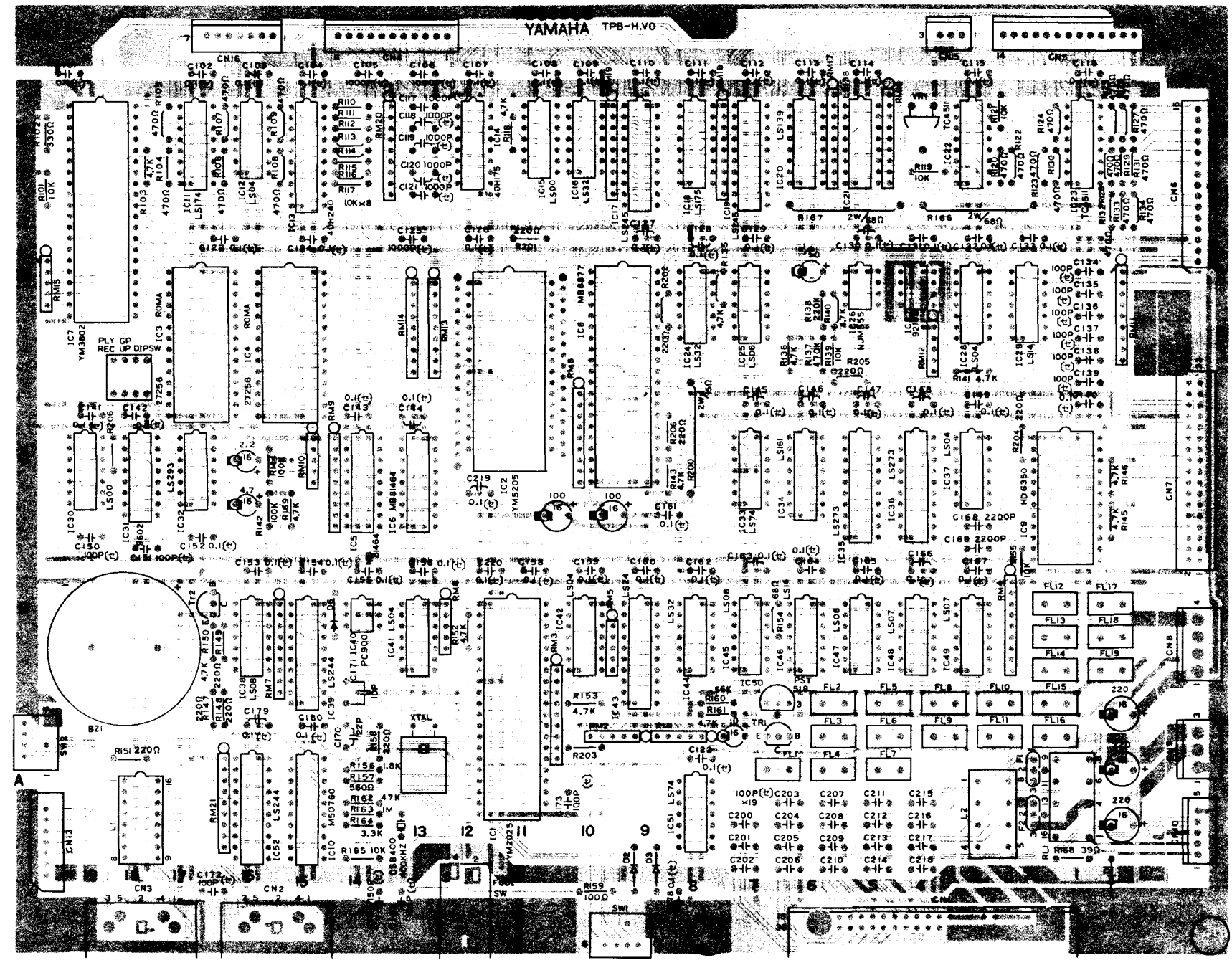
| Pin No. | Pin Name |
|---------|---------------|
| 11 | SW10/SW18/SW8 |
| 10 | SW9/SW17 |
| 9 | SW7/SW13 |
| 8 | SW6/SW12 |
| 6 | SW4/SW14 |
| 5 | SW3/SW15 |
| 4 | SW2/SW11 |
| 3 | BT |
| 2 | B2 |
| 1 | B3 |

CN5

| Pin No. | Pin Name |
|---------|----------|
| 3 | GND |
| 2 | +12V |
| 1 | - |

CN6

| Pin No. | Pin Name |
|---------|----------|
| 14 | D7 |
| 13 | D6 |
| 12 | D5 |
| 11 | D4 |
| 10 | D3 |
| 9 | D2 |
| 8 | D1 |
| 7 | E |
| 6 | E |
| 5 | E |
| 4 | R5 |
| 3 | CSS |
| 2 | VCC |
| 1 | GND |



CN6

| Pin No. | Pin Name |
|---------|----------|
| 15 | A2 |
| 14 | D2 |
| 13 | D2 |
| 12 | D2 |
| 11 | E2 |
| 10 | F2 |
| 9 | G2 |
| 8 | COM |
| 7 | A1 |
| 6 | B1 |
| 5 | C1 |
| 4 | D1 |
| 3 | E1 |
| 2 | F1 |
| 1 | G1 |

CN7

| Pin No. | Pin Name | Pin No. | Pin Name |
|---------|----------|---------|-----------|
| 34 | READY | 17 | GND |
| 33 | GND | 16 | MOTOR |
| 32 | STOSEL | 15 | GND |
| 31 | GND | 14 | - |
| 30 | RD | 13 | - |
| 29 | GND | 12 | - |
| 28 | WRT | 11 | GND |
| 27 | GND | 10 | DRIVESEL0 |
| 26 | TR00 | 9 | GND |
| 25 | GND | 8 | INDEX |
| 24 | WTGAT | 7 | GND |
| 23 | GND | 6 | GND |
| 22 | WTDAT | 5 | - |
| 21 | GND | 4 | - |
| 20 | STEP | 3 | GND |
| 19 | GND | 2 | DTSKIN |
| 18 | DIRC | 1 | - |

CN8

| Pin No. | Pin Name |
|---------|----------|
| 4 | +5V |
| 3 | GND |
| 2 | GND |
| 1 | +12V |

CN9

| Pin No. | Pin Name |
|---------|----------|
| 3 | +5V |
| 2 | +5V |
| 1 | GND |

CN10

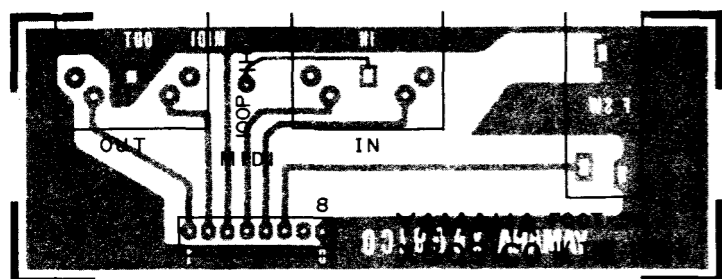
| Pin No. | Pin Name |
|---------|----------|
| 5 | GND |
| 4 | S1 |
| 3 | +5V |
| 2 | P2 |
| 1 | P1 |

CN11

| Pin No. | Pin Name | Pin No. | Pin Name |
|---------|----------|---------|----------|
| 34 | P07 | 17 | IE |
| 33 | P06 | 16 | IE |
| 32 | P05 | 15 | LEDOR |
| 31 | P04 | 14 | DATA |
| 30 | P03 | 13 | CLK |
| 29 | P02 | 12 | GND |
| 28 | P01 | 11 | DET |
| 27 | TR00 | 10 | TR00 |
| 26 | TR01 | 9 | TR01 |
| 25 | TR02 | 8 | TR02 |
| 24 | GND | 7 | +12V |
| 23 | GND | 6 | +12V |
| 22 | GND | 5 | +12V |
| 21 | GND | 4 | +12V |
| 20 | GND | 3 | +5V |
| 19 | GND | 2 | +5V |
| 18 | GND | 1 | +5V |

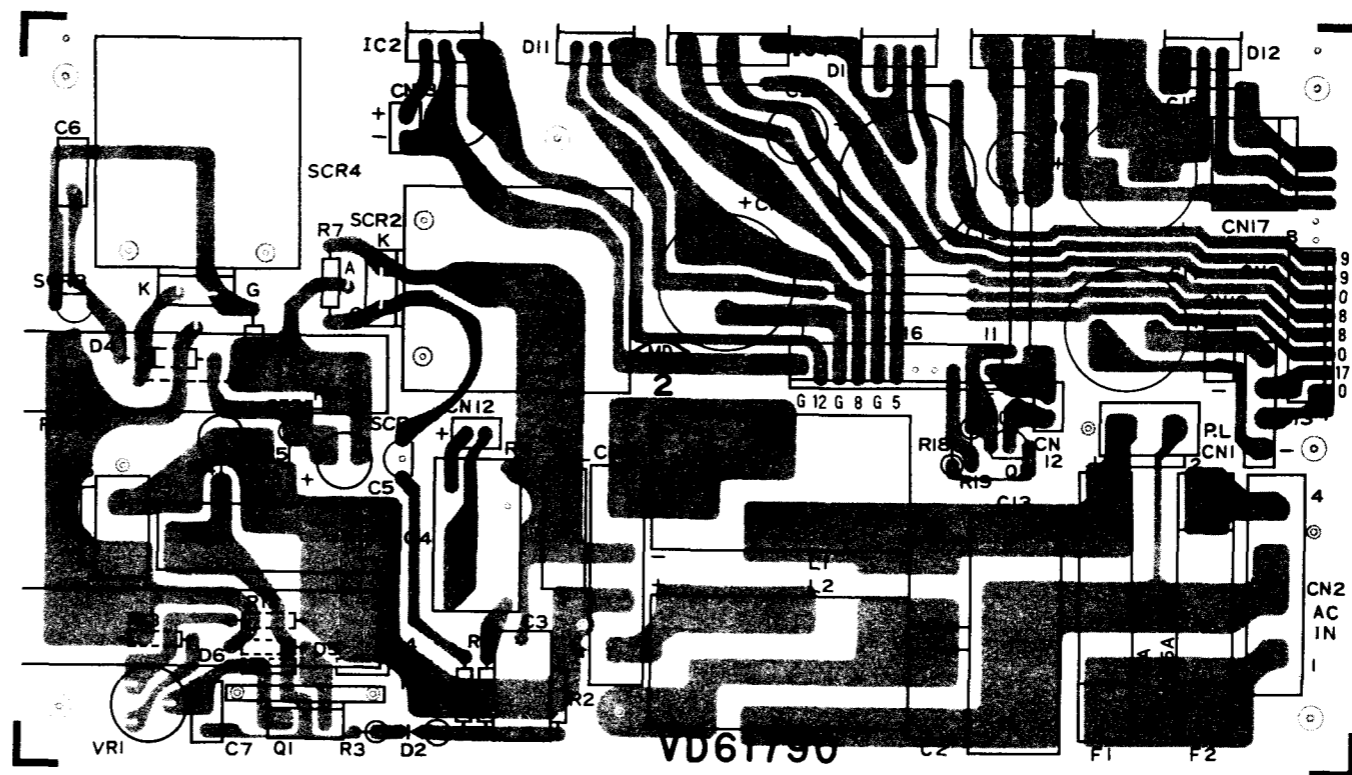
■ Drive Unit, MIDI and Power Supply Circuit Board

● MIDI External Circuit Board



| Pin No. | Pin Name |
|---------|-----------------|
| 1 | MIDI OUT Pin #4 |
| 2 | MIDI OUT Pin #5 |
| 3 | GND |
| 4 | MIDI IN Pin #4 |
| 5 | MIDI IN Pin #5 |
| 6 | FOOT SW Pin #2 |
| 7 | GND |
| 8 | GND |

● Power Supply Circuit Board



CN3

| Pin No. | Pin Name |
|---------|-------------|
| 8 | Transformer |
| 7 | Transformer |
| 6 | Transformer |
| 5 | Transformer |
| 4 | Transformer |
| 3 | Transformer |
| 2 | Transformer |
| 1 | Transformer |

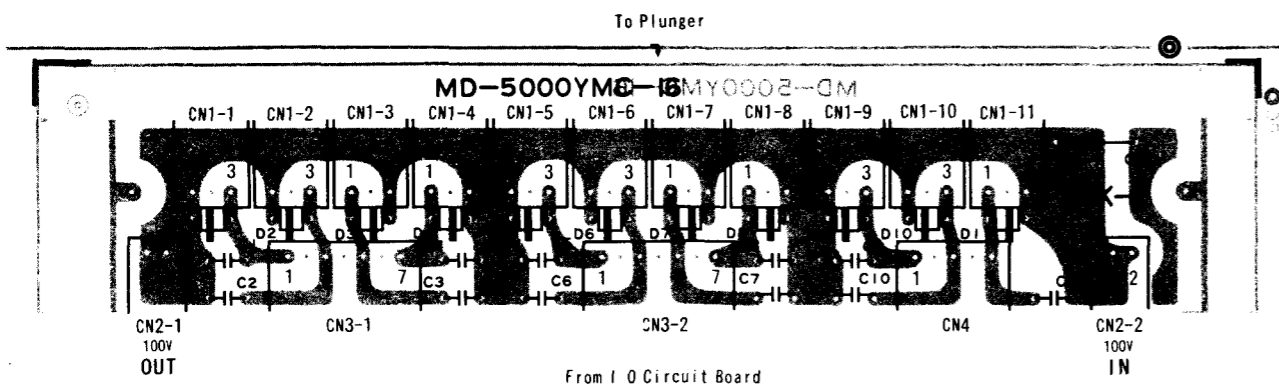
CN2

| Pin No. | Pin Name |
|---------|-------------|
| 4 | Transformer |
| 3 | Transformer |
| 2 | AC IN |
| 1 | AC IN |

CN1

| Pin No. | Pin Name |
|---------|----------|
| 1 | P.L + |
| 2 | P.L - |

● Drive Circuit Board



YAMAHA

disklavier

PARTS LIST

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1.ElectricalParts

| Ref. No. | Part No. | Description | 部 品 名 | Remarks | |
|----------|----------|---------------------------|--------------|-----------|----------------------|
| | | Control Unit | | | |
| | IG136200 | IC | SI-3052V | IC | 2A 5V Regulator |
| | IF000040 | Diode | 1S1555 | ダイオード | |
| | IF008730 | LED | SLR-34URC3H3 | LED | Red |
| | IF008740 | LED | SLR-34MC3H3 | LED | Green |
| * | VB352400 | LCD | | LCD | |
| * | JX550050 | LED 7Seg. Indicator | GL-6P206U | 7セグメント表示器 | |
| * | KX550100 | Power Switch | | パワースイッチ | |
| * | KX550640 | Micro Switch | | MT スイッチ | |
| * | JX550060 | Sensor, Remote Controller | | リモコン受光器 | |
| | | CPU Circuit Board | | | |
| * | VE470300 | CPU Circuit Board | | CPU シート | |
| | IT200200 | IC | YM2025 | IC | CPU |
| | IT520520 | IC | YM5205 | IC | PMAC |
| | XA591001 | IC | YM3802 | IC | MCS |
| * | XA457001 | IC | MB81464-12 | IC | RAM (64k×4bit) |
| | iG132700 | IC | HD6350 | IC | ACIA |
| | IG057300 | IC | MB8877 | IC | FDC |
| | iG137800 | IC | FDC9216B | IC | FD DATA SEPARATER |
| | XA513002 | IC | M50760-463 | IC | REMOTE CONT. DECODER |
| | iG067210 | IC | TC4511BP | IC | BCD to 7 LATCH |
| | iG068100 | IC | TC40H240 | IC | OCT. BUFF. INVERTER |
| | iG096700 | IC | TC40H175 | IC | QUAD D-F.F. |
| | iG057400 | IC | F9602 | IC | MULTI VIBRATOR |
| | iG044200 | IC | LS138 | IC | 3 to 8 DECODER |
| | iG049900 | IC | LS139 | IC | DUAL 2 to 4 DECODER |
| | iG060000 | IC | LS244 | IC | OCT. BUS DRIVER |
| | iG044600 | IC | LS245 | IC | OCT. TRANS |
| | iG050100 | IC | LS175 | IC | QUAD D-F.F. |
| | iG044000 | IC | LS74 | IC | EDGE TRIGGER |
| | iG060200 | IC | LS273 | IC | OCT D-F.F. |
| | iG050300 | IC | LS293 | IC | 4 BIT COUNT. |
| | iG044400 | IC | LS161 | IC | 4 BIT COUNT. |
| | iG049600 | IC | LS14 | IC | HEX SCHMITT TRIGG. |
| | iG049800 | IC | LS32 | IC | QUAD 2 INPUT NOR |
| | iG043700 | IC | LS08 | IC | QUAD 2 INPUT AND |
| | iG027010 | IC | LS04 | IC | HEX INVERTER |
| | iG026910 | IC | LS00 | IC | QUAD 2 INPUT NAND |
| | iG050000 | IC | LS174 | IC | HEX D-F.F. |
| | iG059100 | IC | SN74LS06N | IC | HEX INV/BUFF |
| | iG152700 | IC | MB74LS07P | IC | HEX BUFFER |
| | iG044500 | IC | LS240 | IC | OCT. BUFFER |
| | iG116200 | IC | PST518 | IC | Power Amp. |
| | iG063500 | IC | NJM555 | IC | TIMER |
| * | XD775000 | IC | HN27256G-25 | IC | ROMA |
| * | XD776000 | IC | MB27128-25G | IC | ROMB |
| | iK000420 | Photo coupler | PC900 | フォトカプラー | |
| | HF859100 | Carbon resistor | 1M 1 6W | カーボン抵抗 | |
| | HL324150 | Metal Oxide Resistor | 15Ω2W | 酸化金抵抗 | |
| | HL324680 | Metal Oxide Resistor | 65Ω2W | 酸化金抵抗 | |
| * | VB350500 | Resistor Array | RM8-470Ω | 抵抗モジュール | |
| * | VB350600 | Resistor Array | RM8-1kΩ | 抵抗モジュール | |

* : New Parts (新規部品)

1. Electrical Parts

| Ref. No. | Part No. | Description | 部 品 名 | Remarks |
|----------|----------|-----------------------------|-------------|---------------------|
| | HZ003350 | Resistor Array | RM4-4.7kΩ | 抵 抗 モ ジ ュ ー ル |
| | HZ002940 | Resistor Array | RM8-4.7kΩ | 抵 抗 モ ジ ュ ー ル |
| | HZ003370 | Resistor Array | RM4-10kΩ | 抵 抗 モ ジ ュ ー ル |
| | HZ003380 | Resistor Array | RM8-10kΩ | 抵 抗 モ ジ ュ ー ル |
| | FZ004100 | Semi-Conductive Cera. Cap. | 0.1 16V | 半 導 体 セ ラ コ ン |
| | FP336220 | Tantalum Cap. | 2.2μ16V | タ ン タ ル コ ン |
| | FP336470 | Tantalum Cap. | 4.7μ16V | タ ン タ ル コ ン |
| | iC181520 | Transistor | 2SC1815Y | ト ラ ン ジ ス タ |
| | VA106500 | Diode | 1SS176 | ダ イ オ ー ド |
| | HT370010 | Semi-Variable Resistor | B1K | 半 固 定 ポ リ ュ ー ム |
| | KA000240 | Switch, DIP | KTD04 | DIP ス イ ッ チ |
| | QU001400 | Quartz Crystal | 8MHz | 水 晶 発 振 子 |
| * | QU003500 | Ceramic Resonator | CSB400MT | セ ラ ロ ッ ク |
| | FZ006970 | EMI Filter | 0.022μF | エ ミ フ ィ ル |
| * | VB353000 | Buzzer | PKB9-2A0 | 圧 電 ブ ザ ー |
| | VB835000 | Coil | 5P200 | FL コ イ ル |
| * | VB293400 | Relay | G6A-274 | リ レ ー |
| | KA501760 | Rotary Switch | | ロ ー タ リ ー ス イ ッ チ |
| | LB604730 | Socket | IC30-2806G4 | IC ソ ケ ッ ト |
| | LB021030 | Connector | 3P | コ ネ ク タ |
| | LB021050 | Connector | 5P | コ ネ ク タ |
| | LB021070 | Connector | 7P | コ ネ ク タ |
| | LB916070 | Connector | 7P | コ ネ ク タ |
| | LB021110 | Connector | 11P | コ ネ ク タ |
| | LB021140 | Connector | 14P | コ ネ ク タ |
| | LB021150 | Connector | 15P | コ ネ ク タ |
| | LB604400 | Connector | 34P | コ ネ ク タ |
| | VB316200 | Connector | 34P | PS コ ネ ク タ |
| | LB932030 | Connector | B3P-VH | VH コ ネ ク タ |
| | LB932040 | Connector | B4P-VH | VH コ ネ ク タ |
| | | | | |
| | | MIDI External Circuit Board | | |
| | LB202930 | Phone Jack | | ホ ー ン ジャ ッ ク |
| | LB500520 | DIN Connector | 5P | DIN コ ネ ク タ |
| | LB917080 | Connector | 8P | コ ネ ク タ |
| | | | | |
| | | I/O Circuit Board | | |
| * | VE470200 | I/O Board | | I / O シ ー ト |
| * | iT202000 | IC | YM2020 | IC DIGITAL PWM |
| | iG027010 | IC | HD74LS04P | IC Hex Inverter |
| | iG043700 | IC | HD74LS08P | IC Quad 2 Input AND |
| | iG049800 | IC | HD74LS32 | IC Quad 2 Input NOR |
| | iG036100 | IC | NJM2901 | IC Quad Comparator |
| | iG057400 | IC | F9602 | IC Multi VIBRATOR |
| | iC181520 | Transistor | 2SC1815 | ト ラ ン ジ ス タ |
| | iD102300 | Transistor | 2SD1023 | ト ラ ン ジ ス タ |
| * | VB293700 | Transistor Array | TH3L20 | ト ラ ン ジ ス タ ア レ イ |
| | HZ002940 | Resistor Array | RM8-472 | 抵 抗 ア レ イ |
| * | VB307500 | Resistor Array | RM8-104 | 抵 抗 ア レ イ |
| * | VB307600 | Resistor Array | RM8-105 | 抵 抗 ア レ イ |
| | VA106500 | Diode | 1SS176 | ダ イ オ ー ド |
| * | VB295700 | Diode Array | DAP-801 | ダ イ オ ー ド ア レ イ |

* : New Parts (新規部品)

1.ElectricalParts

| Ref. No. | Part No. | Description | 部 品 名 | Remarks |
|----------|-----------|----------------------------|----------------------|-----------------|
| | iF001840 | Zener Diode | RD3.0EB | ツェナーダイオード |
| * | iF001220 | First Recovery Diode | S2K20 | ダイオード |
| | FZ004100 | Semi-Conductive Cera. Cap. | 0.1 μ F 16V | 半導体セラコン |
| | FP337100 | Tantalum Cera. Cap. | 10 μ F 16V | タンタルコン |
| | FZ000680 | MM Cap. | 0.1 μ F 250V | MM コン |
| | iF003940 | LED | GL-5PR2 | LED |
| * | VB293300 | Ceramic Resonator | CSA4.0MG11 | セラロック |
| * | VB293400 | Relay | G6A-274P DC5V | リレー |
| | iK000420 | Photo Coupler | PC900 | フォトカブラ |
| | LB932020 | Connector | B2P-VH | コネクタ |
| | LB932030 | Connector | B3P-VH | コネクタ |
| | LB932040 | Connector | B4P-VH | コネクタ |
| | LB932050 | Connector | B5P-VH | コネクタ |
| | LB932060 | Connector | B6P-VH | コネクタ |
| | LB916030 | Connector | 3P | コネクタ |
| | LB916090 | Connector | 9P | コネクタ |
| | LB916100 | Connector | 10P | コネクタ |
| | LB916110 | Connector | 11P | コネクタ |
| | LB916150 | Connector | 15P | コネクタ |
| | | | | |
| * | VE470400 | Sensor Circuit Board (A) | | センサーシート (A) |
| | iT201900 | IC | YM2019 | IC KAPP |
| | iG027010 | IC | HD74LS04 | IC HEX INVERTER |
| | iG043700 | IC | HD74LS08 | IC QUAD 2IN AND |
| | iG052800 | IC | TC40H032 | IC QUAD 2IN OR |
| | HZ004700 | Resistor Array | RM12-4.7k Ω | 抵抗アレイ |
| | HL313470 | Metal Oxide Resistor | 4.7 Ω 1/2W | 酸金抵抗 |
| | HL314120 | Metal Oxide Resistor | 12 Ω 1W | 酸金抵抗 |
| | HL315470 | Metal Oxide Resistor | 470 Ω 1W | 酸金抵抗 |
| | FZ413680 | Ceramic Cap. | 6800PF 50V | セラコン |
| | FZ005030 | Semi-conductive Cera. Cap. | 0.1 μ F 25V | 半導体セラコン |
| | iC134540 | Transistor | 2SC1345(D,E) | トランジスタ |
| | iB059630 | Transistor | 2SB596(O,Y) | トランジスタ |
| | iG127300 | Transistor Array | TD62003P | トランジスタアレイ |
| | iF000040 | Diode | 1S1555 | ダイオード |
| | iF001840 | Zener Diode | RD-3.0EB | ツェナーダイオード |
| | VB434900 | Zener Diode | RD-2.2EB1 | ツェナーダイオード |
| | FZ006970 | EMI Filter | DSS310 0.022 μ F | エミフィル |
| | VB293300 | Ceramic Resonator | CSA4.0MG11 | セラロック |
| | VB835000 | Coil | 5R200 | FL コイル |
| | VE472300 | Fuse | 160mA UL | ヒューズ |
| | LB918040 | Connector | B4B-XH-A | コネクタ |
| | L18918090 | Connector | B9B-XH-A | コネクタ |
| | LB918110 | Connector | B11B-XH-A | コネクタ |
| | LB918120 | Connector | B12B-XH-A | コネクタ |
| | LB918130 | Connector | B13B-XH-A | コネクタ |
| | LB918140 | Connector | B14B-XH-A | コネクタ |
| | | | | |
| * | VE470900 | Hammer LED Drive Board | | ハンマー発光シート |
| | VB434100 | LED | H-1000 | 発光ダイオード |
| | CB502580 | Socket | | 発光ブロック |

* : New Parts (新規部品)

1. Electrical Parts

| Ref. No. | Part No. | Description | 部 品 名 | Remarks |
|----------|----------|-----------------------------|-----------|--------------------|
| | LB918130 | Connector | B13B-XH-A | コ ネ ク タ |
| | LB918140 | Connector | B14B-XH-A | コ ネ ク タ |
| * | VE471200 | M1,K Detect Board | | M1,K 受 光 シ ー ト |
| | iG036100 | IC | NJM2901 | IC QUAD COMPARATOR |
| | VB434400 | Photo Diode | S2506-02 | フ ォ ト ダ イ オ ー ド |
| | VB433900 | Resistor Array | RM8-333 | 抵 抗 ア レ イ |
| | H2003190 | Resistor Array | RM8-472 | 抵 抗 ア レ イ |
| | HV753470 | Frame Proof Carbon Resistor | 4.7Ω 4W | 不 燃 化 カ ー ボ ン 抵 抗 |
| | FZ005030 | Semi-Conductive Cera. Cap. | 0.1μF 25V | 半 導 体 セ ラ コ ン |
| | CB502590 | Socket | | 受 光 プ ロ ッ ク |
| | LB918090 | Connector | B9B-XH-A | コ ネ ク タ |
| | LB918110 | Connector | B11B-XH-A | コ ネ ク タ |
| * | VE471300 | M2 Detect Board | | M2 受 光 シ ー ト |
| | iG036100 | IC | NJM2901 | IC QUAD COMPARATOR |
| | VB434400 | Photo Diode | S2506-02 | フ ォ ト ダ イ オ ー ド |
| | VB433900 | Resistor Array | RM8-333 | 抵 抗 ア レ イ |
| | HZ003190 | Resistor Array | RM8-472 | 抵 抗 ア レ イ |
| | HV753470 | Flame Proof Carbon Resistor | 4.7Ω 4W | 不 燃 化 カ ー ボ ン 抵 抗 |
| | FZ005030 | Semi-Conductive Cera. Cap. | 0.1μF 25V | 半 導 体 セ ラ コ ン |
| * | CB502590 | Socket | | 受 光 プ ロ ッ ク |
| | LB918090 | Connector | B9B-XH-A | コ ネ ク タ |
| | LB918110 | Connector | B11B-XH-A | コ ネ ク タ |
| | VE471400 | Key LED Drive Board | | キ ー 発 光 シ ー ト |
| | VB434100 | LED | H-1000 | LED |
| | CB502580 | Socket | | 発 光 プ ロ ッ ク |
| | LB918060 | Connector | B6B-XH-A | コ ネ ク タ |
| | LB918070 | Connector | B7B-XH-A | コ ネ ク タ |
| | | Pedal Sensor Unit | | ペダルセンサーユニット |
| | NB137040 | PK Switch | (1T) | PK ス イ ッ チ |
| | | Power Supply Unit | | |
| * | VD617900 | Power Supply Unit | | 電 源 ユ ニ ッ ト |
| | IG083400 | IC | AN7812 | IC Regulator |
| | IG094300 | IC | SI3052V | IC Regulator |
| | IC181520 | Transistor | 2SC1815 | ト ラ ン ジ ス タ |
| | IC223840 | Transistor | 2SC2238B | ト ラ ン ジ ス タ |
| | 1X603810 | Diode | 10E-4 | ダ イ オ ー ド |
| * | IX554290 | Diode | 1S1585 | ダ イ オ ー ド |
| | IF000040 | Diode | 1S1555 | ダ イ オ ー ド |
| * | IX554300 | Diode | C10P10F | ダ イ オ ー ド |
| * | IX800830 | Diode | S1VB20 | ダ イ オ ー ド |
| * | IX554310 | Diode | BS08A | ダ イ オ ー ド |
| * | IH001050 | Diode | AC16DGM | サ イ リ ス タ |
| * | IH000420 | Diode | CRO2AM4 | サ イ リ ス タ |
| * | IX554440 | Diode Bridge | KBPC2504 | ダ イ オ ー ド ブ リ ッ ジ |

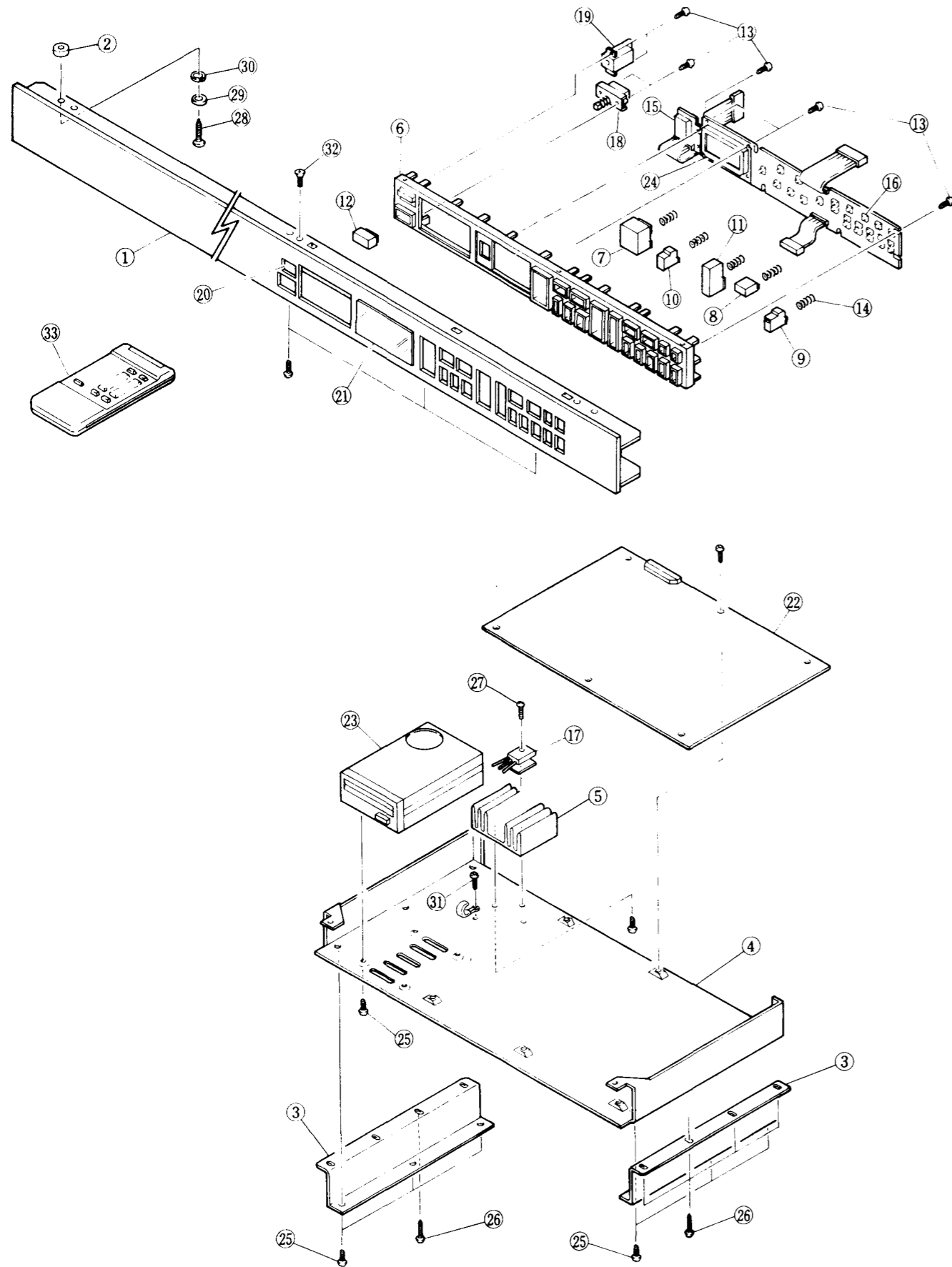
* : New Parts (新規部品)

1. Electrical Parts

| Ref. No. | Part No. | Description | 部 品 名 | Remarks |
|----------|----------|------------------------|-------------|-----------------------|
| | IF001470 | Zener Diode | RD6.2EB2 | ツェナーダイオード |
| * | FX551060 | Ceramic Cap. | 2200PF 250V | セラコン |
| * | FX800230 | Ceramic Cap. | 100PF 1KV | セラコン |
| * | FX551560 | MM Cap. | 0.15μF 250V | MM コ ン |
| | FZ005250 | MM Cap. | 0.22μF 125V | MM コ ン |
| * | FX551570 | MM Cap. | 2.7μF 250V | MM コ ン |
| | FC244100 | MM Cap. | 0.01μF 125V | MM コ ン |
| | HL327220 | Metal Oxide Resistor | 22KΩ2W | 酸 金 抵 抗 |
| | HL327100 | Metal Oxide Resistor | 10KΩ2W | 酸 金 抵 抗 |
| | HM574120 | Cement Mold Resistor | 12Ω10W | セメント抵抗 |
| * | HX551720 | Cement Mold Resistor | 4.7KΩ10W | セメント抵抗 |
| * | KX550680 | Relay | AW62199 | リ レ ー |
| * | HX551730 | Semi-Variable Resistor | B-10K | 半固定ボリューム |
| * | KX550630 | Switch | SDA3S-A-1 | シーソースイッチ |
| * | KX550690 | Fuse | 7A 250V | ヒ ュ ー ズ |
| | KB002740 | Fuse | 1.5A 250V | ヒ ュ ー ズ |
| * | JX550080 | Pilot Lamp | 108-RN 120V | パイロットランプ |
| * | GX550630 | Coil | | チ ョ ー ク コ イ ル |
| * | GX550670 | Transformer | N36-506 | ト ラ ン ス |
| | MG000820 | AC Cable | | 電 源 コ ー ド |
| | | Key Drive Unit | | |
| * | BX550110 | Plunger Ass'y (A) | | プランジャー Ass'y(A) Front |
| * | BX550120 | Plunger Ass'y (B) | | プランジャー Ass'y(B) Rear |
| * | IX554330 | Thermostat | UP62 100°C | サーモスタット |
| * | IX554340 | Diode | DIK20 | ダ イ オ ー ド |

* : New Parts (新規部品)

2. Control Unit

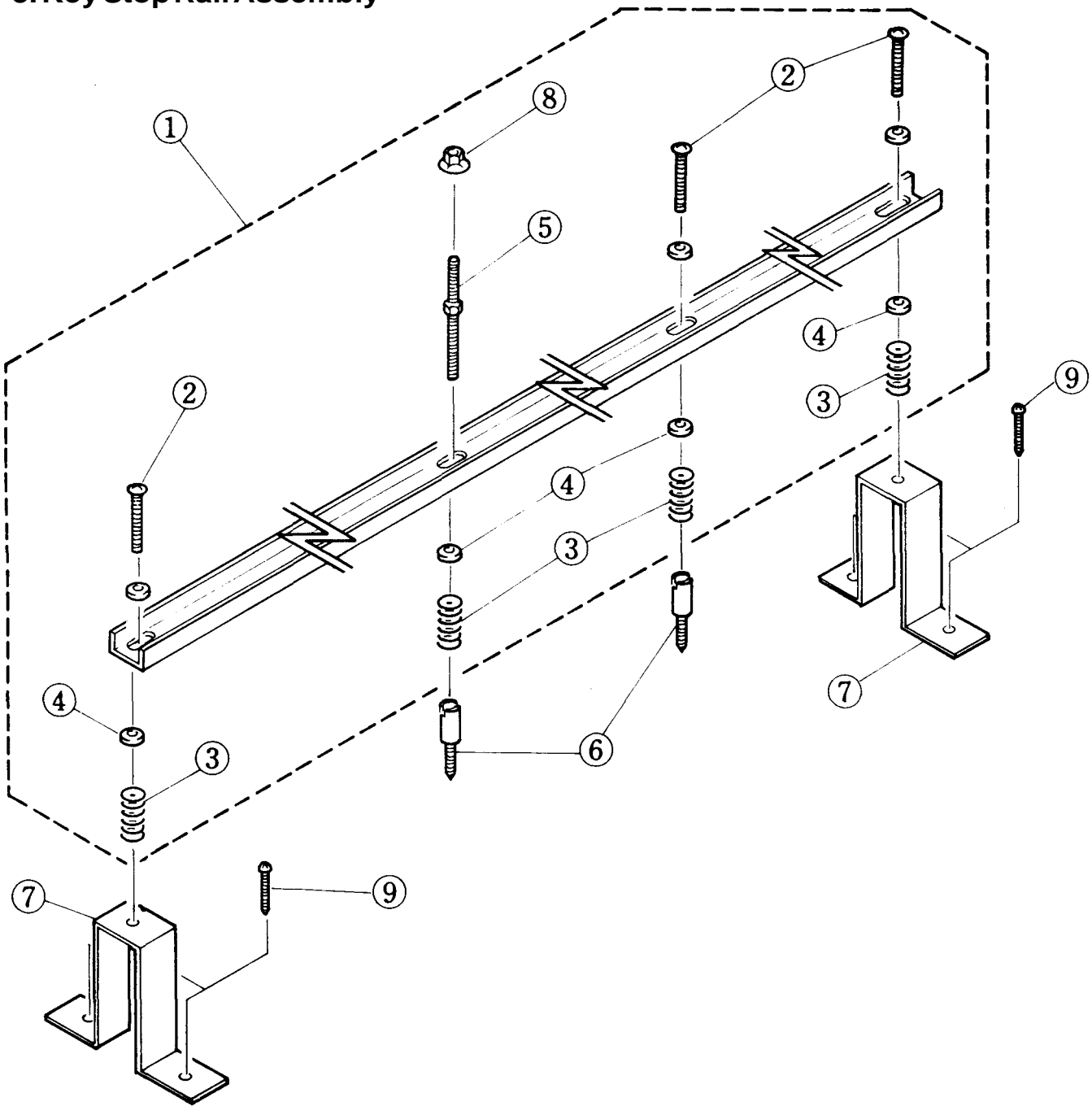


2. Control Unit

| Ref. No. | Part No. | Description | 部 品 名 | Remarks |
|----------|----------|---------------------------|---------------------------|-----------------------|
| * 1 | VE513200 | Front Panel | フ ロ ン ト パ ネ ル | BL |
| * 1 | VF593400 | Front Panel | フ ロ ン ト パ ネ ル | WH |
| * 1 | VF610900 | Front Panel | フ ロ ン ト パ ネ ル | WN |
| * 2 | VF156400 | Spacer | コ ン ト ロ ー ラ ー 取 付 ス ペ ー サ | |
| * 3 | AA503110 | Holder, Controller | コ ン ト ロ ー ル 取 付 金 具 | |
| * 4 | VE513700 | Bottom Cover | コ ン ト ロ ー ル ボ ト ム | |
| * 5 | VB496400 | Heat Sink | 放 熱 板 | |
| * 6 | CX558110 | Sub-panel | ハ ウ ジ ン グ | BL, WN |
| * 6 | CX558120 | Sub-panel | ハ ウ ジ ン グ | WH |
| * 7 | CX557810 | Button, Switch | キ ー ト ッ プ (A) | BL, WN START/PAUSE |
| * 7 | CX557820 | Button, Switch | キ ー ト ッ プ (A) | WH START/PAUSE |
| * 7 | CX557830 | Button, Switch | キ ー ト ッ プ (A) | BL, WN STOP |
| * 7 | CX557840 | Button, Switch | キ ー ト ッ プ (A) | WH STOP |
| * 8 | CX557850 | Button, Switch | キ ー ト ッ プ (C) | BL, WN REW |
| * 8 | CX557860 | Button, Switch | キ ー ト ッ プ (C) | WH REW |
| * 8 | CX557870 | Button, Switch | キ ー ト ッ プ (C) | BL, WN FF |
| * 8 | CX557880 | Button, Switch | キ ー ト ッ プ (C) | WH FF |
| * 8 | CX557890 | Button, Switch | キ ー ト ッ プ (C) | BL, WN - |
| * 8 | CX557900 | Button, Switch | キ ー ト ッ プ (C) | WH - |
| * 8 | CX557910 | Button, Switch | キ ー ト ッ プ (C) | BL, WN + |
| * 8 | CX557920 | Button, Switch | キ ー ト ッ プ (C) | WH + |
| * 9 | CX557930 | Button, Switch | キ ー ト ッ プ (F) | BL, WN Vol, Tempo |
| * 9 | CX557940 | Button, Switch | キ ー ト ッ プ (F) | WH Vol, Tempo |
| * 10 | CX558420 | Button, Switch | キ ー ト ッ プ (D) | BL, WN All Repeat |
| * 10 | CX558450 | Button, Switch | キ ー ト ッ プ (D) | WH All Repeat |
| * 10 | CX558430 | Button, Switch | キ ー ト ッ プ (D) | BL, WN 1 Repeat |
| * 10 | CX558460 | Button, Switch | キ ー ト ッ プ (D) | WH 1 Repeat |
| * 10 | CX558440 | Button, Switch | キ ー ト ッ プ (D) | BL, WN A・B Repeat |
| * 10 | CX558470 | Button, Switch | キ ー ト ッ プ (D) | WH A・B Repeat |
| * 10 | CX558010 | Button, Switch | キ ー ト ッ プ (D) | BL, WN L |
| * 10 | CX558020 | Button, Switch | キ ー ト ッ プ (D) | WH L |
| * 10 | CX558030 | Button, Switch | キ ー ト ッ プ (D) | BL, WN R |
| * 10 | CX558040 | Button, Switch | キ ー ト ッ プ (D) | WH R |
| * 11 | CX558050 | Button, Switch | キ ー ト ッ プ (B) | BL, WN REC. |
| * 11 | CX558060 | Button, Switch | キ ー ト ッ プ (B) | WH REC. |
| * 12 | CX558070 | Button, Switch | キ ー ト ッ プ (E) | BL, WN Power |
| * 12 | CX558080 | Button, Switch | キ ー ト ッ プ (E) | WH Power |
| 13 | EI320066 | Bind Head Tapping Screw | 2×6 ZMC2-BL | バ イ ン ド タ ッ ピ ン グ ネ ジ |
| * 14 | AX550300 | Spring | | コ イ ル ス プ リ ン グ |
| * 15 | JX550050 | LED 7Seg. Indicator | | 7 セ グ メ ン ト 表 示 器 |
| * 16 | KX550640 | Micro Switch | | M T ス イ ッ チ |
| 17 | IG136200 | IC | SI-3052V | IC 2A5V Regulator |
| * 18 | KX550100 | Power Switch | | パ ワ ー ス イ ッ チ |
| * 19 | JX550060 | Sensor, Remote Controller | | リ モ コ ン 受 光 器 |
| * 20 | CX558150 | Window | | フ ィ ル タ ー (A) |
| * 21 | CX558160 | LCD Panel | | 表 示 板 |
| * 22 | VE470300 | CPU Circuit Board | | CPU シ ー ト |
| * 23 | VF035500 | FDD | | FDD VB072400 |
| * 24 | VB352400 | LCD | | LCD |
| 25 | EI330066 | Bind Head Tapping Screw | 3×6 ZMC2-BL | バ イ ン ド タ ッ ピ ン グ ネ ジ |
| 26 | EH340120 | Truss Head Tapping Screw | 4×12 ZMC2-BL | ト ラ ス タ ッ ピ ン グ ネ ジ |
| 27 | ED330086 | Bind Head Screw | 3×8 ZMC2-BL | バ イ ン ド 小 ネ ジ |
| 28 | EH340120 | Truss Head Tapping Screw | 4×12 ZMC2-BL | ト ラ ス タ ッ ピ ン グ ネ ジ |
| 29 | EV203046 | Flat Washer | φ4 ZMC2-BL | 平 座 金 |

* : New Parts (新規部品)

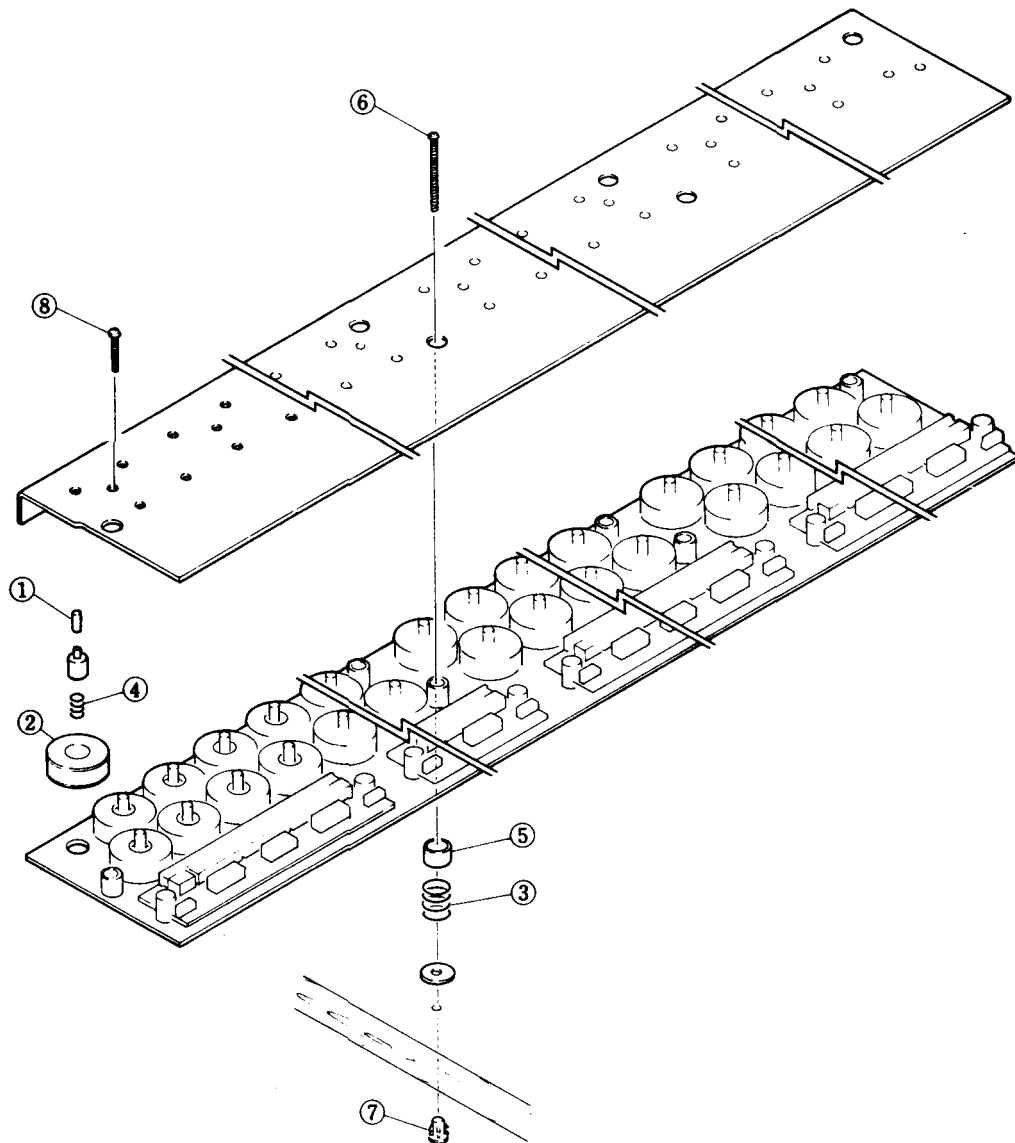
3. Key Stop Rail Assembly



| Ref. No. | Part No. | Description | | 部 品 名 | Remarks |
|----------|----------|---------------------------|-------------|-------------|---------|
| * 1 | NB513830 | Key Stop Rail Ass'y | | 鍵盤押え Ass'y | |
| 2 | EC050300 | Truss Head Screw | 5×30 ZMC2-Y | トラス小ネジ | |
| * 3 | AA504060 | Spring | | 鍵盤押え調整バネ | |
| 4 | EV200066 | Flat Washer | φ6 ZMC2-Y | 平座金 | |
| * 5 | AA504050 | Bolt | | 鍵盤押えスクリーボルト | |
| * 6 | AA504040 | Shaft | | 鍵盤押え固定シャフト | |
| * 7 | AA504130 | Angle | | 鍵盤押え支持台 | |
| 8 | EX600170 | Hexagonal Nut | M5 | フランジ六角ナット | |
| 9 | EH034020 | Truss Head Tapping Scerew | 4×12 ZMC2-Y | トラスタッピングネジ | |
| | | | | | |
| | | | | | |

* : New Parts (新規部品)

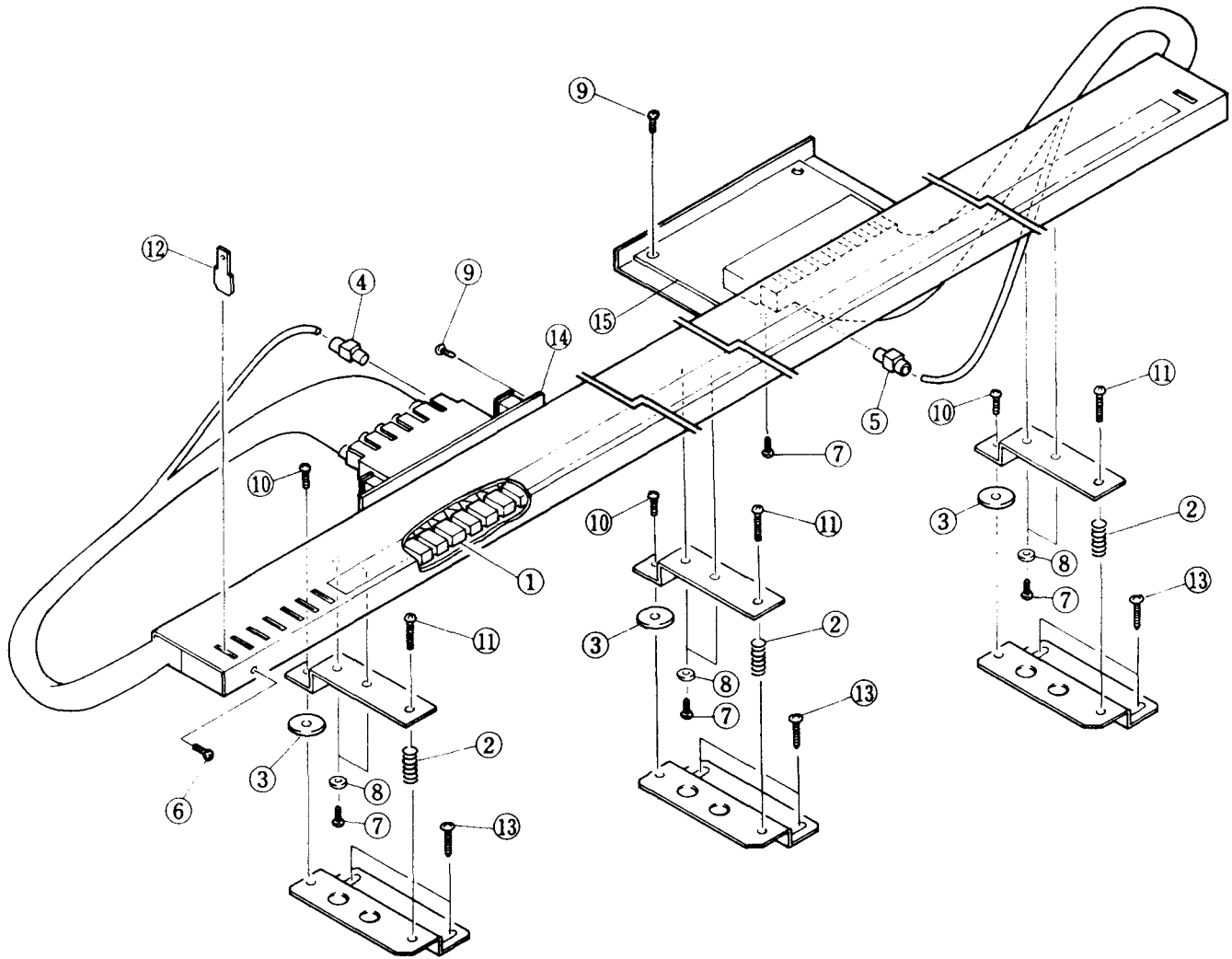
4. Key Drive Unit



| Ref. No. | Part No. | Description | | 部 品 名 | Remarks |
|----------|----------|------------------|---------------|-----------------|---------|
| ※ | VE479600 | Key Drive Unit | | キードライブユニット | |
| ※ | 1 | Damper(A) | BL | ゴムダンパー (A) | |
| ※ | 1 | Damper(B) | WH | ゴムダンパー (B) | |
| ※ | 2 | Plunger Ass'y(A) | | プランジャー Ass'y(A) | |
| ※ | 2 | Plunger Ass'y(B) | Front | プランジャー Ass'y(B) | |
| ※ | 3 | Spring | Rear | クッションスプリング(A) | |
| ※ | 4 | Spring | | コイルスプリング | |
| ※ | 5 | Bushing, Spring | | スプリング固定ブッシュ | |
| | 6 | Pan Head Screw | 4×55 ZMC2-Y | ナベ小ネジ | |
| | 7 | Nut | B4×9.5 ZMC2-Y | ツバ付鬼目ナット | |
| | 8 | Screw | M4 | 共通ヨーク固定ネジ | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |

※ : New Parts (新規部品)

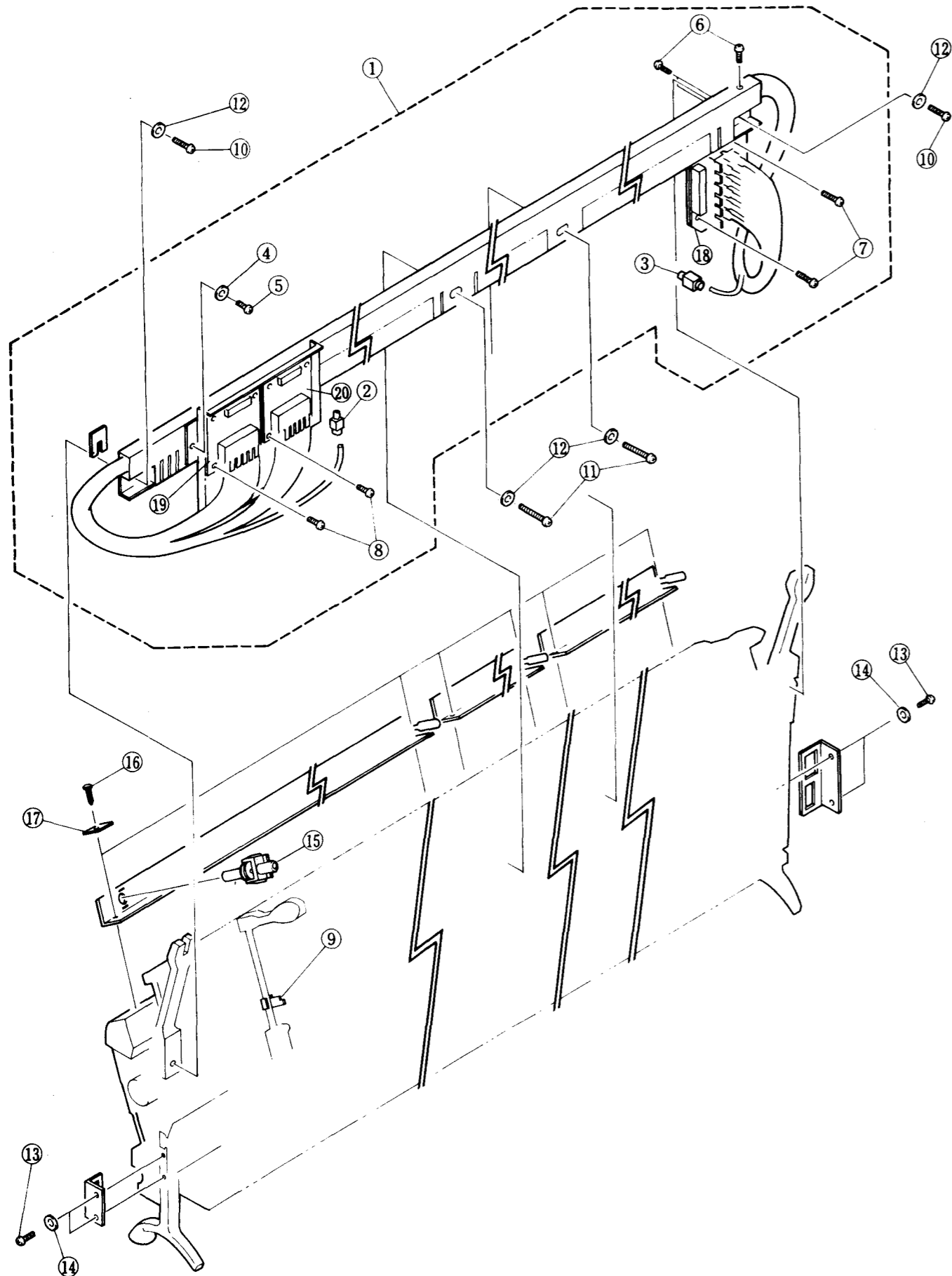
5.KeySensorUnit



| Ref. No. | Part No. | Description | 部 品 名 | Remarks |
|----------|----------|--------------------------|----------------------------|---------|
| * | VE479300 | Key Sensor Unit | キーセンサーユニット | |
| 1 | CB502790 | Sensor Head B | センサーヘッドB | |
| * | AA503970 | Spring A | ZMC2-BL 高さ調整バネA | |
| * | CB502800 | Spacer A | ゴムスペーサーA | |
| 4 | CB502650 | Plug | Emitter 発光プラグ | |
| 5 | CB502700 | Plug | Receiver 受光プラグ | |
| 6 | EA330046 | Pan Head Screw | 3×4 ZMC2-BL ナベ小ネジ | |
| 7 | ED330306 | Bind Head Screw | 3×3 ZMC2-BL バインド小ネジ | |
| 8 | EV203036 | Flat Washer | φ3 平座金 | |
| 9 | ED330056 | Bind Head Screw | 3×5 ZMC2-BL バインド小ネジ | |
| 10 | ED330086 | Bind Head Screw | 3×8 ZMC2-BL バインド小ネジ | |
| 11 | ED330166 | Bind Head Screw | 3×16 ZMC2-BL バインド小ネジ | |
| 11 | ED330186 | Bind Head Screw | 3×18 ZMC2-BL バインド小ネジ | |
| 12 | BB500690 | Key Shutter | ZMC2-BL キーシャッター | |
| 13 | EH340120 | Truss Head Tapping Screw | 4×12 ZMC2-BL トラスタッピングネジ | |
| * | VE471400 | LED Drive Board | キー発光シート | |
| * | VE471200 | M1 K Detect Board | M1 K受光シート | |

* : New Parts (新規部品)

6. Hammer Sensor Unit, Air Damper Unit

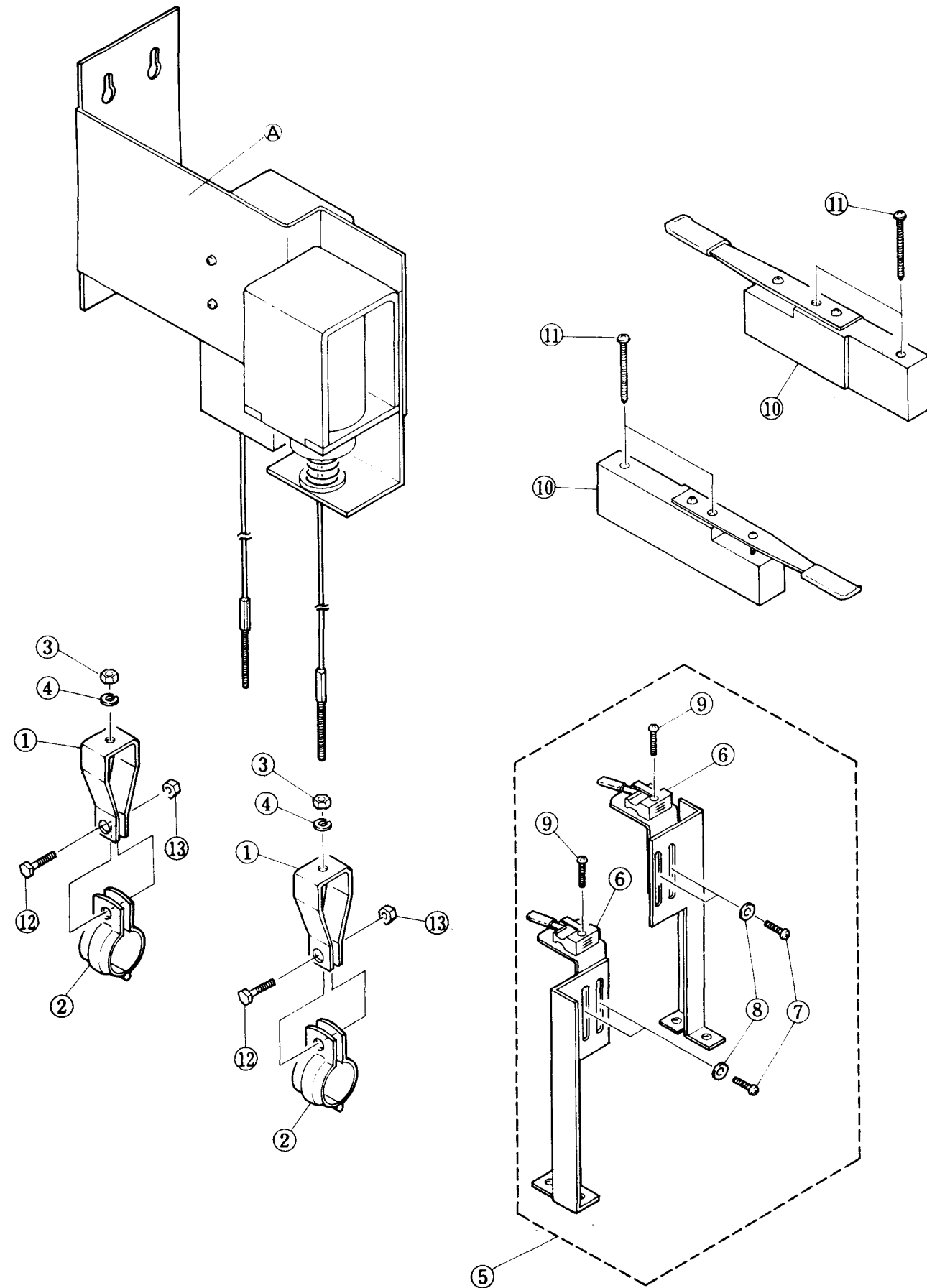


6. Hammer Sensor Unit, Air Damper Unit

| Ref. No. | Part No. | Description | ZH | 部 品 名 | Remarks |
|----------|----------|--------------------------|-------------|--------------|---------|
| * 1 | VE479200 | Hammer Sensor Unit | | ハンマーセンサーユニット | |
| | CB502790 | Sensor Head B | | センサーヘッド B | |
| 2 | CB502700 | Plug | Receiver | 受光プラグ | |
| 3 | CB502600 | Plug | Emitter | 発光プラグ | |
| 4 | EA030036 | Pan Head Screw | 3×3 ZMC2-Y | ナベ小ネジ | |
| 5 | EV200036 | Flat Washer | φ3 | 平座金 | |
| 6 | ED030046 | Bind Head Screw | 3×4 ZMC2-Y | バインド小ネジ | |
| 7 | EA030086 | Pan Head Screw | 3×8 ZMC2-Y | ナベ小ネジ | |
| 8 | ED030056 | Bind Head Screw | 3×5 ZMC2-Y | バインド小ネジ | |
| * 9 | BB500670 | Hammer Shutter A | | ハンマーシャッター A | |
| 10 | EA040106 | Pan Head Screw | 4×10 ZMC2-Y | ナベ小ネジ | |
| 11 | EA040206 | Pan Head Screw | 4×20 ZMC2-Y | ナベ小ネジ | |
| 12 | EV200046 | Flat Washer | φ4 | 平座金 | |
| 13 | EA030046 | Pan Head Screw | 3×4 ZMC2-Y | ナベ小ネジ | |
| 14 | EV200036 | Flat Washer | φ3 ZMC2-Y | 平座金 | |
| * 15 | NX509820 | Air Dumper | | エアダンパー | |
| 16 | EH040100 | Truss Head Tapping Screw | 4×10 ZMC2-Y | トラスタッピングネジ | |
| 17 | | Square Spring Washer | φ4 | バネ座金(角) | |
| * 18 | VE470900 | LED Drive Board | | ハンマー発光シート | |
| * 19 | VE471200 | M1 K Detect Board | | M1 K 受光シート | |
| * 20 | VE471300 | M2 Detect Board | | M2 受光シート | |

* : New Parts (新規部品)

7. Pedal Drive Unit, Pedal Sensor Unit, Pedal Cushion Unit

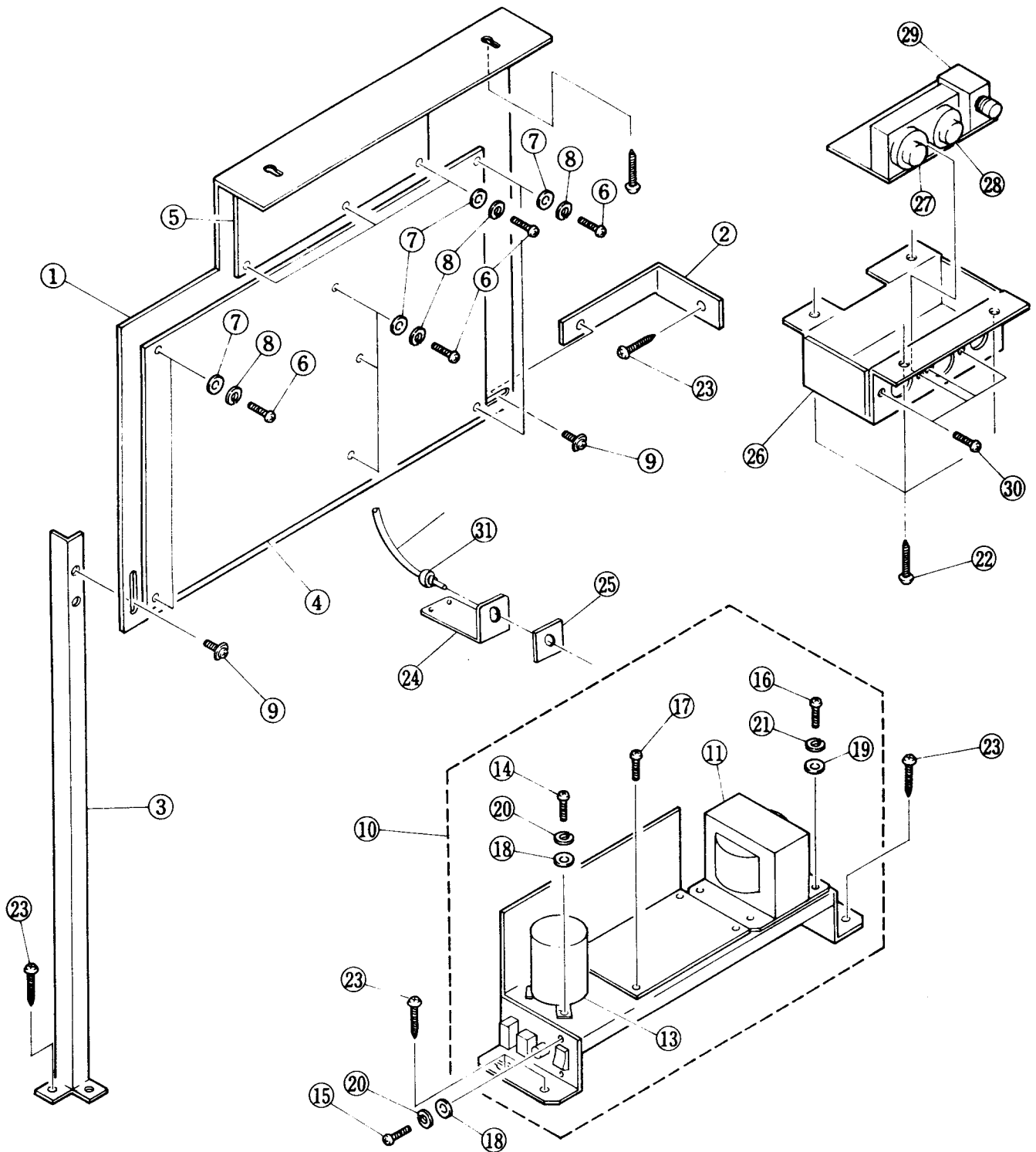


7. Pedal Drive Unit, Pedal Sensor Unit, Pedal Cushion Unit

| Ref. No. | Part No. | Description | 部 品 名 | Remarks |
|----------|----------|--------------------------|--------------|---------------|
| * A | VE479700 | Pedal Drive Unit | ペダルドライブユニット | |
| * 1 | AA504080 | Pedal Lever Hook | 天秤止め金具 | |
| * 2 | AA504590 | Pedal Lever Clamp | 天秤吊りバンド | |
| 3 | EV100046 | Hexagonal Nut | M4 ZMC2-Y | 六角ナット |
| 4 | EV300046 | Spring Washer | φ4 ZMC2-Y | バネ座金 |
| * 5 | NB513260 | Pedal Sensor Unit A | | ペダルセンサーユニットA |
| 6 | NB137040 | PK Switch | (1T) | PKスイッチ |
| 7 | EA030046 | Pan Head Screw | 3×4 ZMC2-Y | ナベ小ネジ |
| 8 | EV200036 | Flat Washer | φ4 ZMC2-Y | 平座金 |
| 9 | ED030126 | Bind Head Screw | 3×12 ZMC2-Y | バインド小ネジ |
| * 10 | NB513620 | Pedal Cushion Ass'y | | ペダルクッションAss'y |
| 11 | EK940406 | Truss Head Tapping Screw | 4×40 ZMC2-Y | トラスタッピングネジ |
| * 12 | EX801360 | Hexagonal Bolt | M5×15 ZMC2-Y | 六角ボルト |
| * 13 | EX600170 | Hexagonal Nut | M5 | フランジ六角ナット |

* : New Parts (新規部品)

8. I/O Unit, Power Supply Unit, MIDI Unit



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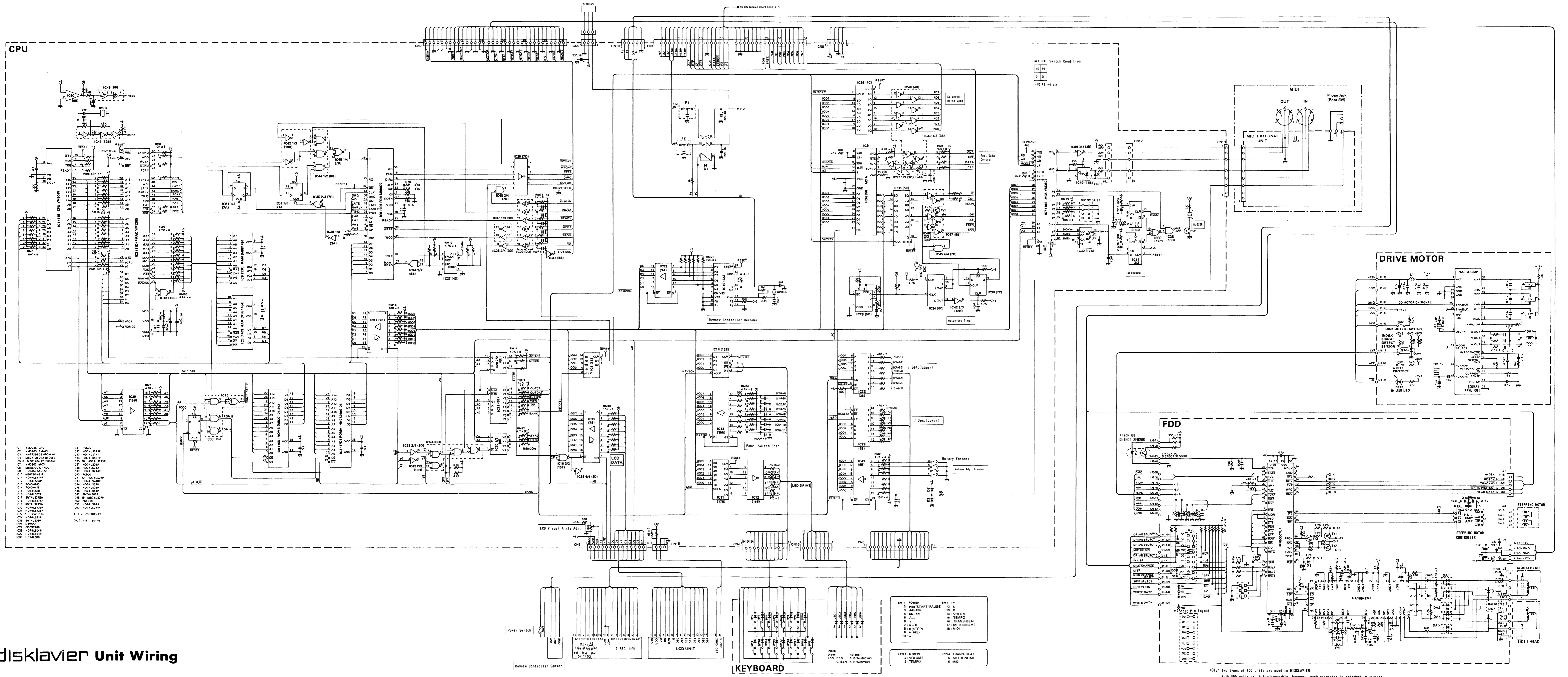
YAMAHA PARTS

MX100A SERVICE MANUAL

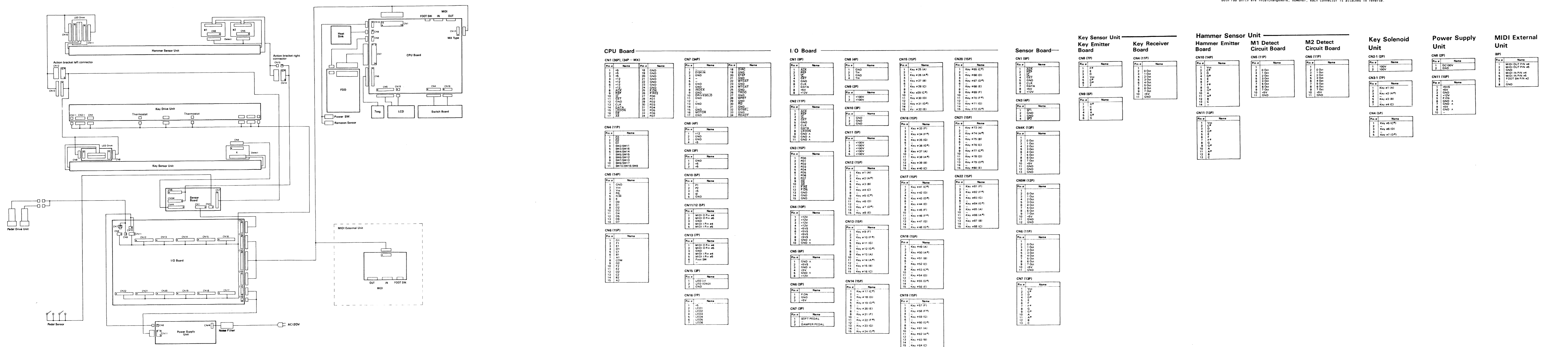


SM8549

disklavier Overall Circuit Diagram (CPU Section)



disklavier Unit Wiring



disklavier Overall Circuit Diagram (I/O, Sensor Section)

